

# MOS FIELD EFFECT TRANSISTOR **2SK1485**

## N-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR SWITCHING

#### **DESCRIPTION**

The 2SK1485, N-channel vertical type MOS FET is a switching device which can be driven directly by the output of ICs having a 5 V power source. As the MOS FET has low on-state resistance and excellent switching characteristics, it is suitable for driving actuators such as motors, relays, and solenoids.

#### **FEATURES**

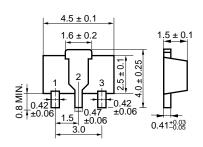
- Directly driven by ICs having a 5 V power source.
- · Low on-state resistance

RDS(on)1 = 1.2  $\Omega$  MAX. (VGS = 4.0 V, ID = 0.5 A)

 $R_{DS(on)2} = 0.8 \Omega MAX$ . (Vgs = 10 V, ID = 0.5 A)

· Complementary to 2SJ199.

## **PACKAGE DRAWING (Unit: mm)**



1.Source 2.Drain 3.Gate

MARK : NC

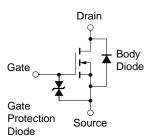
#### ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

	Drain to Source Voltage (Ves = 0 V)	VDSS	100	V
	Gate to Source Voltage (Vps = 0 V)	Vgss	±20	V
*	Drain Current (DC) (Tc = 25°C)	ID(DC)	±1.0	Α
	Drain Current (pulse) Note1	D(pulse)	±2.0	Α
	Total Power Dissipation ( $T_A = 25^{\circ}C$ ) Note2	Рт	2.0	W
	Channel Temperature	Tch	150	°C
	Storage Temperature	Tstg	-55 to +150	°C

**Notes1.** PW  $\leq$  10 ms, Duty Cycle  $\leq$  50%

2. Mounted on ceramic board of 16  $\text{cm}^2\,\times\,0.7~\text{mm}$ 

#### **EQUIVALENT CIRCUIT**



**Remark** The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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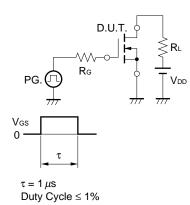
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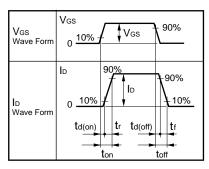


**ELECTRICAL CHARACTERISTICS (TA = 25°C)** 

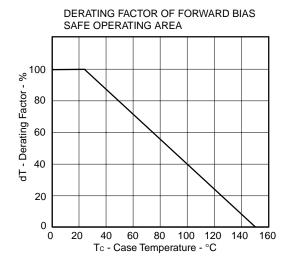
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	Ipss	V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V			10	μΑ
Gate Leakage Current	lgss	Vgs = ±20 V, Vps = 0 V			±10	μΑ
Gate Cut-off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	0.8	1.2	2.0	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 0.5 A	0.4			S
Drain to Source On-state Resistance	RDS(on)1	Vgs = 4.0 V, ID = 0.5 A		0.6	1.2	Ω
	R <sub>DS(on)2</sub>	Vgs = 10 V, ID = 0.5 A		0.5	0.8	Ω
Input Capacitance	Ciss	Vps = 10 V		230		pF
Output Capacitance	Coss	Vgs = 0 V		80		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		12		pF
Turn-on Delay Time	td(on)	V <sub>DD</sub> = 25 V, I <sub>D</sub> = 0.5 A		14		ns
Rise Time	tr	Vgs = 10 V		14		ns
Turn-off Delay Time	td(off)	R <sub>G</sub> = 10 Ω		370		ns
Fall Time	tf			65		ns

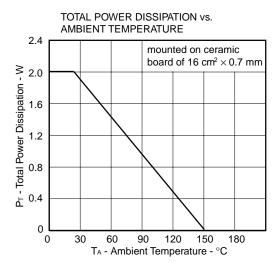
### **SWITCHING TIME**

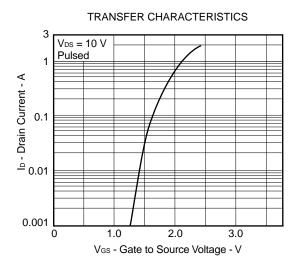


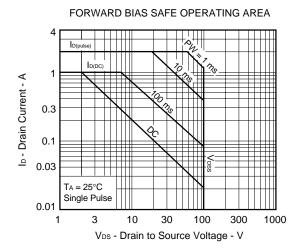


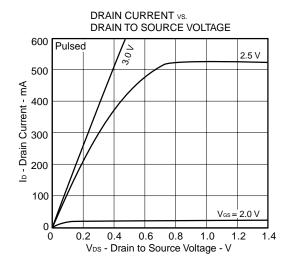
## TYPICAL CHARACTERISTICS (TA = 25°C)

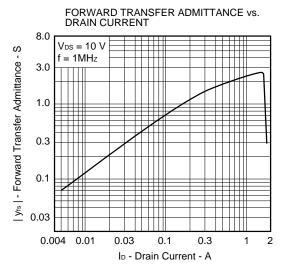


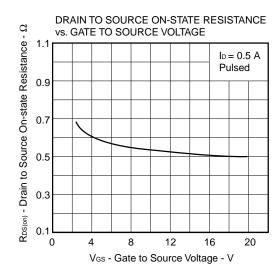


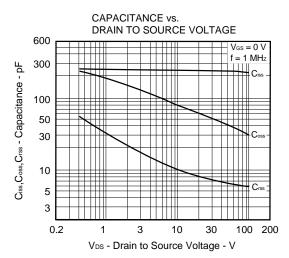




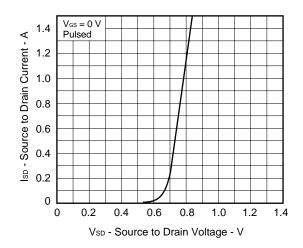


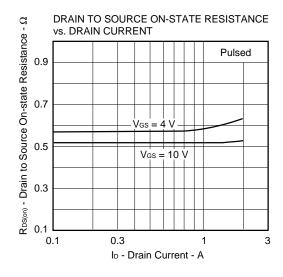


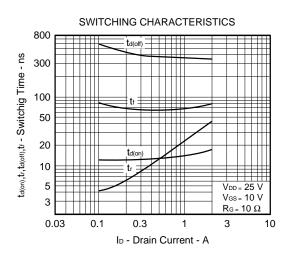




#### SOURCE TO DRAIN DIODE FORWARD VOLTAGE







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