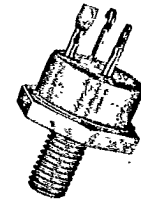


10 AMP/NPN-PNP Pirgo Silicon Planar Power Transistors

10 AMP
100 VOLT
40 WATT
60 MHz
TO-61

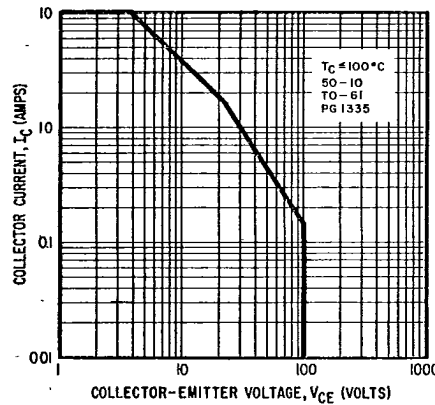


TO-61

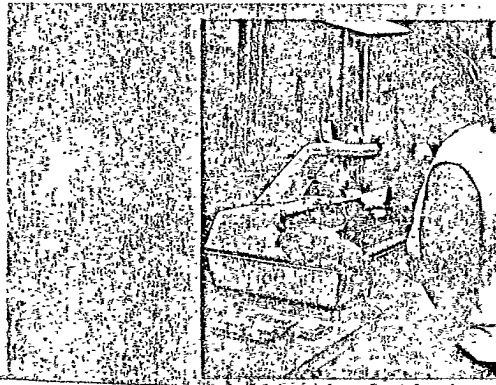
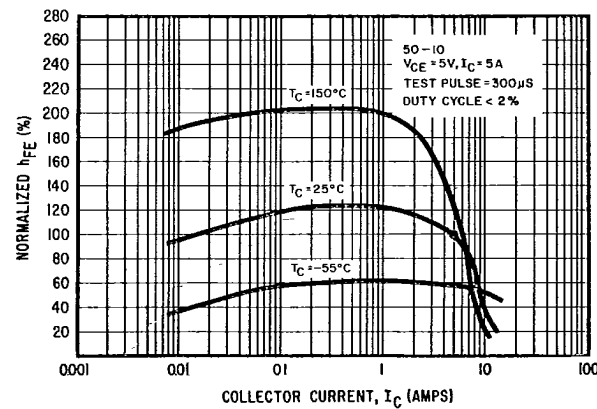
- Linear h_{FE} from 50 mA to 10 amps
- High frequency $f_t = 60$ MHz (typical)
- Low saturation voltage at maximum collector current
- High voltage, $BV_{CEO(sus)}$ to 100 volts

NPN Type	Package	BV_{CBO}	$BV_{CEO(sus)}$	BV_{EBO}	h_{FE}		h_{FE} @ $I_C A$	h_{FE}		$V_{CE(sat)}$		$C_{ob} Max$ pf	PDW @ 100°C	PNP Complement
					Min	Max		Min	@ $I_C A$	Max	@ $I_C A$			
2N2811	TO-61	80	50	8	20	60	5	15	10	1.5	10	350	40	PG2347
2N2812	TO-61	80	50	8	40	120	5	15	10	1.5	10	350	40	PG2348
2N2813	TO-61	120	70	8	20	60	5	15	10	1.5	10	350	40	PG2349
2N2814	TO-61	120	70	8	40	120	5	15	10	1.5	10	350	40	PG2350
PG1335	TO-61	120	100	8	20	60	5	10	10	1.5	10	350	40	PG2335
PG1336	TO-61	120	100	8	40	120	5	15	10	1.5	10	350	40	PG2336
PG1337	TO-61	120	100	8	100	300	5	20	10	1.5	10	350	40	PG2337
PG1338	TO-61	70	50	6	20	60	5			.6	5	350	40	PG2338
PG1339	TO-61	80	70	6	20	60	5			.6	5	350	40	PG2339
PG1340	TO-61	120	100	6	20	60	5			.6	5	350	40	PG2340
PG1341	TO-61	70	50	6	40	120	5			.6	5	350	40	PG2341
PG1342	TO-61	80	70	6	40	120	5			.6	5	350	40	PG2342
PG1343	TO-61	120	100	6	40	120	5			.6	5	350	40	PG2343
PG1344	TO-61	70	50	6	100	300	5			.6	5	350	40	PG2344
PG1345	TO-61	80	70	6	100	300	5			.6	5	350	40	PG2345
PG1346	TO-61	120	100	6	100	300	5			.6	5	350	40	PG2346

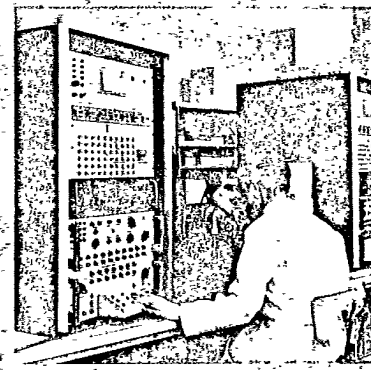
MAXIMUM SAFE OPERATING AREA



NORMALIZED h_{FE} Vs I_C



Slice scribing and breaking.



Inspecting power transistor dice 100%.

PG-0110-11x