

SERIES DL6500 DIGITAL DELAY MODULES

- 16 Pin Configuration • TTL and DTL Compatible • 10 Equally Spaced Taps

Specifications

- Supply Voltage : 4.75 to 5.25VDC
- Logic 1 Input Current : 50 μ A Max
- Logic 0 Input Current : -4mA Max
- Logic 1 Volt Out : 2.5v Min
- Logic 0 Volt Out : 0.5v Max
- Logic 1 Fan-Out : 20/Tap Max
- Logic 0 Fan-Out : 10/Tap Max
- Power Dissipation : 240mW Total
- Operating Temp Range : 0°C to 70°C
- Temp Coefficient : 100PPM/°C

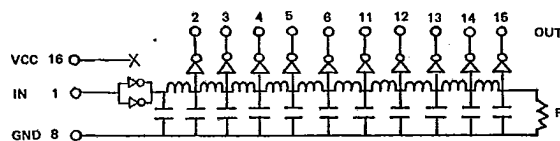
**Input Test Conditions**

- Input Pulse Voltage : 3.2v
- Input Rise Time : ≤ 3 NS
- Input Current : 120mA Typ.
- Pulse Width : Min 40% of Total Delay

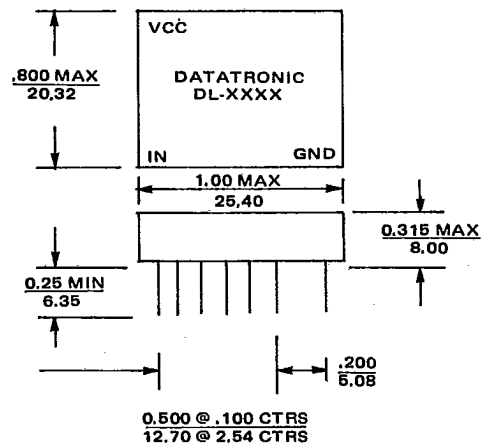
Electrical Specifications at 25°C (measured with no loads on Taps)

Part Number	Total Delay NSEC $\pm 5\%$	Tap to Tap Delay NSEC (1)	Rise Time NSEC Max (2)
DL6501	100	10	4
DL6502	150	15	4
DL6503	200	20	4
DL6504	250	25	4
DL6507	500	50	5
DL6508	1000	100	5

Note: (1) Measured at 1.5v level leading edge, Tol. $\pm 5\%$ or ± 2 NS, whichever is greater.
 (2) Measured from 0.75v to 2.4v.



SCHEMATIC



INCHES .XXX $\pm .010$
 MILLIMETERS .XX $\pm .25$