



PIC18F6410/8410

64/80-Pin High-Performance, Flash Microcontrollers with 10-bit A/D and nanoWatt Technology Product Brief

Low-Power Features:

- Power Managed modes:
 - Run: CPU on, peripherals on
 - Idle: CPU off, peripherals on
 - Sleep: CPU off, peripherals off
- Power Consumption modes:
 - PRI_RUN: 150 μ A @ 1 MHz, 2V
 - PRI_IDLE: 35 μ A @ 1 MHz, 2V
 - SEC_RUN: 9.6 μ A @ 32 kHz, 2V
 - SEC_IDLE: 5.8 μ A @ 32 kHz, 2V
 - RC_RUN: 145 μ A @ 1 MHz, 2V
 - RC_IDLE: 85 μ A @ 1 MHz, 2V
 - Sleep: 0.1 μ A
- Timer1 Oscillator: 1.8 μ A typical @ 32 kHz, 2V
- Watchdog Timer: 2.2 μ A @ 2V
- Two-Speed Oscillator Start-up

Flexible Oscillator Structure:

- Four Crystal modes:
 - LP: up to 200 kHz
 - XT: up to 4 MHz
 - HS: up to 40 MHz
 - HSPLL: 4-10 MHz (16-40 MHz internal)
- Two External Clock modes, up to 40 MHz
- Two External RC modes, up to 4 MHz
- Internal Oscillator block:
 - 8 user selectable frequencies: 31.25 kHz, 125 kHz, 250 kHz, 500 kHz, 1 MHz, 2 MHz, 4 MHz or 8 MHz
 - 4x PLL option generates 16 MHz or 32 MHz
 - Two modes select one or two I/O pins
 - OSCTUNE – Allows user to shift frequency
- Secondary Oscillator using Timer1 @ 32 kHz
- Fail-Safe Clock Monitor:
 - Allows for safe shutdown of device if primary or secondary oscillator/clock fails

External Memory Interface (PIC18F8410 Devices only):

- Address capability of up to 2 Mbytes
- 16-bit/8-bit interface

Peripheral Highlights:

- High current sink/source 25 mA/25 mA
- Up to four external interrupts
- Four 8-bit/16-bit Timer/Counter modules
- Three Capture/Compare/PWM (CCP) modules:
 - Capture is 16-bit, max. resolution 6.25 ns (T_{CY})
 - Compare is 16-bit, max. resolution 100 ns (T_{CY})
 - PWM output: PWM resolution is up to 10-bit
- Master Synchronous Serial Port (MSSP) module with two modes of operation:
 - 3-wire SPI™ (supports all 4 SPI modes)
 - I²C™ Master and Slave mode
- One Addressable USART module
- One Enhanced Addressable USART module:
 - Supports LIN 1.2
 - Auto-wake-up on Start bit
 - Auto-baud detect
- 10-bit, up to 12-channel A/D Converter:
 - Auto-acquisition
 - Conversion available during Sleep
- Two analog comparators
- Programmable reference voltage for comparators

Special Microcontroller Features:

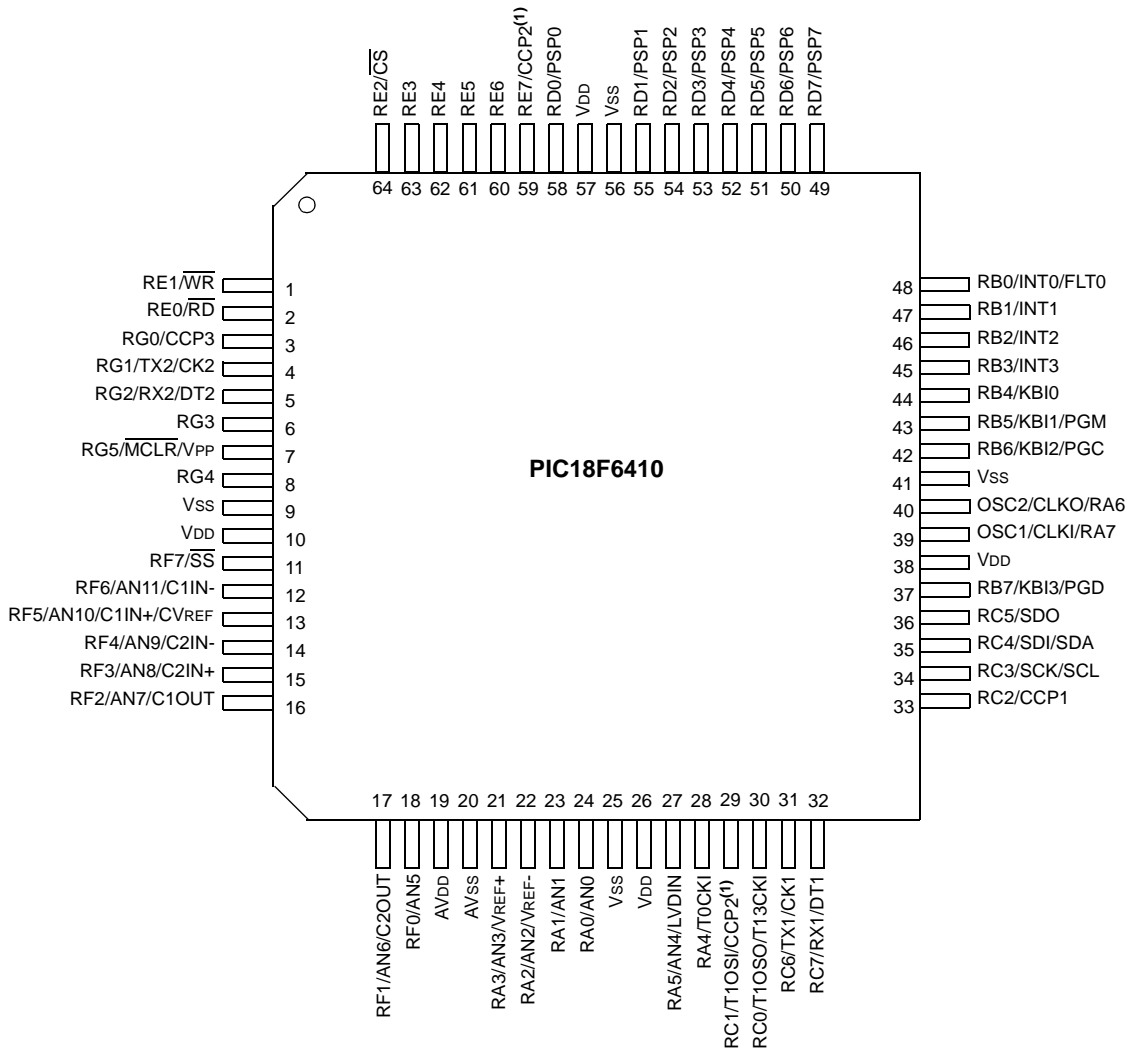
- 1000 erase/write cycle Flash program memory typical
- Flash retention 100 years typical
- Priority levels for interrupts
- 8 x 8 Single Cycle Hardware Multiplier
- Power-on Reset (POR), Power-up Timer (PWRT) and Oscillator Start-up Timer (OST)
- Programmable Brown-out Reset (BOR)
- Programmable 16-level Low-Voltage Detection (LVD) module
- Extended Watchdog Timer (WDT):
 - Programmable period from 4 ms to 131s
- In-Circuit Serial Programming™ (ICSP™) via two pins
- In-Circuit Debug via two pins
- Wide operating voltage range: 2.0V to 5.5V

Device	Program Memory (On-board/External)		Data Memory (bytes)	I/O	10-bit A/D (channels)	Comparators	CCP	MSSP (SPI/ Master I ² C)	EUSART/ AUSART	Timers 8-bit/16-bit
	Bytes	# Single Word Instructions								
PIC18F6410	16K/0	8192/0	768	54	12	2	3	Y	1/1	1/3
PIC18F8410	16K/2M	8192/1M	768	70	12	2	3	Y	1/1	1/3

PIC18F6410/8410

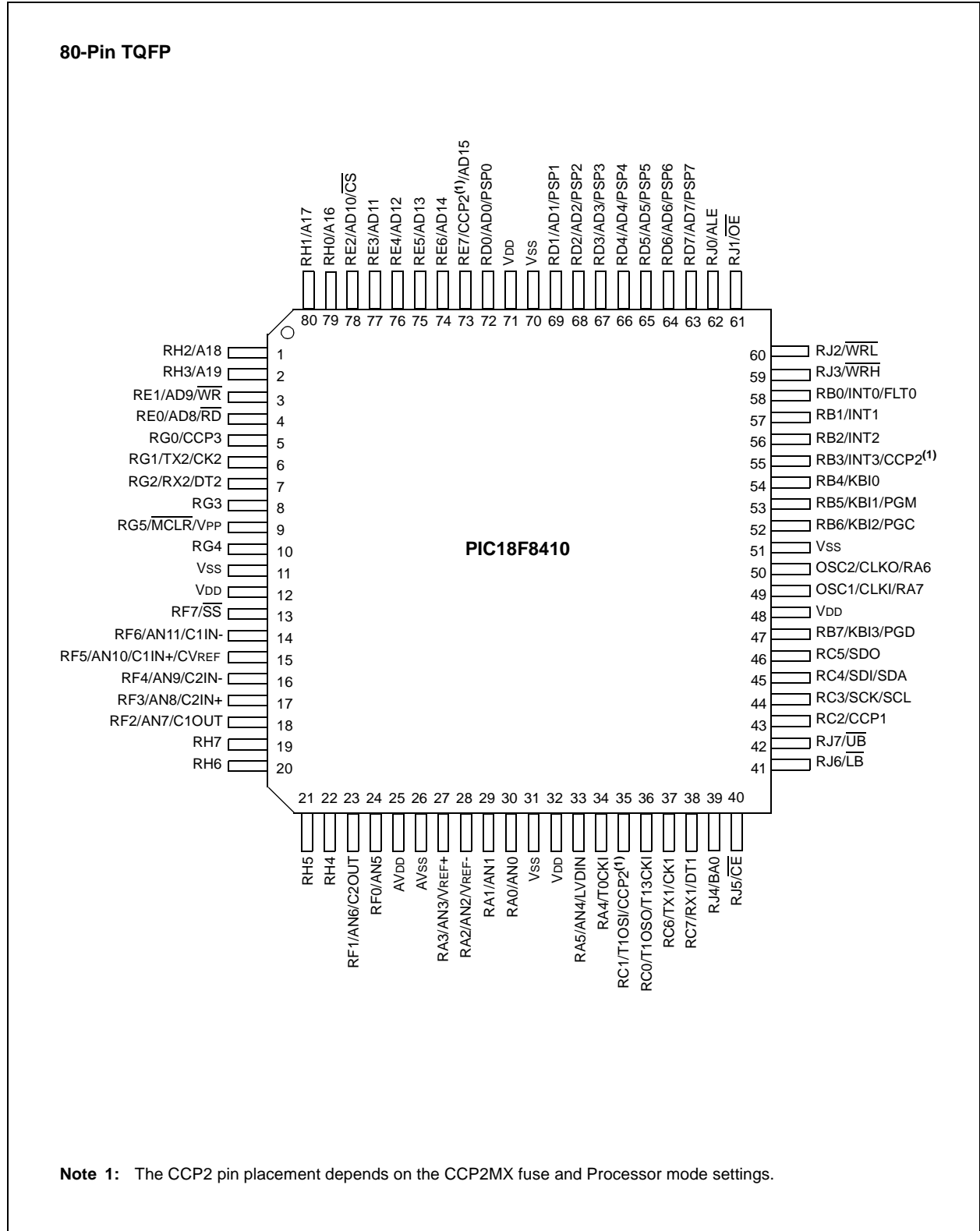
Pin Diagrams

64-Pin TQFP



Note 1: The CCP2 pin placement depends on the CCP2MX fuse setting.

Pin Diagrams (Cont.'d)



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NOTES:

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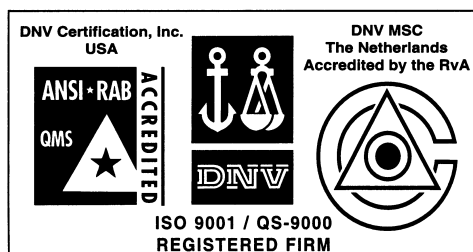
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WORLDWIDE SALES AND SERVICE

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Corporate Office

2355 West Chandler Blvd.
Chandler, AZ 85224-6199
Tel: 480-792-7200
Fax: 480-792-7277
Technical Support: 480-792-7627
Web Address: <http://www.microchip.com>

Atlanta

3780 Mansell Road, Suite 130
Alpharetta, GA 30022
Tel: 770-640-0034
Fax: 770-640-0307

Boston

2 Lan Drive, Suite 120
Westford, MA 01886
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Fax: 978-692-3821

Chicago

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Detroit

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32255 Northwestern Highway, Suite 190
Farmington Hills, MI 48334
Tel: 248-538-2250
Fax: 248-538-2260

Kokomo

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Kokomo, IN 46902
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Fax: 765-864-8387

Los Angeles

18201 Von Karman, Suite 1090
Irvine, CA 92612
Tel: 949-263-1888
Fax: 949-263-1338

Phoenix

2355 West Chandler Blvd.
Chandler, AZ 85224-6199
Tel: 480-792-7966
Fax: 480-792-4338

San Jose

2107 North First Street, Suite 590
San Jose, CA 95131
Tel: 408-436-7950
Fax: 408-436-7955

Toronto

6285 Northam Drive, Suite 108
Mississauga, Ontario L4V 1X5, Canada
Tel: 905-673-0699
Fax: 905-673-6509

ASIA/PACIFIC

Australia

Suite 22, 41 Rawson Street
Epping 2121, NSW
Australia
Tel: 61-2-9868-6733
Fax: 61-2-9868-6755

China - Beijing

Unit 915
Bei Hai Wan Tai Bldg.
No. 6 Chaoyangmen Beidajie
Beijing, 100027, No. China
Tel: 86-10-85282100
Fax: 86-10-85282104

China - Chengdu

Rm. 2401-2402, 24th Floor,
Ming Xing Financial Tower
No. 88 TIDU Street
Chengdu 610016, China
Tel: 86-28-86766200
Fax: 86-28-86766599

China - Fuzhou

Unit 28F, World Trade Plaza
No. 71 Wusi Road
Fuzhou 350001, China
Tel: 86-591-7503506
Fax: 86-591-7503521

China - Hong Kong SAR

Unit 901-6, Tower 2, Metroplaza
223 Hing Fong Road
Kwai Fong, N.T., Hong Kong
Tel: 852-2401-1200
Fax: 852-2401-3431

China - Shanghai

Room 701, Bldg. B
Far East International Plaza
No. 317 Xian Xia Road
Shanghai, 200051
Tel: 86-21-6275-5700
Fax: 86-21-6275-5060

China - Shenzhen

Rm. 1812, 18/F, Building A, United Plaza
No. 5022 Binhe Road, Futian District
Shenzhen 518033, China
Tel: 86-755-82901380
Fax: 86-755-8295-1393

China - Shunde

Room 401, Hongjian Building
No. 2 Fengxiangnan Road, Ronggui Town
Shunde City, Guangdong 528303, China
Tel: 86-765-8395507 Fax: 86-765-8395571

China - Qingdao

Rm. B505A, Fullhope Plaza,
No. 12 Hong Kong Central Rd.
Qingdao 266071, China
Tel: 86-532-5027355 Fax: 86-532-5027205

India

Divyasree Chambers
1 Floor, Wing A (A3/A4)
No. 11, O'Shaughnessy Road
Bangalore, 560 025, India
Tel: 91-80-2290061 Fax: 91-80-2290062

Japan

Benex S-1 6F
3-18-20, Shinyokohama
Kohoku-Ku, Yokohama-shi
Kanagawa, 222-0033, Japan
Tel: 81-45-471-6166 Fax: 81-45-471-6122

Korea

168-1, Youngbo Bldg. 3 Floor
Samsung-Dong, Kangnam-Ku
Seoul, Korea 135-882
Tel: 82-2-554-7200 Fax: 82-2-558-5932 or
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Singapore

200 Middle Road
#07-02 Prime Centre
Singapore, 188980
Tel: 65-6334-8870 Fax: 65-6334-8850

Taiwan

Kaohsiung Branch
30F - 1 No. 8
Min Chuan 2nd Road
Kaohsiung 806, Taiwan
Tel: 886-7-536-4818
Fax: 886-7-536-4803

Taiwan

Taiwan Branch
11F-3, No. 207
Tung Hua North Road
Taipei, 105, Taiwan
Tel: 886-2-2717-7175 Fax: 886-2-2545-0139

EUROPE

Austria

Durisolstrasse 2
A-4600 Wels
Austria
Tel: 43-7242-2244-399
Fax: 43-7242-2244-393

Denmark

Regus Business Centre
Lautrup høj 1-3
Ballerup DK-2750 Denmark
Tel: 45-4420-9895 Fax: 45-4420-9910

France

Parc d'Activite du Moulin de Massy
43 Rue du Saule Trapu
Batiment A - 1er Etage
91300 Massy, France
Tel: 33-1-69-53-63-20
Fax: 33-1-69-30-90-79

Germany

Steinheilstrasse 10
D-85737 Ismaning, Germany
Tel: 49-89-627-144-0
Fax: 49-89-627-144-44

Italy

Via Quasimodo, 12
20025 Legnano (MI)
Milan, Italy
Tel: 39-0331-742611
Fax: 39-0331-466781

Netherlands

P. A. De Biesbosch 14
NL-5152 SC Drunen, Netherlands
Tel: 31-416-690399
Fax: 31-416-690340

United Kingdom

505 Eskdale Road
Winnersh Triangle
Wokingham
Berkshire, England RG41 5TU
Tel: 44-118-921-5869
Fax: 44-118-921-5820