# NTF3055-100

Preferred Device

## Power MOSFET 3.0 Amps, 60 Volts N-Channel SOT-223

Designed for low voltage, high speed switching applications in power supplies, converters and power motor controls and bridge circuits.

#### Applications

- Power Supplies
- Converters
- Power Motor Controls
- Bridge Circuits

#### MAXIMUM RATINGS (T<sub>C</sub> = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>	60	Vdc
Drain–to–Gate Voltage ( $R_{GS}$ = 10 M $\Omega$ )	VDGR	60	Vdc
Gate–to–Source Voltage – Continuous – Non–repetitive (t <sub>p</sub> ≤ 10 ms)	V <sub>GS</sub>	± 20 ± 30	Vdc Vpk
Drain Current – Continuous @ T <sub>A</sub> = 25°C – Continuous @ T <sub>A</sub> = 100°C – Single Pulse (t <sub>p</sub> $\leq$ 10 µs)	ID ID IDM	3.0 1.4 9.0	Adc Apk
Total Power Dissipation @ $T_A = 25^{\circ}C$ (Note 1.) Total Power Dissipation @ $T_A = 25^{\circ}C$ (Note 2.) Derate above $25^{\circ}C$	PD	2.1 1.3 0.014	W W W/∘C
Operating and Storage Temperature Range	TJ, Tstg	–55 to 175	°C
Single Pulse Drain–to–Source Avalanche Energy – Starting $T_J = 25^{\circ}C$ ( $V_{DD} = 25 \text{ Vdc}, V_{GS} = 10 \text{ Vdc},$ $I_L(pk) = 7.0 \text{ Apk}, L = 3.0 \text{ mH}, V_{DS} = 60 \text{ Vdc})$	E <sub>AS</sub>	74	mJ
Thermal Resistance – Junction to Ambient (Note 1.) – Junction to Ambient (Note 2.)	R <sub>θJA</sub> R <sub>θJA</sub>	72.3 114	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	ΤL	260	°C

 When surface mounted to an FR4 board using 1" pad size, 1 oz. (Cu. Area 1.127 in<sup>2</sup>).

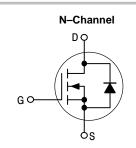
 When surface mounted to an FR4 board using minimum recommended pad size, 2–2.4 oz. (Cu. Area 0.272 in<sup>2</sup>).

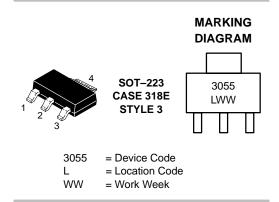


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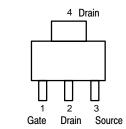
http://onsemi.com

3.0 AMPERES 60 VOLTS RDS(on) = 100 mΩ





**PIN ASSIGNMENT** 



### ORDERING INFORMATION

Device	Package	Shipping	
NTF3055-100T1	SOT-223	1000 Tape & Reel	
NTF3055-100T3	SOT-223	4000 Tape & Reel	
NTF3055-100T3LF	SOT-223	4000 Tape & Reel	

www.DataSheet4U.com

### NTF3055-100

### **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

Charac	Symbol	Min	Тур	Мах	Unit	
OFF CHARACTERISTICS						
Drain–to–Source Breakdown Voltage (Note 3.) ( $V_{GS} = 0 Vdc, I_D = 250 \mu Adc$ ) Temperature Coefficient (Positive)		V(BR)DSS	60 -	68 66		Vdc mV/°C
Zero Gate Voltage Drain Current ( $V_{DS} = 60 Vdc, V_{GS} = 0 Vdc$ ) ( $V_{DS} = 60 Vdc, V_{GS} = 0 Vdc, T_J =$	IDSS			1.0 10	μAdc	
Gate–Body Leakage Current (VG	IGSS	-	-	± 100	nAdc	
ON CHARACTERISTICS (Note 3.)						
Gate Threshold Voltage (Note 3.) $(V_{DS} = V_{GS}, I_D = 250 \mu Adc)$ Threshold Temperature Coefficient (Negative)		VGS(th)	2.0 -	3.0 6.6	4.0	Vdc mV/°C
Static Drain–to–Source On–Resistan (V <sub>GS</sub> = 10 Vdc, I <sub>D</sub> = 1.5 Adc)	R <sub>DS(on)</sub>	-	88	100	mΩ	
Static Drain–to–Source On–Resistance (Note 3.) (V <sub>GS</sub> = 10 Vdc, I <sub>D</sub> = 3.0 Adc) (V <sub>GS</sub> = 10 Vdc, I <sub>D</sub> = 1.5 Adc, T <sub>J</sub> = 150°C)		VDS(on)	_	0.27 0.24	0.36 -	Vdc
Forward Transconductance (Note 3.)	9fs	-	3.2	-	Mhos	
DYNAMIC CHARACTERISTICS						
Input Capacitance		C <sub>iss</sub>	-	324	455	pF
Output Capacitance	(V <sub>DS</sub> = 25 Vdc, V <sub>GS</sub> = 0 V, f = 1.0 MHz)	C <sub>OSS</sub>	-	35	50	-
Transfer Capacitance		C <sub>rss</sub>	-	110	155	
SWITCHING CHARACTERISTICS	<b>S</b> (Note 4.)					
Turn–On Delay Time		<sup>t</sup> d(on)	-	9.4	20	ns
Rise Time	$(V_{DD} = 30 \text{ Vdc}, I_D = 3.0 \text{ Adc},$	tr	-	14	30	
Turn–Off Delay Time	V <sub>GS</sub> = 10 Vdc, R <sub>G</sub> = 9.1 Ω) (Note 3.)	<sup>t</sup> d(off)	-	21	45	
Fall Time		tf	-	13	30	
Gate Charge	(V <sub>DS</sub> = 48 Vdc, I <sub>D</sub> = 3.0 Adc, V <sub>GS</sub> = 10 Vdc) (Note 3.)	QT	-	10.6	22	nC
		Q <sub>1</sub>	-	1.9	-	_
		Q <sub>2</sub>	-	4.2	-	
SOURCE-DRAIN DIODE CHARA	CTERISTICS					
Forward On–Voltage	$(I_S = 3.0 \text{ Adc}, V_{GS} = 0 \text{ Vdc})$ $(I_S = 3.0 \text{ Adc}, V_{GS} = 0 \text{ Vdc},$ $T_J = 150^{\circ}\text{C})$ (Note 3.)	V <sub>SD</sub>	-	0.89 0.74	1.0	Vdc
Reverse Recovery Time		t <sub>rr</sub>	_	30	_	ns
	(I <sub>S</sub> = 3.0 Adc, V <sub>GS</sub> = 0 Vdc,	ta	-	22	-	1
	$dI_{S}/dt = 100 \text{ A}/\mu \text{s}$ (Note 3.)	tb	-	8.6	-	1
	1			ł		+

3. Pulse Test: Pulse Width  $\leq$  300 µs, Duty Cycle  $\leq$  2.0%.

Reverse Recovery Stored Charge

4. Switching characteristics are independent of operating junction temperatures.

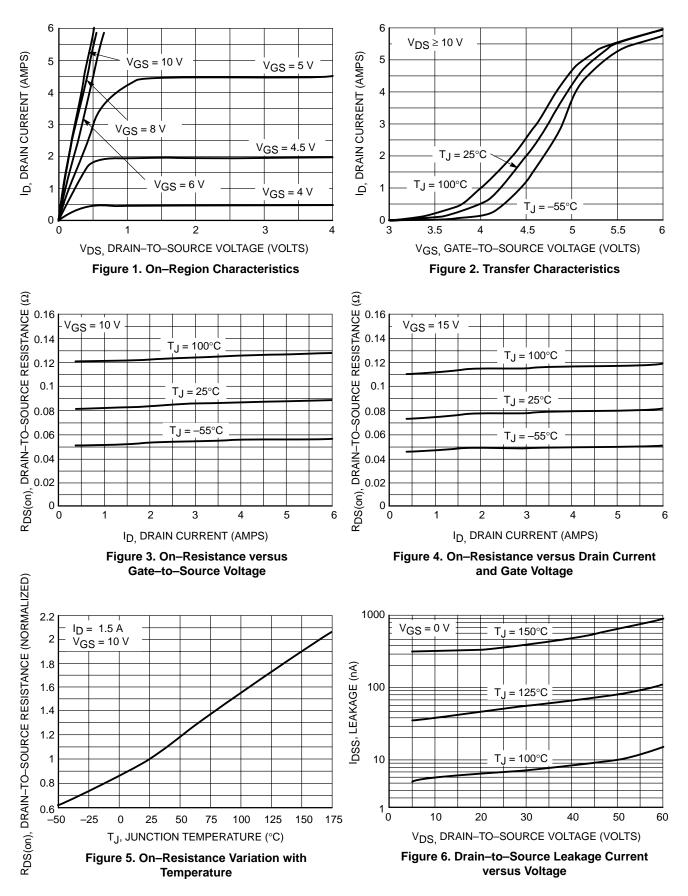
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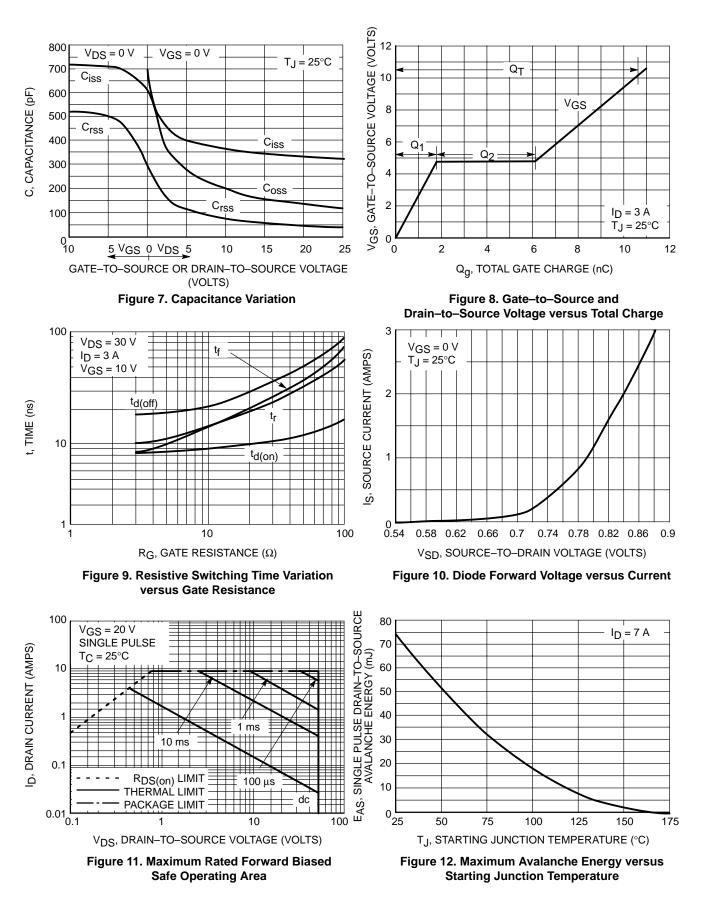
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μC





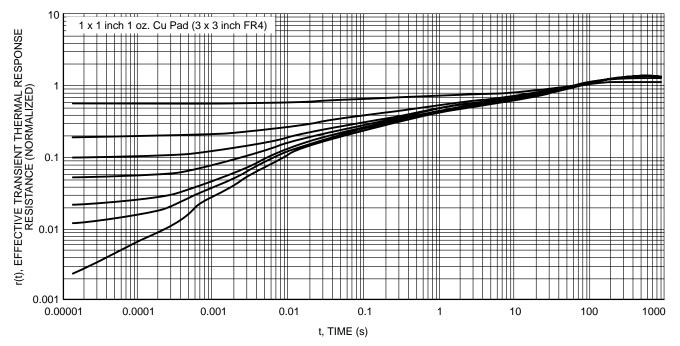
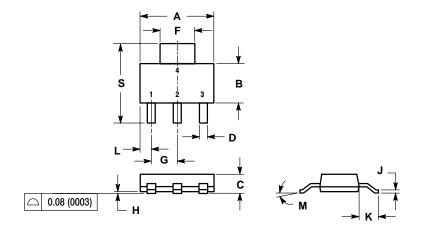


Figure 13. Thermal Response

### PACKAGE DIMENSIONS

**SOT-223 (TO-261)** CASE 318E-04 ISSUE K



NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.249	0.263	6.30	6.70	
В	0.130	0.145	3.30	3.70	
C	0.060	0.068	1.50	1.75	
D	0.024	0.035	0.60	0.89	
F	0.115	0.126	2.90	3.20	
G	0.087	0.094	2.20	2.40	
Н	0.0008	0.0040	0.020	0.100	
J	0.009	0.014	0.24	0.35	
K	0.060	0.078	1.50	2.00	
L	0.033	0.041	0.85	1.05	
Μ	0 °	10 °	0 °	10 °	
S	0.264	0.287	6.70	7.30	

Style 3: Pin 1. gate 2. drain 3. source 4. drain

# <u>Notes</u>

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