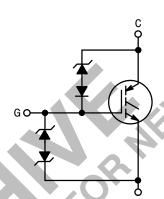
Product Preview

SMARTDISCRETES™ Internally Clamped, N-Channel IGBT

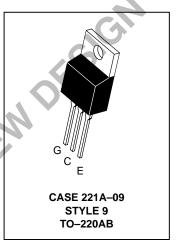
This Logic Level Insulated Gate Bipolar Transistor (IGBT) features Gate–Emitter ESD protection, Gate–Collector overvoltage protection from SMARTDISCRETES™ monolithic circuitry for usage as an **Ignition Coil Driver**.

- Temperature Compensated Gate—Collector Clamp Limits Stress Applied to Load
- Integrated ESD Diode Protection
- Low Threshold Voltage to Interface Power Loads to Logic or Microprocessors
- Low Saturation Voltage
- · High Pulsed Current Capability





20 AMPERES
VOLTAGE CLAMPED
N-CHANNEL IGBT
V_{CE(on)} = 1.9 VOLTS
135 VOLTS (CLAMPED)



MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	V _{CES}	CLAMPED	Vdc
Collector–Gate Voltage	V_{CGR}	CLAMPED	Vdc
Gate–Emitter Voltage	V_{GE}	CLAMPED	Vdc
Collector Current — Continuous — Single Pulsed ($t_p = \pm 10 \mu s$)	I _C I _{CM}	20 60	Adc Apk
Total Power Dissipation (TO–220) Derate Above 25°C	P _D	150 1.0	Watts W/°C
Operating and Storage Temperature Range	T _J , T _{stg}	-55 to 175	°C
Single Pulse Collector–Emitter Avalanche Energy @ Starting $T_J = 25^{\circ}$ C ($V_{CC} = 80 \text{ V}$, $V_{GE} = 5 \text{ V}$, Peak $I_L = 10 \text{ A}$, $L = 10 \text{ mH}$)	E _{AS}	500	mJ

THERMAL CHARACTERISTICS

Thermal Resistance — Junction to Case – (TO–220) — Junction to Ambient	R _{θJC} R _{θJA}	1.0 62.5	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 5 seconds	TL	260	°C
Mounting Torque, 6–32 or M3 screw	10 lbf∙in (1.13 N∙m)		

SMARTDISCRETES is a trademark of Motorola, Inc.

This document contains information on a new product. Specifications and information herein are subject to change without notice.



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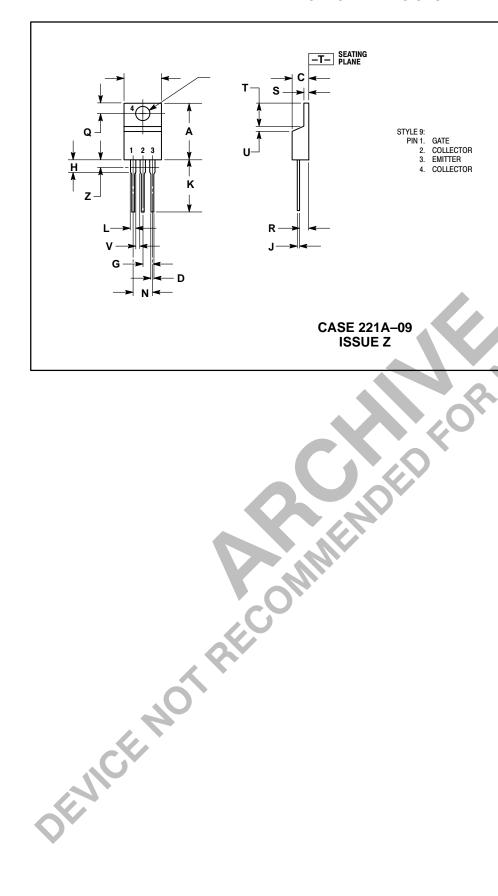
ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Clamp Voltage (I _{Clamp} = 10 mA, T _J = -40 to 150)°C)	V _{(BR)CES}	135			Vdc
Zero Gate Voltage Collector Curre $(V_{CE} = 100 \text{ V}, V_{GE} = 0 \text{ V})$ $(V_{CE} = 100 \text{ V}, V_{GE} = 0 \text{ V}, T_{J} = 1 \text{ V})$		I _{CES}		_ _ _	10 100	μΑ
Gate-Emitter Clamp Voltage (I _G =	1 mA)	V _{(BR)GES}	10			Vdc
Gate-Emitter Leakage Current (V	_{GE} = ±5 V, V _{CE} = 0 V)	I _{GES}	_	_	1.0	μΑ
ON CHARACTERISTICS (1)		•				
Gate Threshold Voltage (V _{CE} = V _{GE} , I _C = 1 mA) Threshold Temperature Coefficient	ent (Negative)	V _{GE(th)}	1.0	1.5 4.4	2.0	V mV/°C
Collector–Emitter On–Voltage $(V_{GE} = 5 \text{ V}, I_C = 10 \text{ A})$ $(V_{GE} = 5 \text{ V}, I_C = 10 \text{ Adc}, T_J = 17 \text{ Adc})$	′5°C)	V _{CE(on)}		<	1.9 1.8	V
Forward Transconductance (V _{CE}	> 15 V, I _C = 10 A)	9 _{fe}	8.0	15	_	Mhos
DYNAMIC CHARACTERISTICS				7/		
Input Capacitance		C _{ies}	/ -/	430	600	pF
Output Capacitance	$(V_{CE} = 25 \text{ Vdc}, V_{GE} = 0 \text{ Vdc}, f = 1.0 \text{ MHz})$	C _{oes}		182	250	
Transfer Capacitance	,	C _{res}	E.	48	100	
SWITCHING CHARACTERISTICS	(1)					
Turn-On Delay Time		t _{d(on)}	_	TBD	TBD	ns
Rise Time	$(V_{CC} = 68 \text{ V}, I_{C} = 20 \text{ A},$	t _r	_	TBD	TBD	
Turn-Off Delay Time	$V_{GE} = 5 \text{ V}, R_G = 9.1 \Omega)$	t _{d(off)}	_	TBD	TBD	
Fall Time		t _f	_	TBD	TBD	
Total Gate Charge		Q _T	_	14	20	nC
Gate–Emitter Charge	$(V_{CC} = 108 \text{ V}, I_{C} = 20 \text{ A}, V_{GE} = 5 \text{ V})$	Q _{ge}	_	3.0	_	
Gate–Collector Charge	vGE = 3 v)	Q _{ac}	_	6.0	_	

Gate–Collector Charge

(1) Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.

PACKAGE DIMENSIONS



- DIMENSIONING AND TOLERANCING PER ANSI
- DIMENSIONING AND TOLEHANCING PEH ANS Y14.5M, 1982. CONTROLLING DIMENSION: INCH. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.570	0.620	14.48	15.75	
В	0.380	0.405	9.66	10.28	
С	0.160	0.190	4.07	4.82	
D	0.025	0.035	0.64	0.88	
F	0.142	0.147	3.61	3.73	
G	0.095	0.105	2.42	2.66	
Н	0.110	0.155	2.80	3.93	
J	0.018	0.025	0.46	0.64	
K	0.500	0.562	12.70	14.27	
L	0.045	0.060	1.15	1.52	
N	0.190	0.210	4.83	5.33	
Q	0.100	0.120	2.54	3.04	
R	0.080	0.110	2.04	2.79	
S	0.045	0.055	1.15	1.39	
T	0.235	0.255	5.97	6.47	
U	0.000	0.050	0.00	1.27	
٧	0.045	-4-	1.15		
Z	1	0.080		2.04	



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