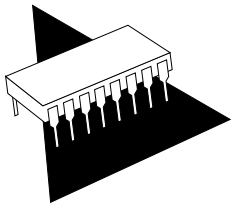
Revision History

- First Edition: March 1999
- Second Edition: February 2000

- Library name change STD111
- All characteristic values are updated with mass product line characteristics.
- Add high density compiled memories to second edition. (chapter 5)
- The name of previous compiled memories are changed for example: spsram into spsram_lp
- power equations are changed. (chapter 1)
- Updated PLL information
- Updated wire-load model



STD111

0.25μm 2.5V CMOS Standard Cell Library for Pure Logic Products

STD111
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for Pure Logic Products
Data Book

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Introduction

This databook contains information about STD111 0.25μm 2.5V standard cell library for pure Logic products developed by SEC (Samsung Electronics Corporation).

The “library” basically contains various kinds of internal and I/O cells and soft-macros which are used for developing ASIC (Application Specific Integrated Circuit). It also includes a design kit helping designers to work in a workstation platform, and all sorts of design environments needed for an automatic chip design.

There are seven chapters in this databook:

In this databook each cell is followed by its AC electrical characteristics, and these characteristic values are almost equal when the corresponding cell is operated in a real chip.

The purpose of this databook is to prevent any misuse or misapplication of STD111 cell library by providing precise information about the cell list, electrical data, directions for use, and matters demanding special attention.

If you want to get more information about Digital cores and Analog cores that are not included in this databook, access the Samsung ASIC web site(http://www.intl.samsungsemi.com) or contact Head Office.

Contents

Flip-Flops

4 Input/Output Cells

6 PLL

NOTE