Silicon Planar Zener Diode for Surge Absorb

HITACHI

ADE-208-851A (Z)

Rev.1 Nov. 2001

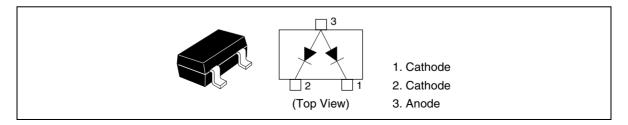
Features

- HZM6.8MWA has two devices in a monolithic, and can absorb surge.
- MPAK Package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Туре No.	Laser Mark	Package Code
HZM6.8MWA	68M	МРАК

Pin Arrangement





Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Power dissipation	Pd *	200	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	–55 to +150	°C

Note: Two device total, See Fig.2.

Electrical Characteristics*¹

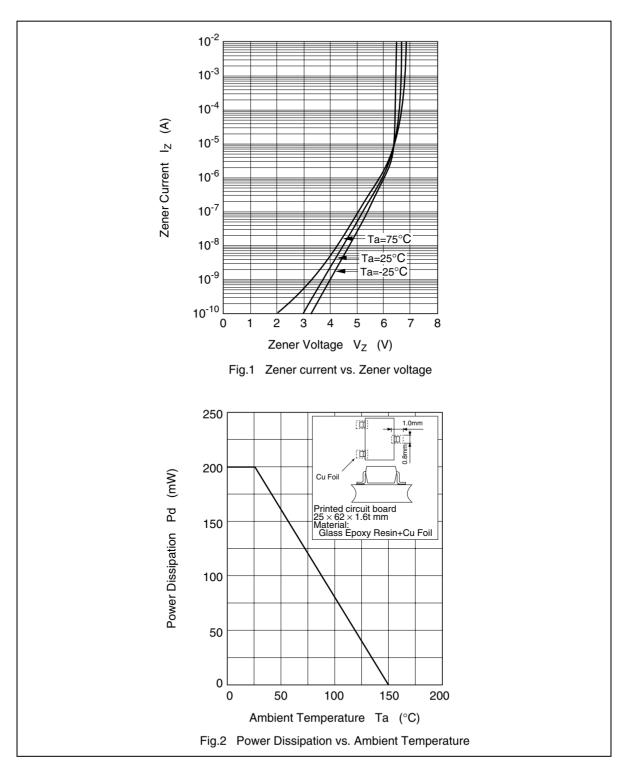
 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Мах	Unit	Test Condition
Zener voltage	Vz	6.47		7.00	V	$I_z = 5 \text{ mA}, 40 \text{ ms pulse}$
Reverse current	I _R			2	μA	V _R = 3.5 V
Capacitance	С			130	pF	$V_{_{\rm H}} = 0 V, f = 1 MHz$
Dynamic resistance	r _d			30	Ω	$I_z = 5 \text{ mA}$
ESD-Capability * ^{2,} * ³	—	30	_	_	kV	C = 150 pF, R = 330 Ω , Both forward and reverse direction 10 pulse

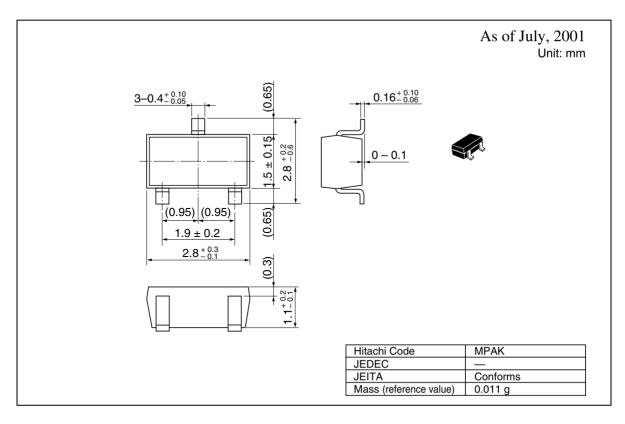
Notes: 1. Per one device.

- 2. Failure criterion ; $I_{_{\rm R}}$ > 2 μA at $V_{_{\rm R}}$ = 3.5 V.
- 3. Between cathode and anode

Main Characteristic



Package Dimensions



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