

TECHNICAL DATA DATA SHEET 602, REV -

HERMETIC POWER MOSFET N-CHANNEL

FEATURES:

- 100 Volt, 0.18 Ohm, 7.4A MOSFET
- Fast Switching
- Low R_{DS (on)}
- Equivalent to IRFE130

MAXIMUM RATINGS

ALL RATINGS ARE AT $T_c = 25$ °C UNLESS OTHERWISE SPECIFIED.

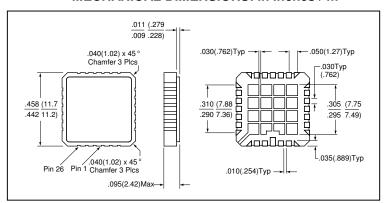
RATING	SYMBOL	MIN.	TYP.	MAX.	UNITS
GATE TO SOURCE VOLTAGE	V_{GS}	-	-	±20	Volts
ON-STATE DRAIN CURRENT @ $T_C = 25^{\circ}C$	I _D	-	-	10.8	Amps
ON-STATE DRAIN CURRENT @ $T_C = 100$ °C	I _D	-	-	6.8	Amps
OPERATING AND STORAGE TEMPERATURE	T_{OP}/T_{STG}	-55	-	+150	°C
TOTAL DEVICE DISSIPATION @ T _C = 25°C	P_{D}	-	-	42	Watts
THERMAL RESISTANCE, JUNCTION TO CASE	R_{thJC}	-	-	3.0	°C/W

ELECTRICAL CHARACTERISTICS

DRAIN TO SOURCE BREAKDOWN VOLTAGE $V_{GS} = 0V, I_D = 1.0 \text{mA}$	BV _{DSS}	100	-	-	Volts
STATIC DRAIN TO SOURCE ON STATE RESISTANCE $V_{GS} = 10V$, $I_D = 5.4A$	R _{DS(ON)}	-	-	0.18	Ω
GATE THRESHOLD VOLTAGE $V_{DS} = V_{GS}$, $I_D = 250 \mu A$	$V_{GS(th)}$	2.0	-	4.0	Volts
FORWARD TRANSCONDUCTANCE	g _{fs}	3.0	-	-	S(1/Ω)
$V_{DS} = 15V, I_{DS} = 6.8A$					
ZERO GATE VOLTAGE DRAIN CURRENT		-	-		
$V_{DS} = 0.8xMax$. Max. Rating, $V_{GS} = 0V$	I_{DSS}			25	μΑ
$V_{DS} = 0.8xMax$. Rating, $V_{GS} = 0V$, $T_{J} = 125$ °C				250	
GATE TO SOURCE LEAKAGE FORWARD V _{GS} = 20V	I_{GSS}	-	-	100	nA
GATE TO SOURCE LEAKAGE REVERSE V _{GS} = -20V				-100	
TURN ON DELAY TIME $V_{DD} = 50V$,	$t_{d(ON)}$	-	-	30	
RISE TIME $I_D = 10.8A$,	t _r			75	nsec
TURN OFF DELAY TIME $R_G = 7.5\Omega$	$t_{d(OFF)}$			40	
FALL TIME	t _f			45	
TOTAL GATE CHARGE $I_D = 10.8A$,	Q_{g}	12.8	-	28.5	nC
GATE TO SOURCE CHARGE $V_{GS} = 10V$,	Q_{gs}	1.0	-	6.3	nC
GATE TO DRAIN CHARGE $V_{DS} = 0.5xMax$. Rating	Q_{qd}	3.8	-	16.6	nC
DIODE FORWARD VOLTAGE $T_C = 25$ °C, $I_S = 10.8$ A, $V_{GS} = 0$ V	V _{SD}	-	-	1.5	Volts
REVERSE RECOVERY CHARGE $T_{\perp} = 25^{\circ}C$,	Q _{RR}	_		3.0	μС
$di/dt \le 100 \text{A/} \mu \text{sec}, \ V_{DD} \le 50 \text{V}$	S AR			0.0	μΟ
REVERSE RECOVERY TIME T _{.1} = 25°C,	t _{rr}	-	-	300	
$I_{\text{F}} = 10.8A$					nsec
$di/dt \le 100A/\mu sec, V_{DD} \le 50V$					
INPUT CAPACITANCE $V_{GS} = 0 \text{ V}$	C _{iss}	-	650	-	
OUTPUT CAPACITANCE $V_{DS} = 25 \text{ V}$	C_{oss}		240		pF
REVERSE TRANSFER CAPACITANCE f = 1.0MHz	C_{rss}		44		

DATA SHEET 602 REVISION -

MECHANICAL DIMENSIONS: in Inches / m



LCC-28T

PINOUT TABLE

Device Type	PIN(S) 1 & 15-28	PINS 5-11	PINS 2,3,13,14
Single MOSFET in a	SOURCE	DRAIN	GATE
LCC-28T Package			



TECHNICAL DATA

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