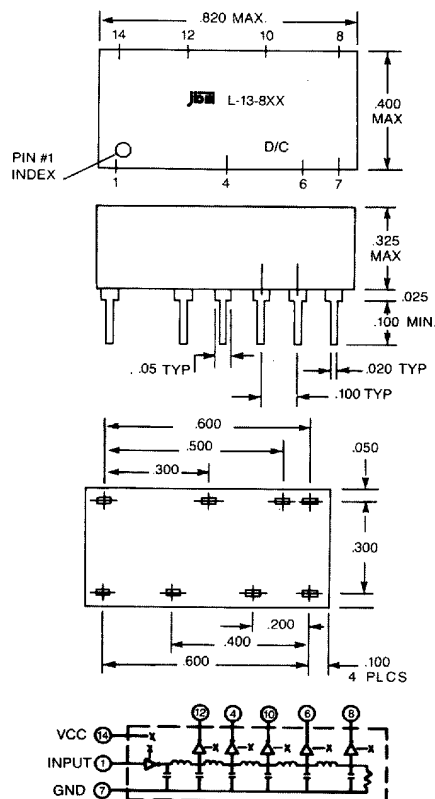


T-47-13

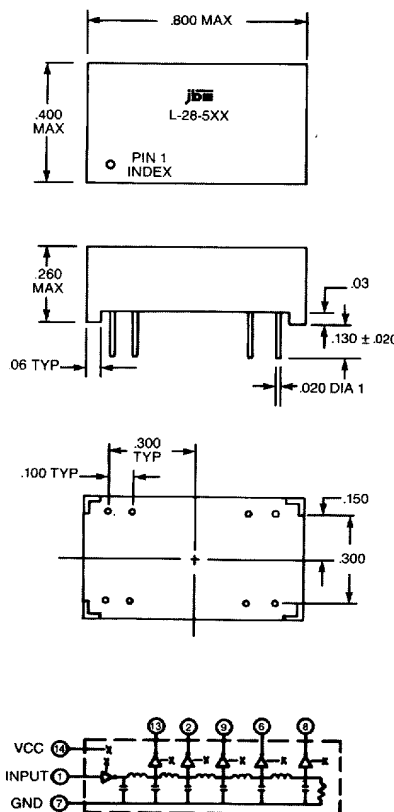
L-13-800 SERIES

5 Tap Active



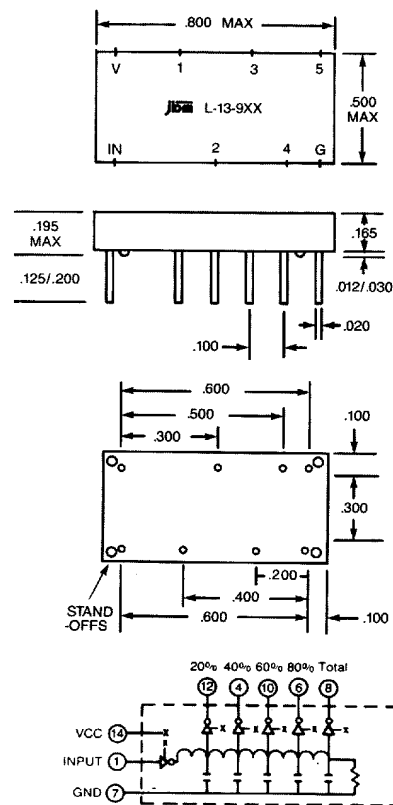
L-28-500 SERIES*

5 Tap
Low Profile



L-13-900 SERIES*

5 Tap
TTL Thin Dip



* See DESC Listing pg. 28

DELAY LINE CHARACTERISTICS

Input Characteristics:

V_{IH} Logic "1" Input Voltage 2.0V Min.
 V_{IL} Logic "0" Input Voltage8V Max.
 I_{IH} Logic "1" Input Current . . . 50 μ A Max.
 I_{IL} Logic "0" Input Current . . . -2mA Max.

Output Characteristics:

V_{OH} Logic "1" Output Voltage . . 2.7V Min.
 V_{OL} Logic "0" Output Voltage . . .5V Max.
 I_{CC} Supply Current75mA Max.

Environment:

Operating temperature -55°C to +125°C
 Storage temperature -55°C to +125°C
 Operating supply voltage . . . 4.5V to 5.5V

DELAY SPECIFICATIONS IN NANoseconds (ns) @ 25°C

TAP 1	TAP 2	TAP 3	TAP 4	TAP 5	STANDARD P/N	LOW PROFILE P/N	THIN P/N
5 ± 2	10 ± 2	15 ± 2	20 ± 2	25 ± 2	L-13-820	L-28-551	L-13-904
6 ± 2	12 ± 2	18 ± 2	24 ± 2	30 ± 2	L-13-821	L-28-552	L-13-905
7 ± 2	14 ± 2	21 ± 2	28 ± 2	35 ± 2	L-13-842	L-28-553	L-13-906
8 ± 2	16 ± 2	24 ± 2	32 ± 2	40 ± 2	L-13-822	L-28-554	L-13-907
9 ± 2	18 ± 2	27 ± 2	36 ± 2	45 ± 5%	L-13-843	L-28-555	L-13-908
10 ± 2	20 ± 2	30 ± 2	40 ± 2	50 ± 5%	L-13-823	L-28-556	L-13-909
12 ± 2	24 ± 2	36 ± 2	48 ± 5%	60 ± 5%	L-13-824	L-28-550	L-13-927
15 ± 2	30 ± 2	45 ± 5%	60 ± 5%	75 ± 5%	L-13-825	L-28-557	L-13-910
20 ± 2	40 ± 2	60 ± 5%	80 ± 5%	100 ± 5%	L-13-826	L-28-558	L-13-911
25 ± 2	50 ± 5%	75 ± 5%	100 ± 5%	125 ± 5%	L-13-827	L-28-559	L-13-912
30 ± 2	60 ± 5%	90 ± 5%	120 ± 5%	150 ± 5%	L-13-828	L-28-560	L-13-913
35 ± 2	70 ± 5%	105 ± 5%	140 ± 5%	175 ± 5%	L-13-844	L-28-561	L-13-914
40 ± 2	80 ± 5%	120 ± 5%	160 ± 5%	200 ± 5%	L-13-829	L-28-562	L-13-915
45 ± 5%	90 ± 5%	135 ± 5%	180 ± 5%	225 ± 5%	L-13-845	L-28-563	L-13-916
50 ± 5%	100 ± 5%	150 ± 5%	200 ± 5%	250 ± 5%	L-13-830	L-28-564	L-13-917
60 ± 5%	120 ± 5%	180 ± 5%	240 ± 5%	300 ± 5%	L-13-831	L-28-565	L-13-918
70 ± 5%	140 ± 5%	210 ± 5%	280 ± 5%	350 ± 5%	L-13-832	L-28-566	L-13-919
80 ± 5%	160 ± 5%	240 ± 5%	320 ± 5%	400 ± 5%	L-13-833	L-28-567	L-13-920
90 ± 5%	180 ± 5%	270 ± 5%	360 ± 5%	450 ± 5%	L-13-834	L-28-568	L-13-921
100 ± 5%	200 ± 5%	300 ± 5%	400 ± 5%	500 ± 5%	L-13-835	L-28-569	L-13-922

Delay Time will vary 2% or 1ns for each 5% change in Vcc.

Delay Time Over Temp. -55°C/125°C ± 3ns or 8%.

FEATURES

- Military temperature range
- Utilizes ceramic IC's screened to MIL-STD-883, Level B
- Temp. coefficient 550PPM/°C: Td

TEST CONDITIONS

Ambient Temperature 25°C
 Supply Voltage (VCC) . . . 5.0 ± .05VDC
 Input Pulse Amplitude 3.2V
 Input Pulse Width 2 × TD
 Input Pulse Duty Cycle 25% TYP
 Input Pulse Rise Time . . . 3ns (10 / 90%)
 Input Fall Time 3ns (90 / 10%)

NOTE - Pin numbers for reference only.