



SDM03MT40/A

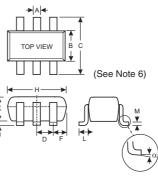
SURFACE MOUNT SCHOTTKY BARRIER DIODE

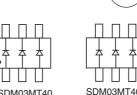
Features

- Low Forward Voltage Drop
- Guard Ring Die Construction for Transient Protection
- Ideal for low logic level applications
- Low Capacitance
- Lead Free/RoHS Compliant (Note 3)
- "Green" Device, Note 4 and 5

Mechanical Data

- Case: SOT-26
- Case Material: Molded Plastic, "Green" Molding Compound, Note 5. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Polarity: See Diagram
- Leads: Matte Tin (Lead Free), Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish annealed over Copper leadframe).
- Marking: Marking Code & Date Code (See Page 3)
- Marking Code: KSR, KSK
- Weight: 0.016 grams (approximate)





(KSR marking

code)

SDM03MT40A (KSK marking code)

SOT-26									
Dim	Min	Max	Тур						
Α	0.35	0.50	0.38						
В	1.50	1.60							
С	2.70	3.00	2.80						
D	_	_ _							
F	_	_	0.55						
Н	2.90	3.10	3.00						
J	0.013	0.10	0.05						
K	1.00 1.30		1.10						
L	0.35 0.55		0.40						
М	0.10 0.20		0.15						
α	0°	8°	_						
All Dimensions in mm									

Maximum Ratings @ T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit		
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _R WM V _R	40	V		
RMS Reverse Voltage	V _{R(RMS)}	28	V		
Forward Continuous Current (Note 2)	I _{FM}	30	mA		
Non-Repetitive Peak Forward Surge Current @8.3ms Single half sine-wave superimposed on rated load (JEDEDC method)	I _{FSM}	200	mA		
Power Dissipation (Note 2)	P _d	225	mW		
Thermal Resistance, Junction to Ambient Air	$R_{\theta JA}$	444	°C/W		
Operating and Storage Temperature Range	T _j , T _{STG}	-40 to +125	°C		

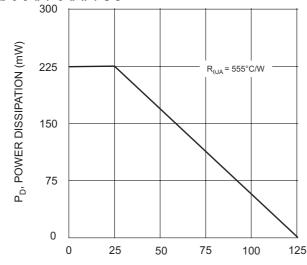
Electrical Characteristics @ T_A = 25°C unless otherwise specified

Characteristic		Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 1)	V _{(BR)R}	40	_	_	V	I _R = 10uA
Forward Voltage Drop (Note 1)	V _F	_	_	370	mV	I _F = 1mA
Leakage Current (Note 1)	I _R	_	_	1	μΑ	V _R = 10V
Total Capacitance	Ст	_	2	_	pF	V _R = 1V f = 1.0 MHz

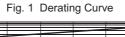
Notes: 1. Short duration test pulse to minimize self-heating effect.

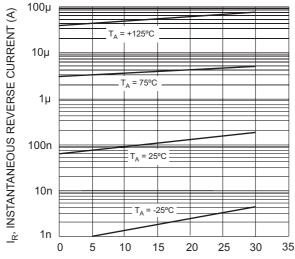
- 2. Device mounted on FR-4 PC board with recommended pad layout, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- 3. No purposefully added lead.
- 4. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
- 5. Product manufactured with Date Code 0609 (week 9, 2006) and newer are built with Green Molding Compound. Product manufactured prior to Date Code 0609 are built with Non-Green Molding Compound and may contain Halogens or Sb2O3 Fire Retardants.
- 6. Package is non-polarized. Parts may be on reel in orientation illustrated, 180° rotated, or mixed (both ways).



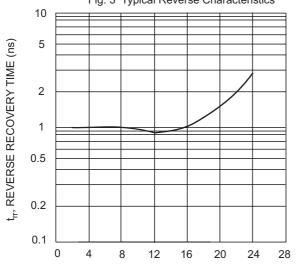


T_A, AMBIENT TEMPERATURE (°C)

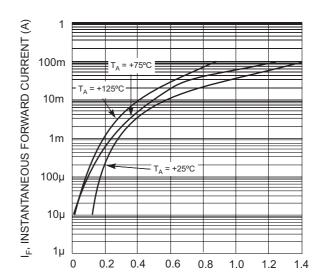




V_R, INSTANTANEOUS REVERSE VOLTAGE (V) Fig. 3 Typical Reverse Characteristics



I_F, FORWARD CURRENT (mA) Fig. 5 Typical Reverse Recovery Time Characteristics



V_F, INSTANTANEOUS FORWARD VOLTAGE (V) Fig. 2 Typical Forward Characteristics

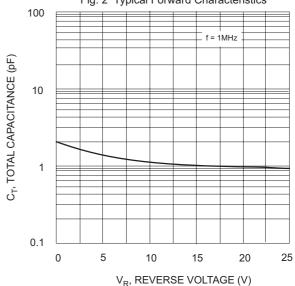


Fig. 4 Total Capacitance vs. DC Voltage

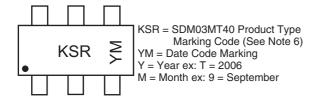


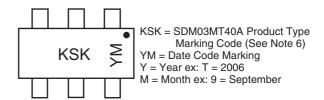
Ordering Information (Note 7)

Device	Packaging	Shipping		
SDM03MT40-7-F	SOT-26	3000/Tape & Reel		
SDM03MT40A-7-F	SOT-26	3000/Tape & Reel		

Notes: 7. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information





Date Code Key

Year		2006	200	07	2008		2009	20	10	2011		2012	
Code		Т	U	UV			W		X	Υ		Z	
Month	Jan	Feb	Mar	Арі	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec	
Code	1	2	3	4	5	6	7	8	9	0	N	D	

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