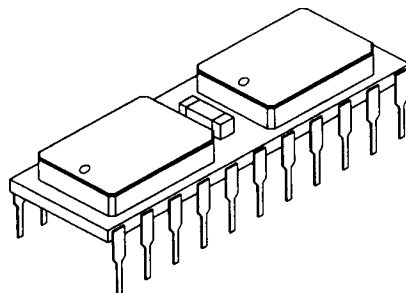


NOT RECOMENDED FOR NEW DESIGNS

DESCRIPTION:

The DPS88H04 is a 4K X 8 CMOS Static Random Access Memory (SRAM) module built with two 4K X 4 SRAMs. The memory utilizes asynchronous circuitry and may be maintained in any state for an indefinite period of time. All pins are TTL compatible, and a single +5V power supply is required.

The DPS88H04 is ideal for use in large computers or signal processors. The DPS88H04 offers the advantages of high speed and low cost.

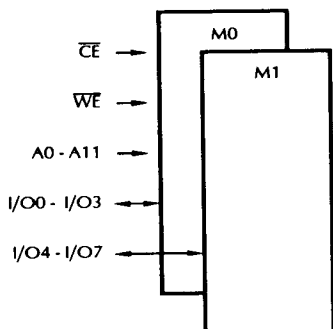


FEATURES:

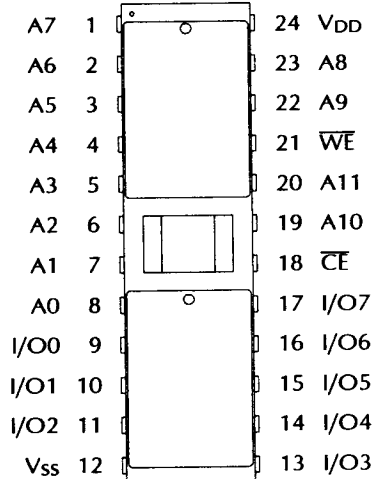
- 4K X 8 Organization
- Access Times: 25, 35, 45ns (max.)
- Fully Static Operation; No Clock or Refresh Required
- TTL-compatible Inputs and Outputs
- Common Data Inputs and Outputs
- Single +5V Operation ($\pm 10\%$ Tolerance)
- Available with All Semiconductor Components used in the Construction of the Module Compliant to MIL-STD-883; Class B
- 24-Pin, Ceramic DIP Package

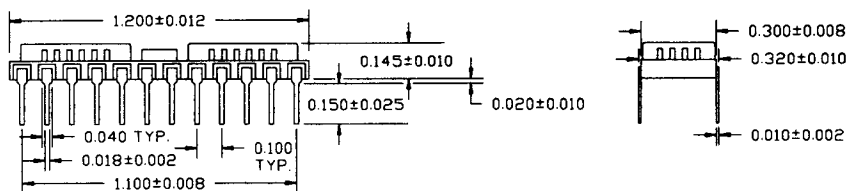
PIN NAMES	
A0 - A11	Address Inputs
I/O0 - I/O7	Data Input/Output
$\overline{CE}$	Chip Enable
$\overline{WE}$	Write Enable
V _{DD}	Power (+5V)
V _{SS}	Ground

FUNCTIONAL BLOCK DIAGRAM



PIN-OUT DIAGRAM



MECHANICAL DRAWING

- CONTACT DENSE-PAC SALES FOR MORE INFORMATION -

Dense-Pac Microsystems, Inc.

7321 Lincoln Way • Garden Grove, California 92641-1428
(714) 898-0007 • (800) 642-4477 (Outside CA) • FAX: (714) 897-1772