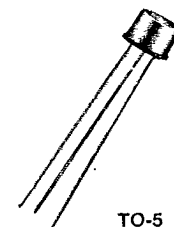


10 AMP/NPN-PNP Pirgo Silicon Planar Power Transistors

10 AMP
90 VOLT
5 WATT
60 MHz
TO-5

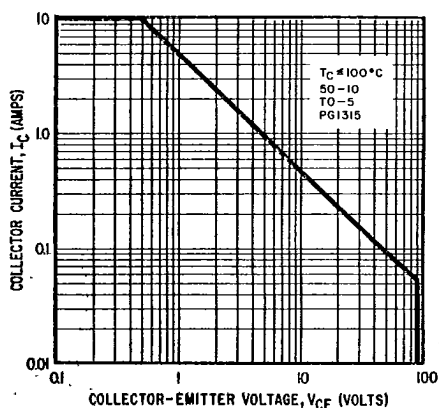


TO-5

- Linear h_{FE} from 50 mA to 10 amps
- Low saturation voltage at maximum collector current
- High frequency $f_t = 60$ MHz (typical)
- High voltage, $BV_{CEO(sus)}$ to 90 volts

NPN Type	Package	BV_{CBO}	BV_{CEO} (sus)	BV_{EBO}	h_{FE}		h_{FE} @ $I_C A$	V_{CE} (sat)		$C_{ob} Max$ pf	P_{DW} @ 100°C	PNP Complement
					Min	Max		Max	@ $I_C A$			
2N4150	TO-5	100	70	5	40	120	5		2.5 10	350	5	PG2328
PG1310	TO-5	70	50	8	20	60	5	10 10	2.5 10	350	5	PG2310
PG1311	TO-5	90	70	8	20	60	5	10 10	2.5 10	350	5	PG2311
PG1312	TO-5	120	100	8	20	60	5	10 10	2.5 10	350	5	PG2312
PG1313	TO-5	70	50	8	40	120	5	15 10	2.5 10	350	5	PG2313
PG1314	TO-5	90	70	8	40	120	5	15 10	2.5 10	350	5	PG2314
PG1315	TO-5	120	100	8	40	120	5	15 10	2.5 10	350	5	PG2315
PG1316	TO-5	70	50	8	100	300	5	20 10	2.5 10	350	5	PG2316
PG1317	TO-5	90	70	8	100	300	5	20 10	2.5 10	350	5	PG2317
PG1318	TO-5	120	100	8	100	300	5	20 10	2.5 10	350	5	PG2318
PG1319	TO-5	70	50	5	20	60	5		1.0 5	350	5	PG2319
PG1320	TO-5	90	70	5	20	60	5		1.0 5	350	5	PG2320
PG1321	TO-5	120	100	5	20	60	5		1.0 5	350	5	PG2321
PG1322	TO-5	70	50	5	40	120	5		1.0 5	350	5	PG2322
PG1323	TO-5	90	70	5	40	120	5		1.0 5	350	5	PG2323
PG1324	TO-5	120	100	5	40	120	5		1.0 5	350	5	PG2324
PG1325	TO-5	70	50	5	100	300	5		1.0 5	350	5	PG2325
PG1326	TO-5	90	70	5	100	300	5		1.0 5	350	5	PG2326
PG1327	TO-5	120	100	5	100	300	5		1.0 5	350	5	PG2327

MAXIMUM SAFE OPERATING AREA



NORMALIZED h_{FE} Vs I_C

