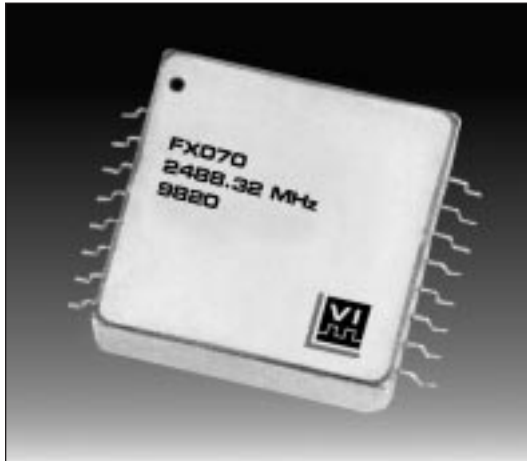


FX070 Frequency Translator



Features

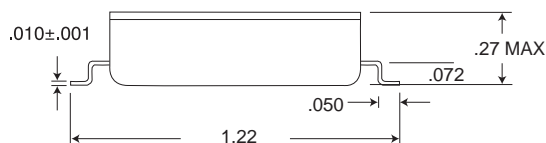
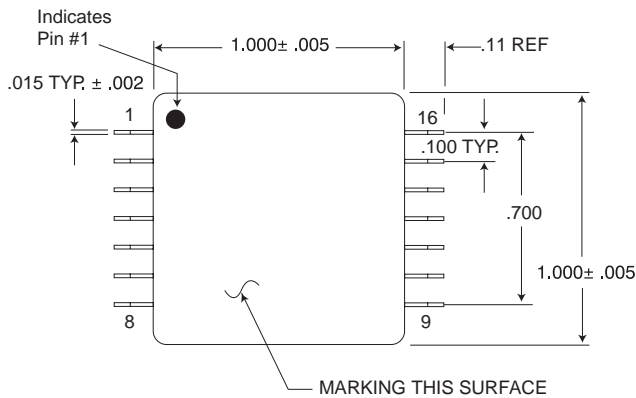
- 2488.32 MHz Output Locked to a 12.96 MHz Input Clock
- 16 pin 1 X 1 X 0.28 Flat Pack
- Single 5.0 Vdc Supply
- Sinewave Output
- <5 ps rms Output Jitter

Applications

- Clock Frequency Translation
- SONET/SDH/ATM
- Clock Distribution

Description

Vectron's FX070 frequency translator is a low noise, narrow band PLL that generates a 2488.32 MHz output clock locked to an input 12.96 MHz reference clock.



Note: Dimensions are in inches

Pin	Function
1	No Connection
7	Lock Detect
9	Output
16	+5 Vdc ±5% Supply
2,3,4,5,6,8,10	Case Ground
13,14,15	Case Ground
11	12.96 MHz Clock Out
12	12.96 MHz Clock In

FX070 Frequency Translator

Performance Characteristics

Table 1. Performance Characteristics					
Parameter	Symbol	Minimum	Typical	Maximum	Units
Supply Voltage	V _{DD}	4.75	5.0	5.25	V
Supply Current	I _{DD}	-	150	200	mA
Input Signal	CLKIN	1	-	2	V _{pp}
Output Signal	CLKOUT	Minimum 0 dbm / 50V. Sinewave			-
Output Jitter		-	3.5	5	rms
Tracking Range		-400	-	+400	ppm
Sub-Harmonics		-	-30	-20	dbc

Ordering Information

Table Part Numbering				
FX070FC				
Package	Lead Config.		Operating Temp.	
12.96 MHz to 2488.32 MHz Frequency Translator	F	Flat Pack	C	0 to 70°C

Handling Precautions

Although protection circuitry has been designed into this device, proper precautions should be taken to avoid exposure to electrostatic discharge (ESD) during handling and mounting.

*This Bulletin is provided for informational purposes to enable early engineering discussions. It should not be assumed that the product described in this document is qualified or available for manufacture.



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