



C107

Aut
DGF
092386

Silicon Controlled Rectifier

Flat Pack Design

Up to 600 Volts 4 Amperes (RMS)

M-03

Model C107

MAXIMUM ALLOWABLE RATINGS

Type	Repetitive Peak Off-State Voltage, V_{DRM} $R_{HK} = 1000 \text{ Ohms}$ $T_c = -40^\circ\text{C to } +110^\circ\text{C}$	Working and Repetitive Peak Reverse Voltage, V_{RRM} and V_{IRM} $T_c = -40^\circ\text{C to } +110^\circ\text{C}$
C107Q1, C107Q2, C107Q3, C107Q4	15 Volts	15 Volts
C107Y1, C107Y2, C107Y3, C107Y4	30 Volts	30 Volts
C107F1, C107F2, C107F3, C107F4	50 Volts	50 Volts
C107A1, C107A2, C107A3, C107A4	100 Volts	100 Volts
C107B1, C107B2, C107B3, C107B4	200 Volts	200 Volts
C107C1, C107C2, C107C3, C107C4	300 Volts	300 Volts
C107D1, C107D2, C107D3, C107D4	400 Volts	400 Volts
C107E1, C107E2, C107E3, C107E4	500 Volts	500 Volts
C107M1, C107M2, C107M3, C107M4	600 Volts	600 Volts

C107

CHARACTERISTICS

Test	Symbol	Min.	Typ.	Max.	Units	Test Conditions
Peak Reverse and Off-State Current (All Types)	I_{RRM} or I_{DRM}	-	0.1	10	μA	$V_{RRM} = V_{DRM} = \text{Rated Value.}$ $T_L = 25^\circ\text{C, } R_{GK} = 1000 \text{ Ohms}$
		-	10	100	μA	$V_{RRM} = V_{DRM} = \text{Rated Value.}$ $T_L = 110^\circ\text{C, } R_{GK} = 1000 \text{ Ohms.}$
*DC Gate Trigger Current	I_{GT}	-	-	500	μA_{DC}	$T_L = 25^\circ\text{C, } V_D = 6 \text{ Vdc, } R_L = 100 \text{ Ohms.}$ $R_{GK} = 1000 \text{ Ohms.}$
DC Gate Trigger Voltage	V_{GT}	0.4	0.5	0.8	Volts DC	$T_L = 25^\circ\text{C, } V_D = 6 \text{ Vdc, } R_L = 100 \text{ Ohms.}$ $R_{GK} = 1000 \text{ Ohms.}$
		0.2	-	-	Volts DC	$T_L = 110^\circ\text{C, Rated } V_{DRM}$ $R_L = 3000 \text{ Ohms, } R_{GK} = 1000 \text{ Ohms.}$
Peak On-State Voltage	V_{TM}	-	2.2	2.5	Volts	$T_L = 25^\circ\text{C, } I_{TM} = 4 \text{ Amperes Peak,}$ Single Half Sine Wave Pulse, 2 Millisec. Wide.
Holding Current	I_H	0.3	2.0	6.0	mA_{DC}	$T_L = 25^\circ\text{C, Anode Supply} = 12 \text{ Vdc,}$ $R_{GK} = 1000 \text{ Ohms.}$
		0.14	1.2	4.0	mA_{DC}	$T_L = 110^\circ\text{C, Anode Supply} = 12 \text{ Vdc,}$ $R_{GK} = 1000 \text{ Ohms.}$
Latching Current	I_L	0.3	3.0	8.0	mA_{DC}	$T_L = 25^\circ\text{C, Anode Supply} = 12 \text{ Vdc,}$ $R_{GK} = 1000 \text{ Ohms.}$
Critical Rate of Rise of Off-State Voltage	dv/dt	-	8	-	Volts/ Micro-second	$T_L = 110^\circ\text{C, Rated } V_{DRM}$ $R_{GK} = 1000 \text{ Ohms.}$
Turn-On Time	$t_d + t_r$	-	1.2	-	Micro-seconds	$T_L = 25^\circ\text{C, Rated } V_{DRM}$ $I_{TM} = 1 \text{ Ampere, Gate Pulse} = 4 \text{ Volts,}$ 300 Ohms, 5 Microseconds Wide.
Circuit Commutated Turn-Off Time	t_q	-	40	100	Micro-seconds	$T_L = 110^\circ\text{C, rectangular current waveform.}$ Rate of Rise of current $< 10 \text{ amps}/\mu\text{sec.}$ Rate of reversal of current $< 5 \text{ amps}/\mu\text{sec.}$ $I_{TM} = 1 \text{ Amp (50 } \mu\text{sec. pulse). Repetition}$ Rate = 60 pps. Rated V_{RRM} $V_R = 15 \text{ Volts Minimum. Rated } V_{DRM}$ Rate of Rise Reapplied Forward Blocking Voltage = 5 Volts/ $\mu\text{sec.}$ Gate Bias = 0 Volts, 100 Ohms (during turn-off time interval).

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EDITOR