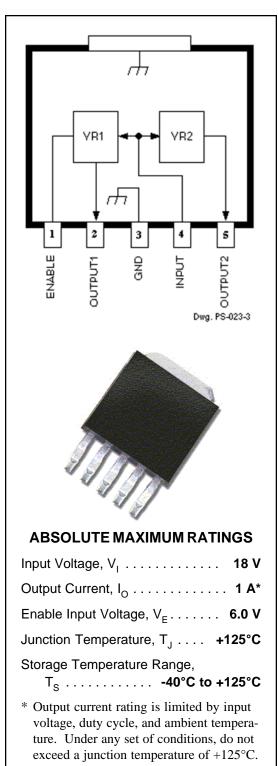


PRELIMINARY ISSUE Subject to change without notice October 4, 2004

# **SI-3005**KWM

# 1 A, Low-Dropout, Dual Output, 1.5 V & 1.8 V Regulator





Designed to meet the high-current requirements in industrial and consumer applications; embedded core, memory, or logic supplies; TVs, VCRs, and office equipment, the SI-3005KWM voltage regulator offers the reduced dropout voltage and low quiescent current essential for improved efficiency. This device delivers dual regulated outputs at up to 1 A. Integrated thermal and overcurrent protection enhance overall system reliability. Devices with other output voltages are also available.

Quiescent current does not increase significantly as the dropout voltage is approached, an ideal feature in standby/resume power systems where data integrity is crucial. Regulator accuracy and excellent temperature characteristics are provided by a bandgap reference. An LS-TTL/CMOS-compatible input gives the designer complete control over Channel 1 (1.8 V) power up, standby, or power down. A pnp pass element provides a dropout voltage of less than 600 mV at 1 A of load current. Low output voltages eliminate the need for expensive PWM buck converters. The low dropout voltage permits more efficient regulation before output regulation is lost.

This device is supplied in a 5-lead TO-252 style surface-mount plastic package with ground tab to provide a low-resistance path for maximum heat dissipation.

#### FEATURES

- 1 A Output Current per Channel
- 0.6 V Maximum Dropout Voltage at  $I_0 = 1 A$
- Fast Transient Response
- 1.5 mA Maximum Standby Current
- Foldback Current Limiting
- Ground Tab for Superior Heat Dissipation
- Thermal Protection

#### APPLICATIONS

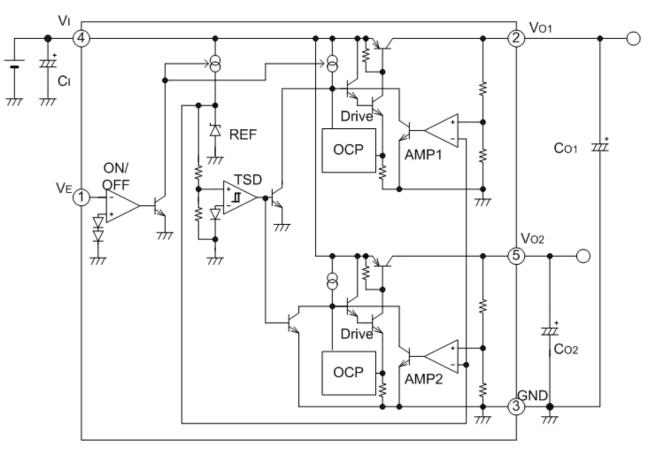
- TVs, VCRs, Electronic Games
- Embedded Core, Memory, or Logic Supplies
- Printers and Other Office Equipment
- Industrial Machinery
- Secondary-Side Stabilization of Multi-Output SMPS

Always order by complete part number, e.g., **SI-3005KWM-TL**, where "-TL" indicates tape and reel.

Sanken Power Devices from Allegro MicroSystems

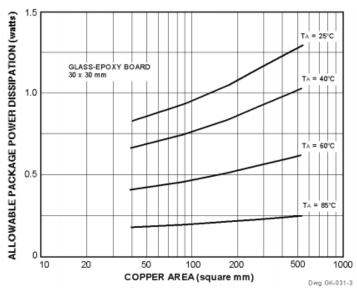






FUNCTIONAL BLOCK DIAGRAM

#### Allowable Package Power Dissipation



#### **Recommended Operating Conditions**

	Min	Max	Units
DC Output Current	0	1	А
Operating Ambient Temp.	-30	+85	°C
Operating Junction Temp.	-20	+100	°C

This data sheet is based on Sanken data sheet SSJ-xxxxx



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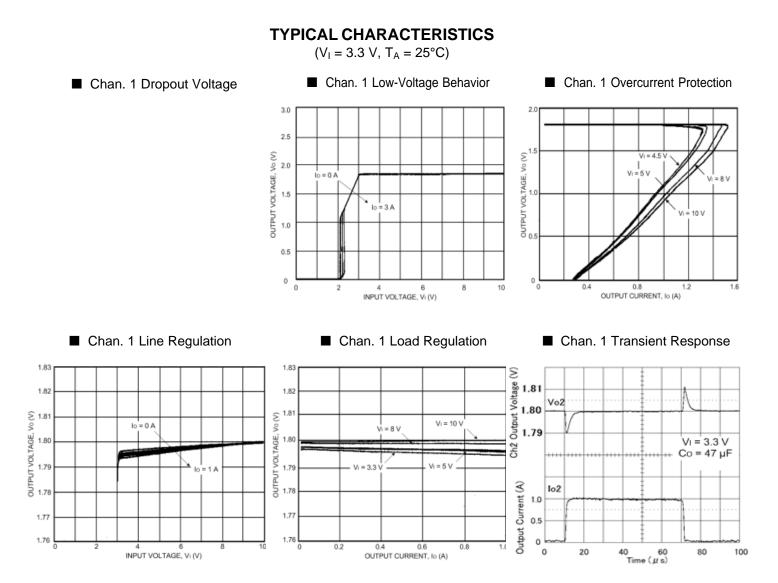
## **ELECTRICAL CHARACTERISTICS** at $T_A = +25^{\circ}C$ , $V_E = 2 V$ (unless otherwise noted).

			Limits			
Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Channel 1						
Output Voltage	V <sub>01</sub>	V <sub>I</sub> = 3.3 V, I <sub>O1</sub> = 10 mA	1.764	1.800	1.836	V
Output Volt. Temp. Coeff.	a <sub>vo1</sub>	$0^{\circ}C \leq T_{J} \leq 100^{\circ}C$	_	±0.3		mV/°C
Output Short-Circuit Current	I <sub>OM1</sub>	$V_{I} = 3.3 V$ , See note	1.2	_		А
Line Regulation	$\Delta V_{O(\Delta VI)1}$	$V_{I} = 3.0 \text{ V} \sim 5.0 \text{ V}, I_{O1} = 10 \text{ mA}$	_	_	20	mV
Load Regulation	$\Delta V_{O(\Delta IO)1}$	V <sub>I</sub> = 3.3 V, I <sub>O1</sub> = 0 A ~ 1.0 A	_	_	30	mV
Dropout Voltage	V <sub>Imin</sub> - V <sub>O1</sub>	I <sub>O1</sub> = 1.0 A	_	_	0.6	V
Ripple Rejection Ratio	PSRR	$V_{\rm I}$ = 3.3 V, 100 Hz $\leq$ f $\leq$ 120 Hz	_	60	_	dB
Channel 2			-			
Output Voltage	V <sub>O2</sub>	V <sub>I</sub> = 3.3 V, I <sub>O2</sub> = 10 mA	1.470	1.500	1.530	V
Output Volt. Temp. Coeff.	a <sub>vo2</sub>	$0^{\circ}C \leq T_{J} \leq 100^{\circ}C$	_	±0.3		mV/°C
Output Short-Circuit Current	I <sub>OM2</sub>	V <sub>I</sub> = 3.3 V, See note	1.2	_		A
Line Regulation	$\Delta V_{O(\Delta VI)2}$	$V_{I} = 3.0 \text{ V} \sim 5.0 \text{ V}, I_{O2} = 10 \text{ mA}$	_	_	20	mV
Load Regulation	$\Delta V_{O(\Delta IO)2}$	$V_1 = 3.3 \text{ V}, \ I_{O2} = 0 \text{ A} \sim 1.0 \text{ A}$	_	_	30	mV
Dropout Voltage	V <sub>Imin</sub> - V <sub>O2</sub>		_	_	0.6	V
Ripple Rejection Ratio	PSRR	$V_{\rm I}$ = 3.3 V, 100 Hz $\leq$ f $\leq$ 120 Hz	_	60	_	dB
Logic						
Ground Terminal Current	I <sub>GND</sub>	V <sub>I</sub> = 3.3 V, I <sub>O</sub> = 0 mA, V <sub>E</sub> = 2.0 V	_	1.0	1.5	mA
		V <sub>I</sub> = 3.3 V, V <sub>E</sub> = 0 V	_	_	0.5	mA
Enable Input Voltage	V <sub>EH</sub>	Output ON	2.0	_	_	V
(Channel 1 only)	V <sub>EL</sub>	Output OFF	_	_	0.8	V
Enable Input Current	I <sub>EH</sub>	V <sub>E</sub> = 2.7 V			5.0	μA
(Channel 1 only)	I <sub>EL</sub>	$V_{E} = 0.4 V$	_	_	-100	μA
Thermal Shutdown	Т <sub>Ј</sub>	I <sub>0</sub> = 10 mA	135	150		°C

Typical values are given for circuit design information only.

Note: Output short-circuit current is at point where output voltage (at V<sub>1</sub> = 5 V,  $I_0$  = 10 mA) has decreased 5%.

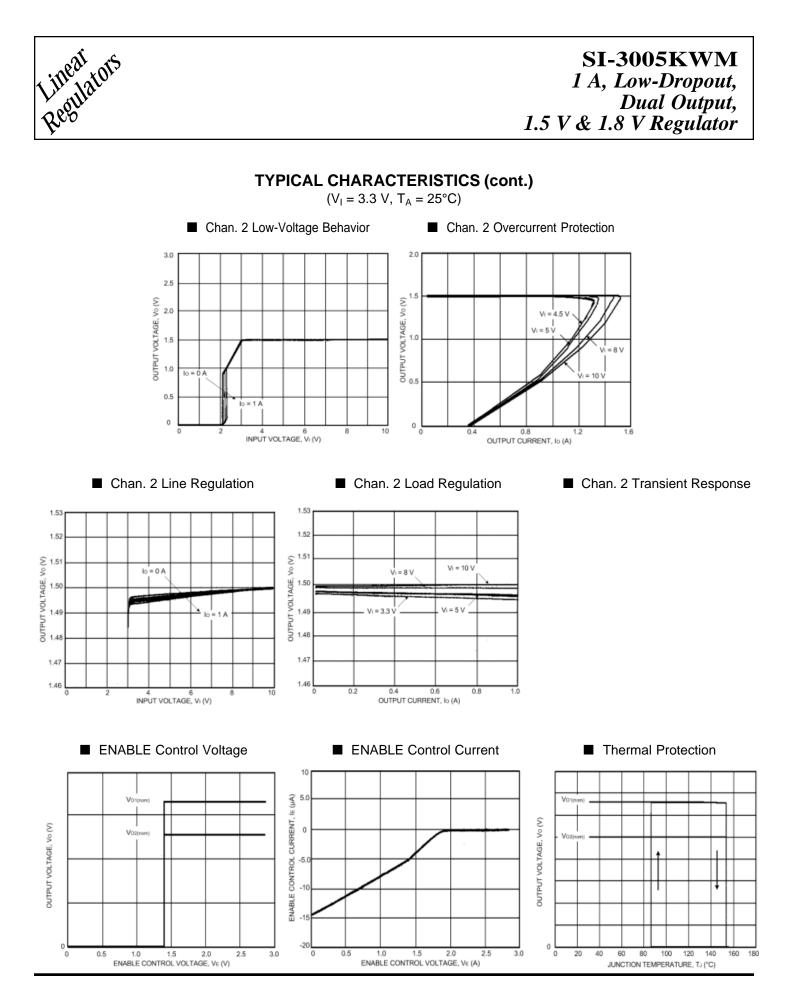






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#### **APPLICATIONS INFORMATION**

**Input Capacitor** (C<sub>1</sub>,  $0.1 \sim 10 \ \mu$ F). This is necessary either when the input line includes inductance or when the wiring is long.

**Output Capacitor** ( $C_0$ , >22  $\mu$ F). This device is not designed for a use with a very low ESR output capacitor such as a ceramic capacitor. Output oscillation may occur with that kind of capacitor.

**ENABLE Input.** The ENABLE (control) input features an internal pull-up resistor. Leaving this input open causes the output to turn on.

**Parallel Operation.** Parallel operation to increase load current is not permitted.

**Determination of DC Input Voltage.** The minimum input voltage  $V_I(min)$  should be higher than the sum of the fixed output voltage and the maximum rated dropout voltage.

**Overcurrent Protection.** The SI-3000KWM series has a built-in fold-back type overcurrent protection circuit, which limits the output current at a start-up mode. It thus cannot be used in applications that require current at the start-up mode such as:

(1) constant-current load,

(2) power supply with positive and negative outputs to common load (a center-tap type power supply), or(3) raising the output voltage by putting a diode or a resistor between the device ground and system ground.

**Thermal Protection.** Circuitry turns off the pass transistor when the junction temperature rises above 135°C. It is intended only to protect the device from failures due to excessive junction temperatures and should not imply that output short circuits or continuous overloads are permitted.

**Heat Radiation and Reliability.** The reliability of the IC is directly related to the junction temperature  $(T_J)$  in its operation. Accordingly, careful consideration should be given to heat dissipation.

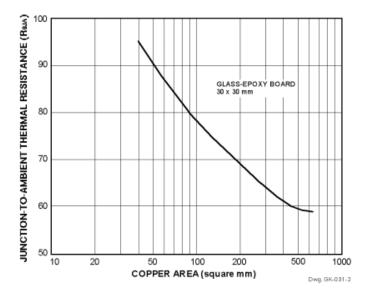
The inner frame on which the integrated circuit is mounted is connected to the GND terminal (pin 3). Therefore, it is very effective for heat radiation to enlarge the copper area that is connected to the GND terminal. The graph illustrates the effect of the copper area on the junction-toambient thermal resistance ( $R_{\text{HA}}$ ).

The junction temperature  $(T_J)$  can be determined from either of the following equations:

 $T_{I} = (P_{D} \times R_{\theta IA}) + T_{A}$ 

or

$$\begin{split} T_{J} &= (P_{D} \times R_{\theta JT}) + T_{T} \\ \text{where} \quad P_{D} &= I_{OI}(V_{I} - V_{O1}) + I_{O2}(V_{I} - V_{O2}) \text{ and} \\ R_{\theta JT} &= 6^{\circ}C/W. \end{split}$$



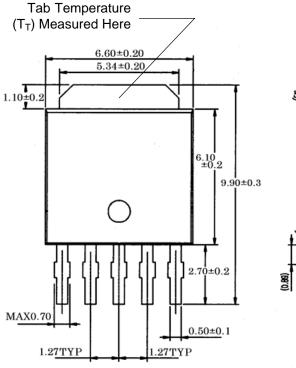


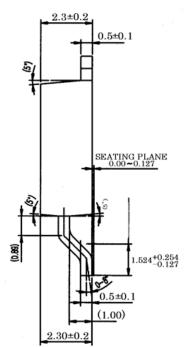
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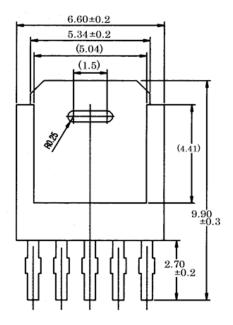


### SI-3005KWM 1 A, Low-Dropout, Dual Output, 1.5 V & 1.8 V Regulator

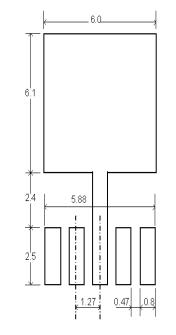
#### **Dimensions in Millimeters**







**Recommended Land Pattern** 



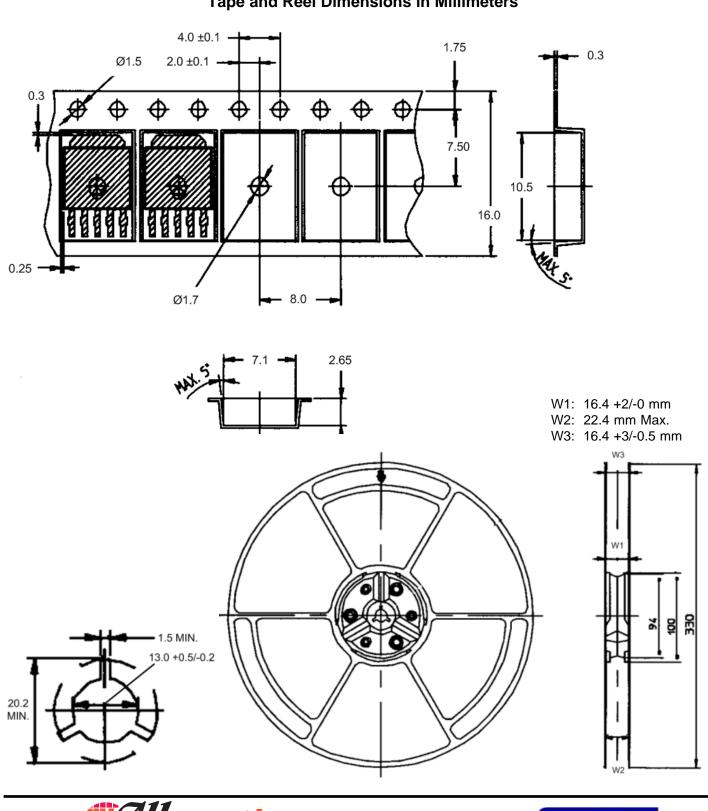


Terminal Finish: Pure Sn (category e3)

Product Weight: Approx. 0.33 g

3000 pieces per reel.







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