Hex Inverters

# HITACHI

ADE-205-248 (Z) 1st Edition March 1999

#### Description

The HD74LVU04A has six inverters with unbuffered outputs in a 14-pin package. Low-voltage and high-speed operation is suitable for the battery-powered products (e.g., notebook computers), and the low-power consumption extends the battery life.

#### Features

- $V_{CC} = 2.0 \text{ V}$  to 5.5 V operation
- All inputs  $V_{IH}$  (Max.) = 5.5 V (@V<sub>CC</sub> = 0 V to 5.5 V)
- All outputs  $V_0$  (Max.) = 5.5 V (@V<sub>CC</sub> = 0 V)
- Typical  $V_{OL}$  ground bounce < 0.8 V (@V<sub>CC</sub> = 3.3 V, Ta = 25°C)
- Typical  $V_{OH}$  undershoot > 2.3 V (@V<sub>CC</sub> = 3.3 V, Ta = 25°C)
- Output current  $\pm 6 \text{ mA}$  (@V<sub>cc</sub> = 3.0 V to 3.6 V),  $\pm 12 \text{ mA}$  (@V<sub>cc</sub> = 4.5 V to 5.5 V)

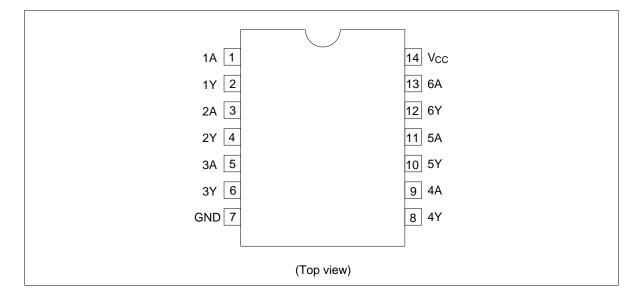
#### **Function Table**

Input A	Output Y
Н	L
L	Н
Note: H: High level	

L: Low level



## Pin Arrangement



#### **Absolute Maximum Ratings**

ltem	Symbol	Ratings	Unit	Conditions
Supply voltage range	V <sub>cc</sub>	-0.5 to 7.0	V	
Input voltage range*1	V	-0.5 to 7.0	V	
Output voltage range*1,2	Vo	–0.5 to V <sub>cc</sub> + 0.5	V	Output: H or L
Input clamp current	I <sub>IK</sub>	-20	mA	V <sub>1</sub> < 0
Output clamp current	Ι <sub>οκ</sub>	±50	mA	$V_{o}$ < 0 or $V_{o}$ > $V_{cc}$
Continuous output current	I <sub>o</sub>	±25	mA	$V_{o}$ = 0 to $V_{cc}$
Continuous current through $V_{cc}$ or GND	$I_{\rm CC}$ or $I_{\rm GND}$	±50	mA	
Maximum power dissipation at Ta = $25^{\circ}$ C (in still air)* <sup>3</sup>	Ρ <sub>τ</sub>	785	mW	SOP
		500	-	TSSOP
Storage temperature	Tstg	-65 to 150	°C	

Notes: The absolute maximum ratings are values which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

2. This value is limited to 5.5 V maximum.

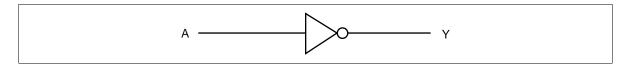
3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

# **Recommended Operating Conditions**

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V <sub>cc</sub>	2.0	5.5	V	
Input voltage range	V	0	5.5	V	
Output voltage range	Vo	0	V <sub>cc</sub>	V	
Output current	I <sub>OH</sub>	_	-50	μΑ	$V_{cc} = 2.0 V$
		_	-2	mA	$V_{cc}$ = 2.3 to 2.7 V
		_	-6		$V_{cc}$ = 3.0 to 3.6 V
		_	-12		$V_{cc}$ = 4.5 to 5.5 V
	I <sub>OL</sub>	_	50	μΑ	$V_{cc} = 2.0 V$
		_	2	mA	$V_{cc}$ = 2.3 to 2.7 V
		_	6		$V_{cc}$ = 3.0 to 3.6 V
		_	12		$V_{cc}$ = 4.5 to 5.5 V
Operating free-air temperature	Та	-40	85	°C	

Note: Unused or floating inputs must be held high or low.

#### Logic Diagram



#### **DC Electrical Characteristics**

#### • Ta = -40 to $85^{\circ}C$

Item	Symbol	V <sub>cc</sub> (V)*	Min	Тур	Max	Unit	Test Conditions
Input voltage	V <sub>IH</sub>	2.0	1.7	_	—	V	
		2.3 to 2.7	$V_{CC}  imes 0.8$		—	_	
		3.0 to 3.6	$V_{CC}  imes 0.8$	—	—	_	
		4.5 to 5.5	$V_{CC}  imes 0.8$	_	—	_	
	V <sub>IL</sub>	2.0	_		0.3	_	
		2.3 to 2.7	—		$V_{CC} \times 0.2$	_	
		3.0 to 3.6	—		$V_{CC} \times 0.2$	_	
		4.5 to 5.5	_		$V_{CC}  imes 0.2$	_	
Output voltage	$V_{OH}$	Min to Max	$V_{\rm CC} - 0.1$	—	—	V	$I_{OL} = -50 \ \mu A$
		2.3	2.0	_	_	_	$I_{OL} = -2 \text{ mA}$
		3.0	2.48	_	_	_	$I_{OL} = -6 \text{ mA}$
		4.5	3.8		—	_	$I_{OL} = -12 \text{ mA}$
	V <sub>OL</sub>	Min to Max	_	—	0.1	V	$I_{OL} = 50 \ \mu A$
		2.3	_	_	0.4	_	$I_{OL} = 2 \text{ mA}$
		3.0	_	_	0.44	_	$I_{OL} = 6 \text{ mA}$
		4.5	_		0.55	_	I <sub>OL</sub> = 12 mA
Input current	I <sub>IN</sub>	0 to 5.5	_		±1	μΑ	$V_{IN} = 5.5 \text{ V or GND}$
Quiescent supply current	I <sub>cc</sub>	5.5	_	—	20	μA	$V_{IN} = V_{CC}$ or GND, $I_0 = 0$
Input capacitance	C <sub>IN</sub>	3.3	_	4.0	_	pF	$V_{I} = V_{CC}$ or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

## **Switching Characteristics**

•  $V_{CC} = 2.5 \pm 0.2 \text{ V}$ 

		Ta =	25°C		Ta = –	40 to 85°C				
ltem	Symbol	Min	Тур	Max	Min	Max	Unit	Test Conditions	FROM (Input)	TO (Output)
Propa- gation delay time	t <sub>PLH</sub> t <sub>PHL</sub>		3.2	10.9	1.0	14.0	ns	C <sub>L</sub> = 15 pF	A	Y
		—	6.6	13.4	1.0	16.0	-	C <sub>L</sub> = 50 pF		

		Ta =	25°C		Ta = -	40 to 85°C				
Item Symbol	Symbol	Min	Тур	Max	Min	Max	Unit	Test Conditions	FROM (Input)	TO (Output)
Propa- gation delay time	t <sub>PLH</sub> t <sub>PHL</sub>	_	2.5	8.9	1.0	10.5	ns	C <sub>L</sub> = 15 pF	A	Y
		_	4.7	11.4	1.0	13.0	-	C <sub>1</sub> = 50 pF		

		Ta =	25°C		Ta = -	40 to 85°C				
ltem Symbol	Min	Тур	Max	Min	Max	Unit	Test Conditions	FROM (Input)	TO (Output)	
Propa- gation delay time	t <sub>PLH</sub> t <sub>PHL</sub>	_	2.2	5.5	1.0	6.5	ns	C <sub>L</sub> = 15 pF	A	Y
		_	3.9	7.0	1.0	8.0	-	C <sub>L</sub> = 50 pF		

## **Operating Characteristics**

#### • $C_L = 50 \ pF$

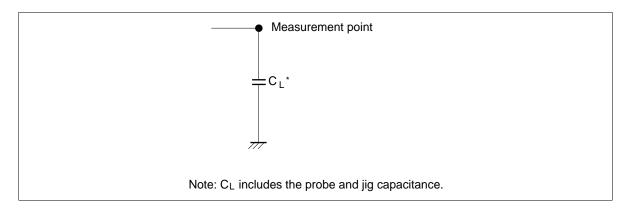
				Ta = 25	°C			
ltem	Symbol	V <sub>cc</sub> (V)	Min	Тур	Мах	Unit	Test Conditions	
Power dissipation capacitance	C <sub>PD</sub>	3.3	_	5.6	_	pF	f = 10 MHz	
		5.0	_	6.7	_			

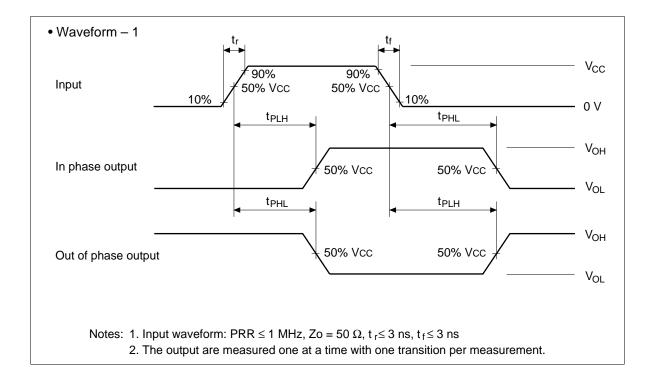
#### **Noise Characteristics**

•  $C_L = 50 \text{ pF}$ 

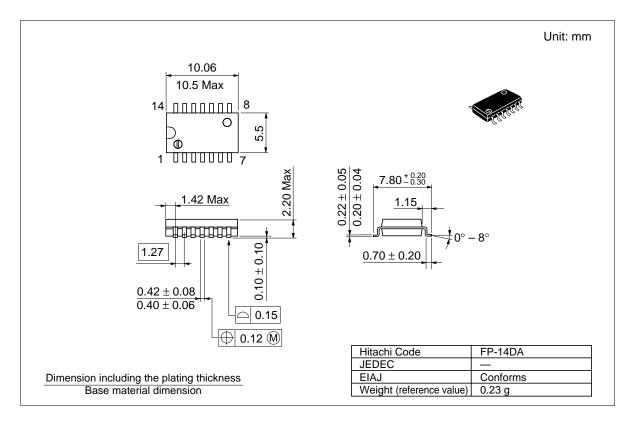
			Ta = 25°	°C			
Item	Symbol	V <sub>cc</sub> (V)	Min	Тур	Max	Unit	Test Conditions
Quiet output, maximum dynamic V <sub>oL</sub>	V <sub>OL (P)</sub>	3.3	_	0.5	0.8	V	
Quiet output, minimum dynamic V <sub>oL</sub>	$V_{OL(V)}$	3.3	_	-0.1	-0.8		
Quiet output, minimum dynamic V <sub>OH</sub>	$V_{OH(V)}$	3.3	_	3.0	_		
High-level dynamic put voltage	V <sub>IH (D)</sub>	3.3	2.31	_	_	V	_
Low-level dynamic put voltage	V <sub>IL (D)</sub>	3.3	—	—	0.99		

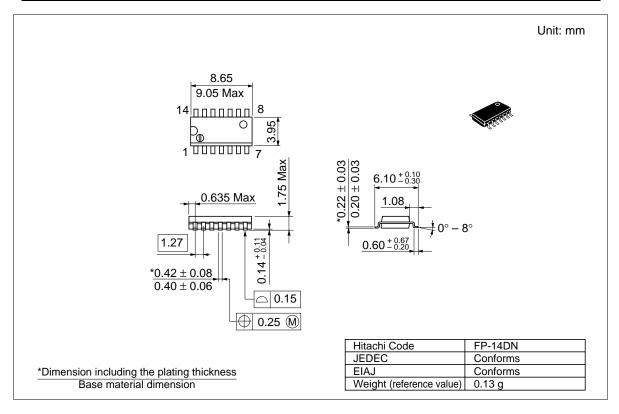
#### **Test Circuit**

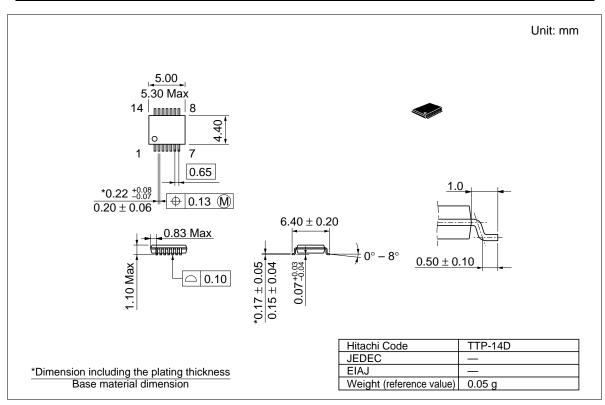




## **Package Dimensions**







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