# NTD3055-094

# Advance Information

Power MOSFET 12 Amps, 60 Volts N-Channel DPAK

Designed for low voltage, high speed switching applications in power supplies, converters and power motor controls and bridge circuits.

# Features

- Lower RDS(on)
- Lower VDS(on)
- Lower and Tighter  $V_{SD}$
- Lower Diode Reverse Recovery Time
- Lower Reverse Recovery Stored Charge

## **Typical Applications**

- Power Supplies
- Converters
- Power Motor Controls
- Bridge Circuits

# **MAXIMUM RATINGS** (T<sub>J</sub> = $25^{\circ}$ C unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage	VDSS	60	Vdc
Drain-to-Gate Voltage (R <sub>GS</sub> = 10 MΩ)	VDGR	60	Vdc
Gate-to-Source Voltage			Vdc
– Continuous – Non–Repetitive (t <sub>p</sub> ≤10 ms)	VGS VGS	$\pm 20 \pm 30$	
Drain Current – Continuous @ $T_A = 25^{\circ}C$ – Continuous @ $T_A = 100^{\circ}C$ – Single Pulse ( $t_p \le 10 \ \mu$ s)	I <sub>D</sub> I <sub>D</sub> IDM	12 10 45	Adc Apk
Total Power Dissipation @ $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$ Total Power Dissipation @ $T_A = 25^{\circ}C$ (Note 1.) Total Power Dissipation @ $T_A = 25^{\circ}C$ (Note 2.)	PD	48 0.32 2.1 1.5	W W/°C W W
Operating and Storage Temperature Range	т <sub>Ј</sub> , Т <sub>stg</sub>	–55 to +175	°C
Single Pulse Drain–to–Source Avalanche Energy – Starting $T_J = 25^{\circ}C$ (V <sub>DD</sub> = 25 Vdc, V <sub>GS</sub> = 10 Vdc, L = 1.0 mH I <sub>L</sub> (pk) = 11 A, V <sub>DS</sub> = 60 Vdc)	EAS	61	mJ
Thermal Resistance – Junction–to–Case – Junction–to–Ambient (Note 1.) – Junction–to–Ambient (Note 2.)	R <sub>θ</sub> JC R <sub>θ</sub> JA R <sub>θ</sub> JA	3.13 71.4 100	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	ΤL	260	°C

 When surface mounted to an FR4 board using 1" pad size, (Cu Area 1.127 in<sup>2</sup>).

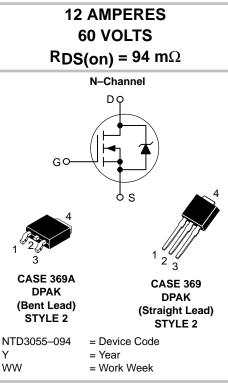
 When surface mounted to an FR4 board using the minimum recommended pad size, (Cu Area 0.412 in<sup>2</sup>).

This document contains information on a new product. Specifications and information herein are subject to change without notice.

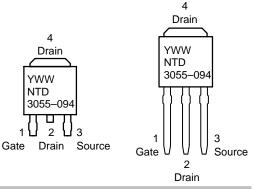


# **ON Semiconductor**

http://onsemi.com







### **ORDERING INFORMATION**

Device	Package	Shipping
NTD3055-094	DPAK	75 Units/Rail
NTD3055-094-1	DPAK Straight Lead	75 Units/Rail
NTD3055-094T4	DPAK	2500 Tape & Reel

# NTD3055-094

# **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = $25^{\circ}$ C unless otherwise noted)

	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS						
Drain–to–Source Breakdown Voltage (Note 3.) (V <sub>GS</sub> = 0 Vdc, I <sub>D</sub> = 250 μAdc) Temperature Coefficient (Positive)		V(BR)DSS	60 -	68 54.4		Vdc mV/°C
Zero Gate Voltage Drain Cur $(V_{DS} = 60 \text{ Vdc}, V_{GS} = 0 \text{ V}$ $(V_{DS} = 60 \text{ Vdc}, V_{GS} = 0 \text{ V}$	/dc)	IDSS			1.0 10	μAdc
Gate-Body Leakage Current	$(V_{GS} = \pm 20 \text{ Vdc}, V_{DS} = 0 \text{ Vdc})$	IGSS	-	-	±100	nAdc
ON CHARACTERISTICS (Not	e 3.)					
Gate Threshold Voltage (Not $(V_{DS} = V_{GS}, I_D = 250 \mu Ac$ Threshold Temperature Coef	ic)	VGS(th)	2.0	2.9 6.3	4.0	Vdc mV/°C
Static Drain-to-Source On-Resistance (Note 3.) (V <sub>GS</sub> = 10 Vdc, I <sub>D</sub> = 6.0 Adc)			_	84	94	mOhm
Static Drain-to-Source On-V (V <sub>GS</sub> = 10 Vdc, $I_D$ = 12 Ac (V <sub>GS</sub> = 10 Vdc, $I_D$ = 6.0 A	VDS(on)		0.85 0.77	1.35 -	Vdc	
Forward Transconductance (	9FS	_	6.7	_	mhos	
DYNAMIC CHARACTERISTIC	S					
Input Capacitance		C <sub>iss</sub>	-	323	450	pF
Output Capacitance	(V <sub>DS</sub> = 25 Vdc, V <sub>GS</sub> = 0 Vdc, f = 1.0 MHz)	C <sub>oss</sub>	_	107	150	
Transfer Capacitance	,	C <sub>rss</sub>	_	34	70	
SWITCHING CHARACTERIS	TICS (Note 4.)					
Turn–On Delay Time		<sup>t</sup> d(on)	_	7.7	15	ns
Rise Time	$(V_{DD} = 48 \text{ Vdc}, I_D = 12 \text{ Adc},$	tr	_	32.3	70	
Turn–Off Delay Time	$V_{GS} = 10 \text{ Vdc}, R_G = 9.1 \Omega$ (Note 3.)	<sup>t</sup> d(off)	-	25.2	50	
Fall Time		tf	-	23.9	50	
Gate Charge		QT	-	10.9	20 nC	nC
	(V <sub>DS</sub> = 48 Vdc, I <sub>D</sub> = 12 Adc, V <sub>GS</sub> = 10 Vdc) (Note 3.)	Q <sub>1</sub>	-	3.1	-	_
		Q <sub>2</sub>	-	4.2	-	
SOURCE-DRAIN DIODE CHA	RACTERISTICS					
Forward On–Voltage	$(I_{S} = 12 \text{ Adc}, V_{GS} = 0 \text{ Vdc}) \text{ (Note 3.)}$ $(I_{S} = 12 \text{ Adc}, V_{GS} = 0 \text{ Vdc}, T_{J} = 150^{\circ}\text{C})$	V <sub>SD</sub>		0.94 0.82	1.15 -	Vdc
Reverse Recovery Time		t <sub>rr</sub>	_	33.1	_	ns
	(I <sub>S</sub> = 12 Adc, V <sub>GS</sub> = 0 Vdc, dI <sub>S</sub> /dt = 100 A/µs) (Note 3.)	<sup>t</sup> a	-	24	-	1
		t <sub>b</sub>	-	8.9	-	
	•		1	1	1	1

Reverse Recovery Stored Charge

Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.
Switching characteristics are independent of operating junction temperatures.

0.047

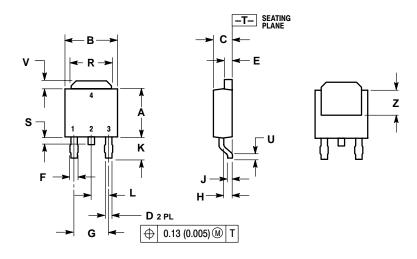
 $Q_{RR}$ 

\_

μC

# PACKAGE DIMENSIONS

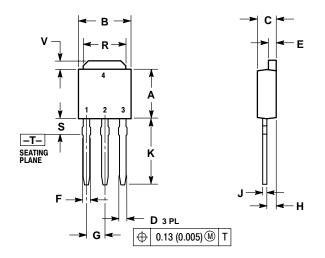
DPAK CASE 369A–13 ISSUE AA



Y14.5	M, 1982.	AND TO		
	INC	HES	MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.235	0.250	5.97	6.35
В	0.250	0.265	6.35	6.73
С	0.086	0.094	2.19	2.38
D	0.027	0.035	0.69	0.88
Е	0.033	0.040	0.84	1.01
F	0.037	0.047	0.94	1.19
G	0.180			BSC
Н	0.034	0.040	0.87	1.01
J	0.018	0.023	0.46	0.58
Κ	0.102	0.114	2.60	2.89
L	0.090 BSC		2.29 BSC	
R	0.175	0.215	4.45	5.46
S	0.020	0.050	0.51	1.27
U	0.020		0.51	
۷	0.030	0.050	0.77	1.27
Ζ	0.138		3.51	

STYLE 2: PIN 1. GATE 2. DRAIN 3. SOURCE 4. DRAIN

DPAK CASE 369–07 ISSUE M



NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

2.	CONTROLLING DIMENSION: INCH.

	INC	HES	MILLIN	IETERS		
DIM	MIN	MAX	MIN	MAX		
Α	0.235	0.250	5.97	6.35		
в	0.250	0.265	6.35	6.73		
С	0.086	0.094	2.19	2.38		
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Е	0.033	0.040	0.84	1.01		
F	0.037	0.047	0.94	1.19		
G	0.090	90 BSC 2.29		0.090 BSC		BSC
Η	0.034	0.040	0.87 1.0			
ſ	0.018	0.023	0.46	0.58		
Κ	0.350	0.380	8.89	9.65		
R	0.175	0.215	4.45	5.46		
s	0.050	0.090	1.27	2.28		
٧	0.030	0.050	0.77	1.27		

STYLE 2: PIN 1. GATE 2. DRAIN 3. SOURCE 4. DRAIN

## NTD3055-094

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