

High-side Switch

Features

- High-side switch. (Inverter input)
- Enable to 25 voltage operation.
- · Low power-loss.
- · Pch MOSFET open drain output.

Truth Table

IN	OUT
L	Z
Н	Н

H: High level voltage

L : Low level voltage

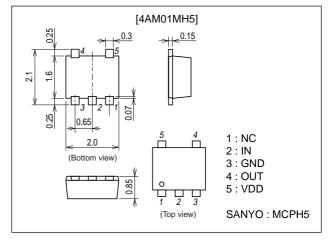
Z: High impedance

Specifications

Absolute Maximum Ratings at Ta=25°C

Package Dimensions

unit : mm 2217



Parameter	Symbol	Conditions	Ratings	Unit
DC Supply Voltage	V _{DD}		-0.3 to +25	V
Input Voltage	VIN		-0.3 to VDD+0.3	V
Output Voltage	Vout		V _{DD} -25 to V _{DD} +0.3	V
Input Current	IIN		±10	mA
Output Current	lout		75	mA
Allowable Power Dissipation	PD	Mounted on a ceramic board (600mm²X0.8mm)	0.8	W
Storage Temperature	Tstg		-55 to +150	°C

Recommended Operating Conditions at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
DC Supply Voltage	V_{DD}		3 to +25	V
Input Voltage	VIN		0 to V _{DD}	V
Output Voltage	Vout		$V_{\mbox{DD}}$ -25 to $V_{\mbox{DD}}$	V
Input Rise And Fall Time	Δt / Δv	V _{DD} <5V	≤100	ns / V
		V _{DD} ≥5V	20	ns / V
Operating Temperature	Topr		-40 to +85	°C

Marking : XG

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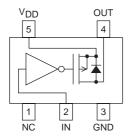
DC Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
Parameter			min	typ	max	Unit
High level lagut Veltage	VIH	V _{DD} =5V	3.5			V
		V _{DD} =10V	7.0			V
High-level Input Voltage		V _{DD} =15V	11.0			V
		V _{DD} =25V	18.0			V
		V _{DD} =5V			1.5	V
Low level Input Voltage	\/	V _{DD} =10V			3.0	V
Low-level Input Voltage	VIL	V _{DD} =15V			4.0	V
		V _{DD} =25V			7.0	V
Input Leakage Current	lini	V _{DD} =25V, V _{IN} =25V			0.1	μΑ
Input Leakage Current	IN	V _{DD} =25V, V _{IN} =0			0.1	μΑ
		V _{DD} =5V, I _O =1mA	4.5			V
	\/a	V _{DD} =10V, I _O =2.5mA	9.0			V
	VOH	V _{DD} =15V, I _O =5.0mA	13.5			٧
		V _{DD} =25V, I _O =10mA	22.5			V
Supply Current	I _{DD}	V _{DD} =5V, V _{IN} =V _{DD} , GND			0.25	μΑ
		V _{DD} =10V, V _{IN} =V _{DD} , GND			0.5	μΑ
		V _{DD} =15V, V _{IN} =V _{DD} , GND			1.0	μΑ
		V _{DD} =25V, V _{IN} =V _{DD} , GND			2.0	μΑ

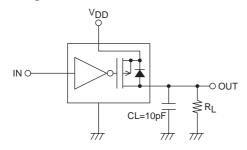
AC Characteristics at Ta=25°C

Parameter	Symbol	Conditions		Ratings		
	Symbol		min	typ	max	Unit
Propagation Delay Time tPZH tPZH		V _{DD} =5V, R _L =1.5kΩ		40		ns
	to	V_{DD} =10V, R_L =1k Ω		20		ns
	I IPZH	V _{DD} =15V, R _L =750Ω		15		ns
		V _{DD} =25V, R _L =500Ω		12		ns
		V _{DD} =5V, R _L =1.5kΩ		30		ns
	+	V_{DD} =10V, R_L =1k Ω		20		ns
	PHZ	V _{DD} =15V, R _L =750Ω		15		ns
		V _{DD} =25V, R _L =500Ω		12		ns
Input Capacitance	CIN			8		pF

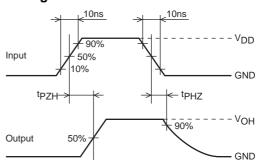
Block Diagram

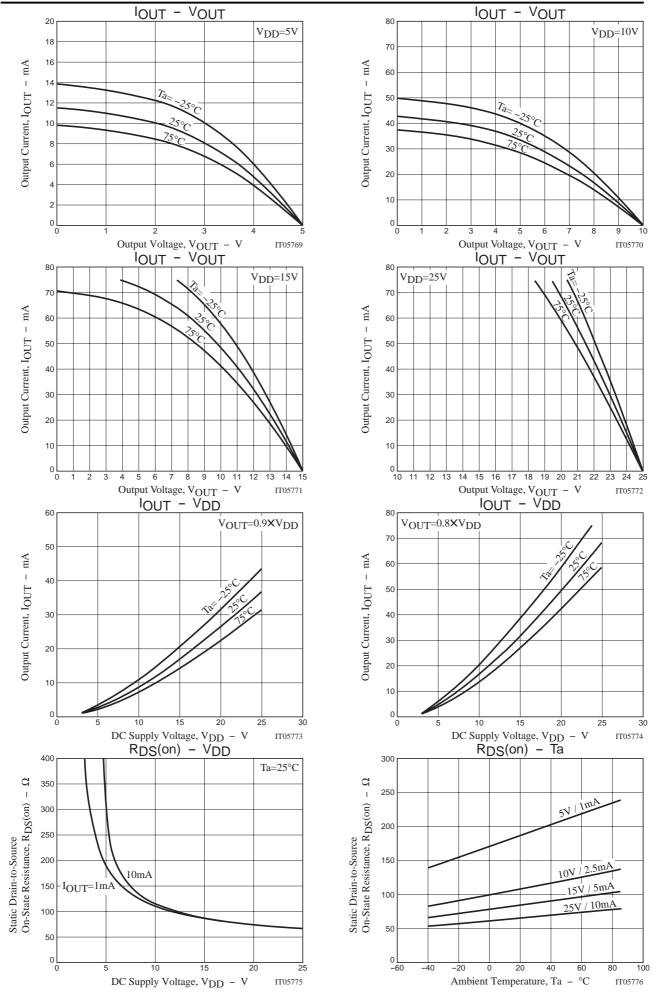


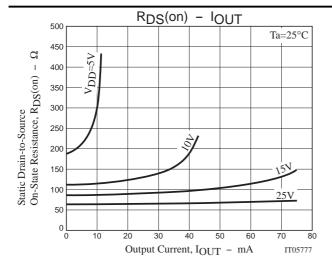
Switching Time Test Circuit

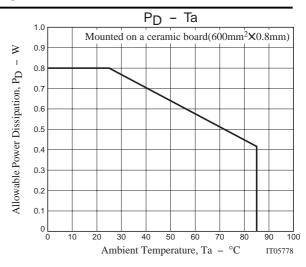


Switching Time Wave Form









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