







DUAL 20V NPN LOW SATURATION SWITCHING TRANSISTOR

Features

- V_{CEO} = 20V
- R_{SAT} = 47mΩ
- I_C = 4.5A Continuous Collector Current
- Low Equivalent On Resistance
- Low Saturation Voltage (150mV @ 1A)
- h_{FE} specified up to 6A
- Lead, Halogen, and Antimony Free/RoHS Compliant (Note 1)
- "Green" Devices (Note 2)

Applications

- DC DC Converters (FET Drivers)
- · Charging circuits
- Power switches
- Motor control

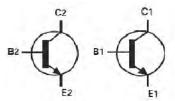
Mechanical Data

- Case: DFN3020B-3
- Case material: Molded Plastic. "Green" Molding Compound.
- Terminals: Pre-Plated NiPdAu leadframe.
- UL Flammability Rating 94V-0
- Nominal package height: 0.8mm
- Moisture Sensitivity: Level 1 per J-STD-020
- Weight: 0.013 grams (approximate)

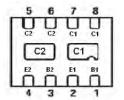
DFN3020B-8



Top View



Device symbol



Pin Configuration

Ordering Information

Product	Status	Package	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXTD618MCTA	Active	DFN3020B-8	DBB	7	8	3000

Notes:

- 1. No purposefully added lead. Halogen and Antimony Free.
- 2. Diodes Inc's "Green" Policy can be found on our website at http://www.diodes.com

Marking Information



DBB = Product type Marking Code
Dot Denotes Pin 1





Maximum Ratings

Parameter	Symbol	Limit	Unit
Collector-Base Voltage	V _{CBO}	40	V
Collector-Emitter Voltage	V _{CEO}	20	V
Emitter-Base Voltage	V _{EBO}	7.5	V
Peak Pulse Current	I _{CM}	12	Α
Continuous Collector Current (Notes a and f)	I _C	4.5	Α
Continuous Collector Current (Notes b and f)	Ic	5.0	Α
Base Current	I _B	5	А

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation at T _A = 25°C (Notes a and f) Linear Derating Factor	P _D	1.5 12	W mW/°C
Power Dissipation at T_A = 25°C (Notes b and f) Linear Derating Factor	P _D	2.45 19.6	W mW/°C
Power Dissipation at T_A = 25°C (Notes c and f) Linear Derating Factor	P _D	1 8	W mW/°C
Power Dissipation at T_A = 25°C (Notes d and f) Linear Derating Factor	P _D	1.13 9	W mW/°C
Power Dissipation at $T_A = 25^{\circ}C$ (Notes d and g) Linear Derating Factor	P _D	1.7 13.6	W mW/°C
Power Dissipation at $T_A = 25^{\circ}C$ (Notes e and g) Linear Derating Factor	P _D	3 24	W mW/°C
Junction to Ambient (Notes a and f)	$R_{ hetaJA}$	83.3	°C/W
Junction to Ambient (Notes b and f)	$R_{ hetaJA}$	51	°C/W
Junction to Ambient (Notes c and f)	$R_{ hetaJA}$	125	°C/W
Junction to Ambient (Notes d and f)	$R_{ hetaJA}$	111	°C/W
Junction to Ambient (Notes d and g)	$R_{ heta JA}$	73.5	°C/W
Junction to Ambient (Notes e and g)	$R_{ heta JA}$	41.7	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Notes:

- a. For a dual device surface mounted on 8 sq cm single sided 2 oz copper on FR4 PCB, in still air conditions with all exposed pads attached. The copper area is split down the centre line into two separate areas with one half connected to each half of the dual device.
- b. Measured at t <5 secs for a dual device surface mounted on 8 sq cm single sided 2 oz copper on FR4 PCB, in still air conditions with all exposed pads attached. The copper area is split down the centre line into two separate areas with one half connected to each half of the dual device.
- c. For a dual device surface mounted on 8 sq cm single sided 2 oz copper on FR4 PCB, in still air conditions with minimal lead connections only.

 d. For a dual device surface mounted on 10 sq cm single sided 1 oz copper on FR4 PCB, in still air conditions with minimal lead connections only.

 d. For a dual device surface mounted on 10 sq cm single sided 1 oz copper on FR4 PCB, in still air conditions with minimal lead connections only.

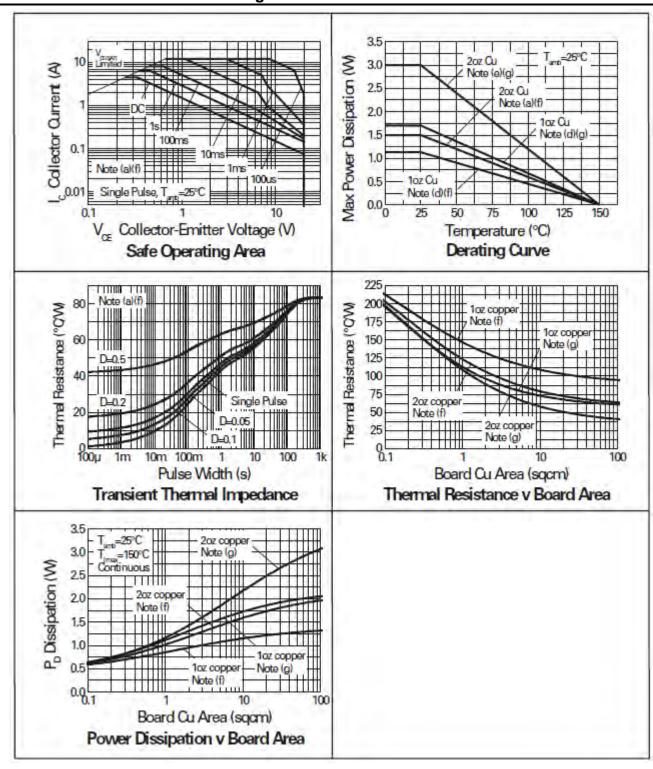
 d. For a dual device surface mounted on 10 sq cm single sided 1 oz copper on FR4 PCB, in still air conditions with minimal lead connections only.

 d. For a dual device surface mounted on 10 sq cm single sided 1 oz copper on FR4 PCB, in still air conditions with minimal lead connections only.
- e. For a dual device surface mounted on 85 sq cm single sided 2 oz copper on FR4 PCB, in still air conditions with all exposed pads attached. The copper area is split down the centre line into two separate areas with one half connected to each half of the dual device.
- f. For a dual device with one active die.
- g. For dual device with 2 active die running at equal power.





Thermal Characteristics and Derating information







Electrical Characteristics @TA = 25°C unless otherwise specified

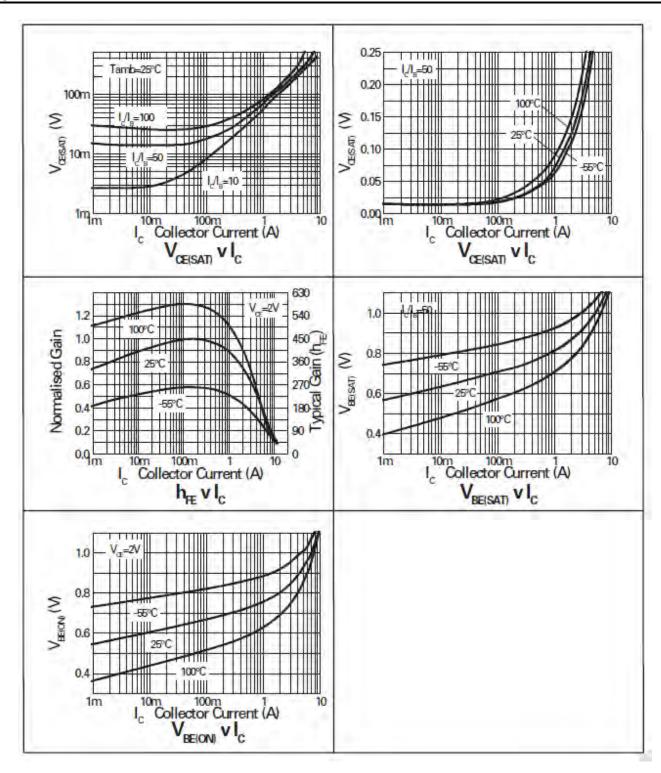
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	$V_{(BR)CBO}$	40	100	-	V	I _C = 100μA
Collector-Emitter Breakdown Voltage (Note 3)	$V_{(BR)CEO}$	20	27	-	V	I _C = 10mA
Emitter-Base Breakdown Voltage	V _{(BR)EBO}	7.5	8.2	-	V	I _E = 100μA
Collector Cutoff Current	I _{CBO}	-	-	25	nA	V _{CB} = 32V
Emitter Cutoff Current	I _{EBO}	-	-	25	. nA	V _{EB} = 6V
Collector Emitter Cutoff Current	I _{CES}	-	-	25	nA	V _{CES} = 16V
Static Forward Current Transfer Ratio (Note 3)	h _{FE}	200 300 200 100	400 450 360 180	- - -	-	$I_{C} = 10$ mA, $V_{CE} = 2$ V $I_{C} = 200$ mA, $V_{CE} = 2$ V $I_{C} = 2$ A, $V_{CE} = 2$ V $I_{C} = 6$ A, $V_{CE} = 2$ V
Collector-Emitter Saturation Voltage (Note 3)	V _{CE} (sat)	- - - -	8 90 115 190 210	15 150 135 250 270	mV	$I_C = 0.1A, I_B = 10mA$ $I_C = 1A, I_B = 10mA$ $I_C = 2A, I_B = 50mA$ $I_C = 3A, I_B = 100mA$ $I_C = 4.5A, I_B = 125mA$
Base-Emitter Turn-On Voltage (Note 3)	$V_{BE(on)}$	-	0.88	0.95	V	I _C = 4.5A, V _{CE} = 2V
Base-Emitter Saturation Voltage (Note 3)	V _{BE(sat)}	-	0.98	1.05	V	$I_C = 4.5A$, $I_B = 125mA$
Output Capacitance	C _{obo}	-	23	30	pF	V _{CB} = 10V. f = 1MHz
Transition Frequency	f _T	100	140	-	MHz	V _{CE} = 10V, I _C = 50mA, f = 100MHz
Turn-on Time	t _{on}	-	170	-	ns	V _{CC} = 10V, I _C = 3A
Turn-off Time	t _{off}	-	400	-	ns	$I_{B1} = I_{B2} = 10 \text{mA}$

Notes: 3. Measured under pulsed conditions. Pulse width = 300 μ s. Duty cycle \leq 2%





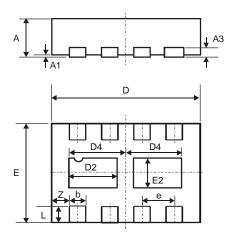
Typical Characteristics





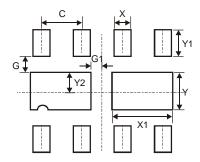


Package Outline Dimensions



DFN3020B-8						
Dim	Min	Max	Тур			
Α	0.77	0.83	0.80			
A1	0	0.05	0.02			
A3	1	-	0.15			
b	0.25	0.35	0.30			
D	2.95	3.075	3.00			
D2	0.82	1.02	0.92			
D4	1.01	1.21	1.11			
е	-	-	0.65			
Е	1.95	2.075	2.00			
E2	0.43	0.63	0.53			
L	0.25	0.35	0.30			
Z	-	-	0.375			
All Dimensions in mm						

Suggested Pad Layout



Dimensions	Value (in mm)
С	0.650
G	0.285
G1	0.090
Х	0.400
X1	1.120
Y	0.730
Y1	0.500
٧2	0.365





IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
 - 1. are intended to implant into the body, or
 - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices-or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2009, Diodes Incorporated

www.diodes.com