

**Vishay Siliconix** 

# P-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ.)		
- 20	0.184 at V <sub>GS</sub> = - 4.5 V	- 0.94	4.23		
	0.268 at V <sub>GS</sub> = - 2.5 V	- 0.78	4.23		

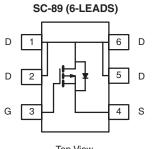
### **FEATURES**

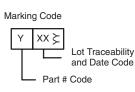
- Halogen-free Option Available
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % Rg Tested

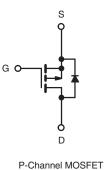


#### **APPLICATIONS**

· Load Switch for Portable Devices







Ordering Information: Si1069X-T1-E3 (Lead (Pb)-free) Si1069X-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS	T <sub>A</sub> = 25 °C, unle	ss otherwise n	oted		
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	- 20	- v	
Gate-Source Voltage		V <sub>GS</sub>	± 12		
Continuous Drain Current (T <sub>1</sub> = 150 °C)	T <sub>A</sub> = 25 °C	I_	- 0.94 <sup>b, c</sup>		
Continuous Drain Current (1) = 150 °C)	T <sub>A</sub> = 70 °C	D ID	- 0.75 <sup>b, c</sup>	A	
Pulsed Drain Current		I <sub>DM</sub>	- 8		
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	۱ <sub>S</sub>	- 0.2 <sup>b, c</sup>	7	
Mauinum Davier Dissingtional	T <sub>A</sub> = 25 °C	P <sub>D</sub>	0.236 <sup>b, c</sup>	w	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C		0.151 <sup>b, c</sup>	~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	

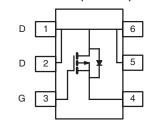
THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum lunction to Ambientic	t ≤ 5 s	R <sub>thJA</sub>	440	530	°C/W
Maximum Junction-to-Ambient <sup>a, b</sup>	Steady State	<sup>1</sup> 'thJA	540	650	C/ W

Notes:

a. Based on T<sub>A</sub> = 25 °C.

b. Surface Mounted on 1" x 1" FR4 board.

c. t = 5 s.



Top View

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<b>SPECIFICATIONS</b> $T_J = 25 \circ C$	C, unless oth	erwise noted				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						•
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = - 250 μA	- 20			V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = - 250 μA		- 16.7		
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	η <sub>D</sub> = - 250 μΑ		2.95		mV/°C
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 0.6		- 1.5	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 12 V$			± 100	nA
Zara Cata Valtaga Drain Current	<b> </b>	$V_{DS} = -20 V, V_{GS} = 0 V$			- 1	nA
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = -20 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 85 ^{\circ}\text{C}$			- 10	μΑ
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = \ge 5 \text{ V}, \text{ V}_{GS} = -4.5 \text{ V}$	- 8			Α
Drain-Source On-State Resistance <sup>a</sup>	P	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 0.94 A		0.153	0.184	
	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 0.78 A		0.218	0.268	Ω
Forward Transconductance	9 <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 0.94 A		4		S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>			308		pF
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = - 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz		78		
Reverse Transfer Capacitance	C <sub>rss</sub>			59		
Total Cata Charge	0	$V_{DS} = -10 \text{ V}, \text{ V}_{GS} = -5 \text{ V}, \text{ I}_{D} = -0.94 \text{ A}$		4.57	6.86	nC
Total Gate Charge	Qg			4.23	6.35	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -0.94 \text{ A}$		0.71		
Gate-Drain Charge	Q <sub>gd</sub>			1.67		
Gate Resistance	Rg	f = 1 MHz		9	13.5	Ω
Turn-On Delay Time	t <sub>d(on)</sub>			19	28.5	
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 10 V, $R_L$ = 13.3 $\Omega$		31	47	- ns
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D \cong$ - 0.75 A, $V_{GEN}$ = - 4.5 V, $R_g$ = 1 $\Omega$		23	34.5	
Fall Time	t <sub>f</sub>			7	10.5	
Drain-Source Body Diode Characteria	stics					•
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				8	А
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 0.64 A		- 0.8	- 1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>			19	28.5	nC
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = - 0.64 A, di/dt = 100 A/μs		6.65	10	ns
Reverse Recovery Fall Time	t <sub>a</sub>	$F = -0.04 \text{ A}, \text{ unut} = 100 \text{ A/}\mu\text{S}$		7		
Reverse Recovery Rise Time	t <sub>b</sub>	-1 -		12		1

Notes:

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

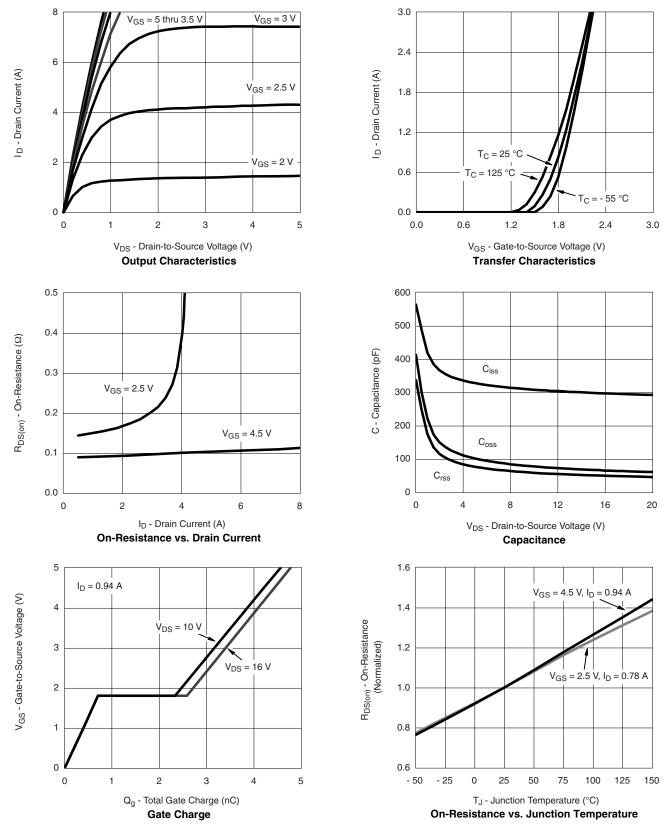
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



Si1069X Vishay Siliconix

### **TYPICAL CHARACTERISTICS** $T_A = 25$ °C, unless otherwise noted

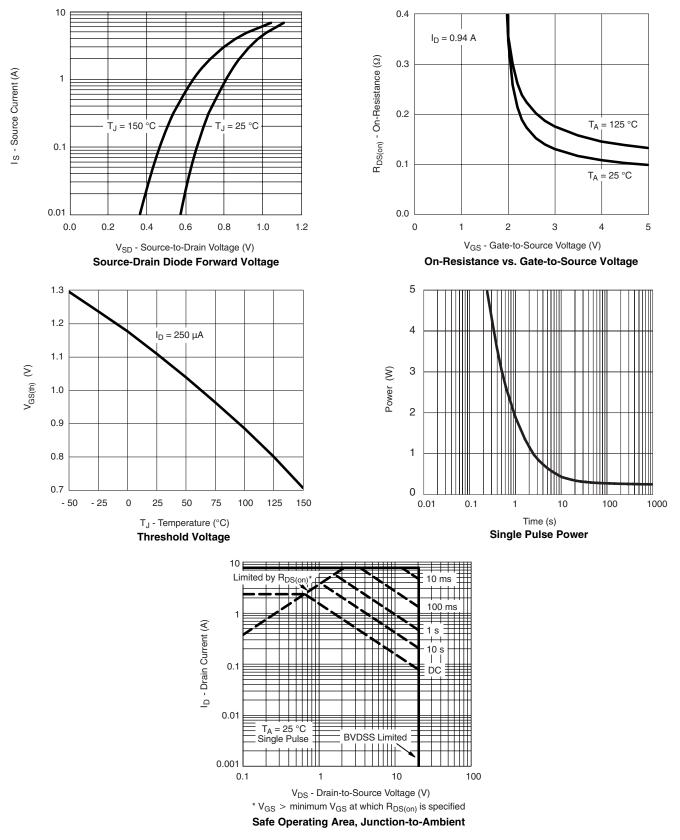


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# Si1069X

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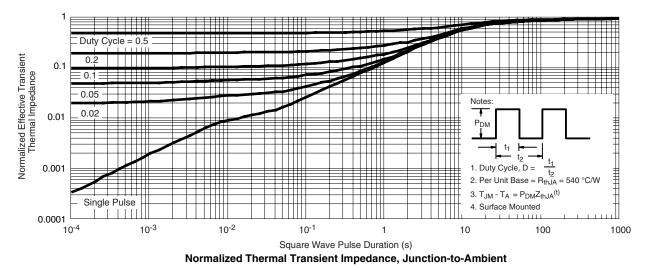
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