

Vishay Siliconix

N-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A) ^a	Q _g (Typ.)	
	0.420 at V _{GS} = 4.5 V	0.606		
20	0.501 at V _{GS} = 2.5 V	0.505	0.92	
	0.660 at V _{GS} = 1.8 V	0.15		

FEATURES

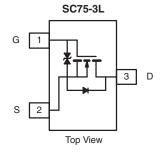
- Halogen-free Option Available
- TrenchFET[®] Power MOSFET: 1.8 V Rated
- ESD Protected: 2000 V

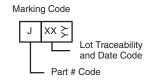


RoHS

APPLICATIONS

- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories
- Battery Operated Systems
- Power Supply Converter Circuits
- Load/Power Switching Cell Phones, Pagers





Ordering Information: Si1046R-T1-E3 (Lead (Pb)-free)

Si1046R-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS	$I_A = 25 ^{\circ}C$, unio	ess otherwise no	oted	
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V _{DS}	20	V
Gate-Source Voltage		V_{GS}	± 8	v
Cartinua Duais Commant /T 150 90\8	T _A = 25 °C		0.606 ^{b, c}	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C	I _D	0.485 ^{b, c}	A
Pulsed Drain Current		I _{DM}	2.5	^
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	0.21 ^{b, c}	
Mariana Barra Birainatian	T _A = 25 °C	P _D	0.25 ^{b, c}	W
Maximum Power Dissipation ^a	T _A = 70 °C	L D	0.16 ^{b, c}	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Marrian de Ambiento	t ≤ 5 s	R_{thJA}	440	530	°C/W	
Maximum Junction-to-Ambient ^{b, d}	Steady State		540	650	0/ 00	

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- a. Based on $T_C = 25$ °C.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 5 s.
- d. Maximum under steady state conditions is 650 °C/W.

Si1046R

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static				•			
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	20			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	J 050 A		20.5		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 2.12			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	0.35		0.95	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 30	mA	
Zana Oata Wallana Burin Oana i	I _{DSS}	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$			1	μΑ	
Zero Gate Voltage Drain Current		V _{DS} = 20 V, V _{GS} = 0 V, T _J = 85 °C			10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = \ge 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	2.5			Α	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 0.606 A		0.336	0.420	Ω	
		V _{GS} = 2.5 V, I _D = 0.505 A		0.395	0.501		
	, ,	V _{GS} = 1.8 V, I _D = 0.150 A		0.438	0.660		
Forward Transconductance	9 _{fs}	$V_{DS} = 10 \text{ V}, I_D = 0.606 \text{ A}$		2.1		S	
Dynamic ^b				•			
Input Capacitance	C _{iss}			66		pF	
Output Capacitance	C _{oss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		17			
Reverse Transfer Capacitance	C _{rss}			7			
Total Cata Chausa	Q _g	$V_{DS} = 10 \text{ V}, V_{GS} = 5 \text{ V}, I_D = 0.606 \text{ A}$		0.99	1.49	nC	
Total Gate Charge				0.92	1.38		
Gate-Source Charge	Q_{gs}	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 0.606 \text{ A}$		0.15			
Gate-Drain Charge	Q _{gd}			0.30			
Gate Resistance	R_{g}	f = 1 MHz		212		Ω	
Turn-On Delay Time	t _{d(on)}			17	26		
Rise Time	t _r	$V_{DD} = 10 \text{ V}, R_{L} = 20.8 \Omega$		19	28.5		
Turn-Off Delay Time	t _{d(off)}	$I_{D} \cong 0.48 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_{g} = 1 \Omega$		76	114	ns	
Fall Time	t _f			27	41		
Drain-Source Body Diode Characteristic	cs						
Pulse Diode Forward Current ^a	I _{SM}				2.5	Α	
Body Diode Voltage	V_{SD}	I _S = 0.48 A		8.0	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			16	24	nC	
Body Diode Reverse Recovery Charge	Q_{rr}	L = 1.0 A dl/dt = 100 A/::2		4.8	7.2	ns	
Reverse Recovery Fall Time	t _a	I _F = 1.0 A, dl/dt = 100 A/μs		12.3			
Reverse Recovery Rise Time	t _b	1		3.7			

Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

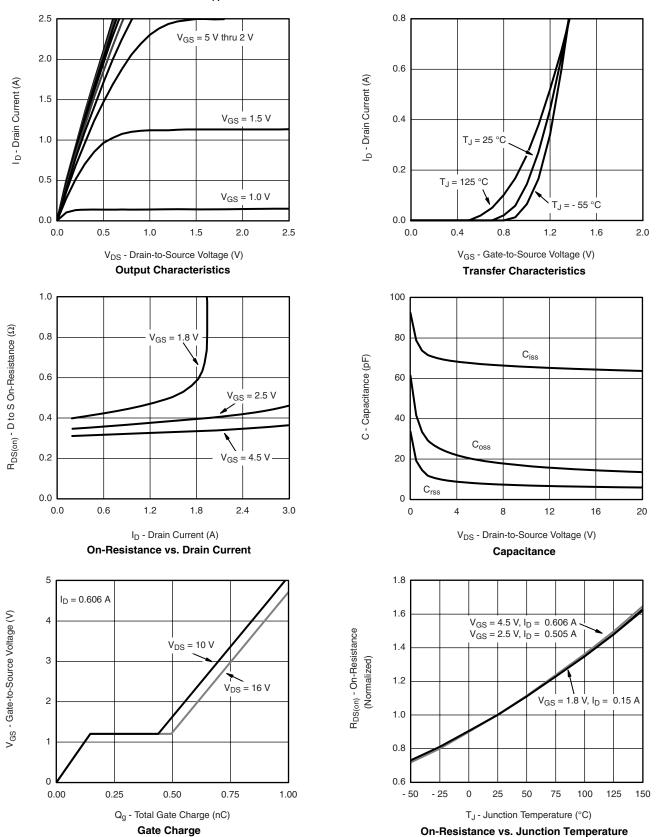
a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.



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TYPICAL CHARACTERISTICS $T_A = 25$ °C, unless otherwise noted

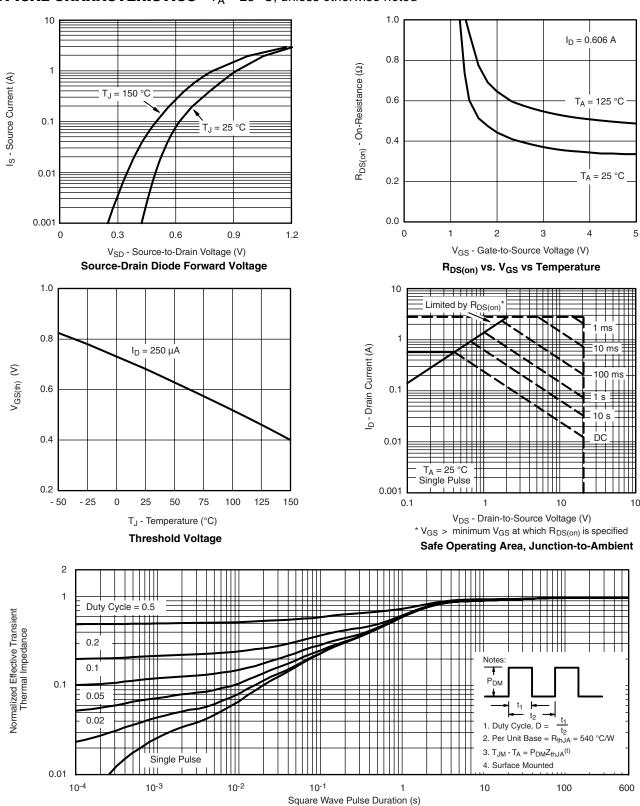


Si1046R

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TYPICAL CHARACTERISTICS $T_A = 25 \, ^{\circ}C$, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?74595.



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