

SMD Passive Delay Line

PCA Part Number : EPA3192

Electrical Specifications

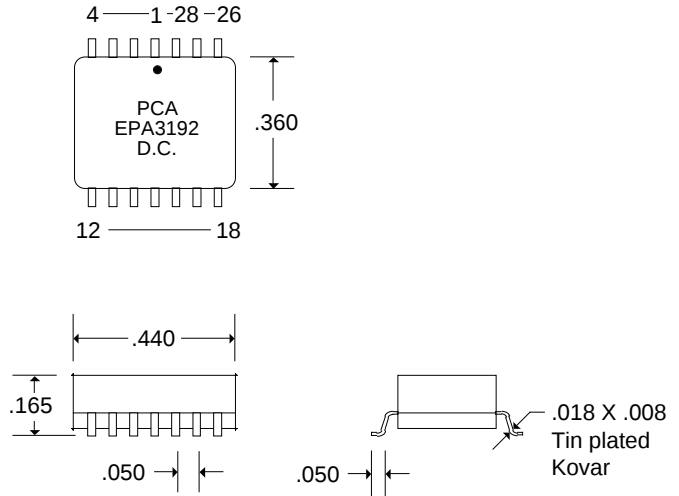
Impedance	100 W ± 10%
TR Output	2.6 nS Max.
TR True	-----
Tap Delay	1.3 nS ± 1 nS
Delay Time	13.0 nS ± 5%
Attenuation	0.8% Ref.
Distortion	± 10.0% Max.
D.C. Resistance	0.86 WRef.
I.R. @ 100 Vdc	1 KM WMin.
Dielectric Strength	100 Vdc
Temp. Coef. of Delay	100 PPM / °C
Operating Temp.	0 to 70°C

Test Conditions @ 25°C

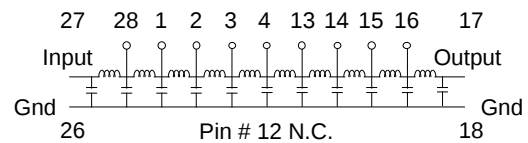
Pw	39.0 nS	Vcc
Prr	1.0 MHz	Temp
Ein	1.2 V	Load
Tri	2.0 nS	
Tfi		

*Unless otherwise specified, all tests to be conducted
@ 25°C and RH of 93% Max.

Package



Schematic



Notes :