

FEATURES

- 75 watts total output power
5V/3.3V, 5V/1.8V, 3.3V/2.5V, 3.3V/1.8V
- independently regulated outputs
- no minimum load
- under-voltage lockout
- efficiency up to 88%
- half-brick package
- tight line-load regulation

The HCD series is a high density, half-brick size, high current, dual output dc/dc converter with through-hole mounting. The series provides on-board conversion of standard telecom and datacom input voltages to two independently regulated output voltages.



SPECIFICATIONS: HCD SERIES

All specifications apply at 25° C ambient unless otherwise noted.

INPUT SPECIFICATIONS

Input Voltage Range.....36 - 75VDC
Input Filter.....L-C type

OUTPUT SPECIFICATIONS

Output Current.....see table
Voltage Accuracy.....+/- 1.5%
Line Regulation (HL-LL) @ full load.....+/- 0.2%
Load Regulation (10-100% load).....+/- 0.2%
Short Circuit Protection.....hiccup, auto recovery
Ripple/Noise (20MHz BW).....100 mV P-P
Transient Response (25% load step).....200 uS

GENERAL SPECIFICATIONS

Efficiency.....see table
Isolation Voltage (input to output).....1600 VDC
Isolation Resistance (input to output).....10⁹ ohms
Switching Frequency.....300 kHz

ENVIRONMENTAL SPECIFICATIONS

Operating Temperature (baseplate).....-40° to 100° C
Storage Temperature.....-55° to +125° C
Humidity (non-condensing).....5% to 95% RH
Cooling.....heat sink optional

PHYSICAL SPECIFICATIONS

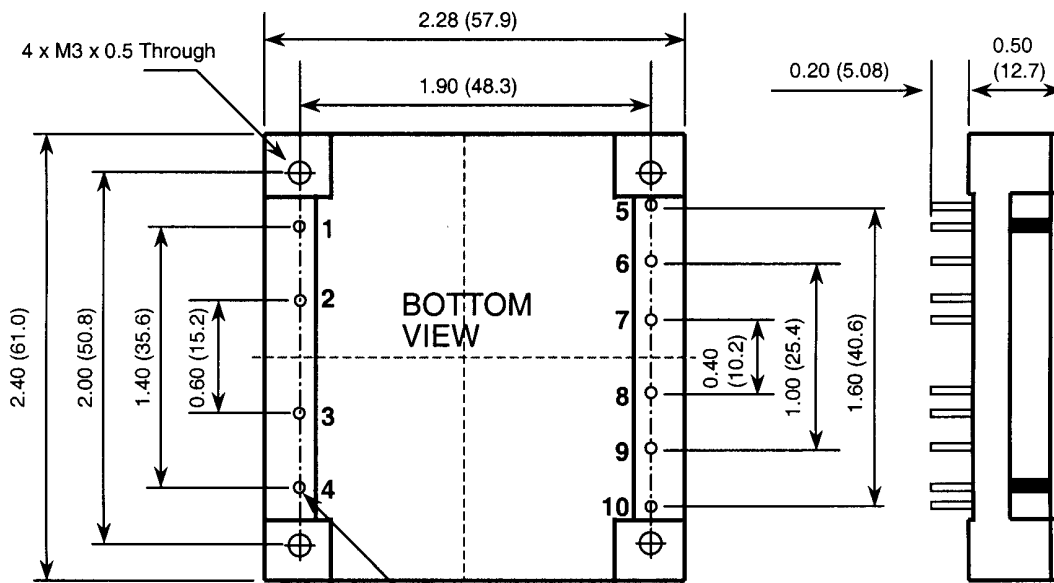
Dimensions.....2.40" x 2.28" x 0.50"
Weight.....1.5 oz
Case Material.....open frame

Due to advances in technology, specifications subject to change without notice.

Input Voltage (VDC)	Output Voltage (VDC)	Output Current (A)	Efficiency (%)	Model Number
36-75	5/3.3	15/15	88%	HCD48-533-75
	3.3/2.5	15/15	81%	HCD48-3325-75
	5/1.8	15/15	85%	HCD48-518-75
	3.3/1.8	15/15	81%	HCD48-3318-75

NOTES:

1. The on/off control function is positive logic (standard), or negative logic (optional). To order negative logic on/off, add the suffix "R".
2. The pin voltage is referenced to negative input.
3. Heat sink is optional, consult factory.
4. The efficiency test condition is at 7.5A on both outputs.



1. All dimensions in Inches (mm)
2. Pin pitch tolerance $\pm 0.014(0.35)$

PIN	Define	Diameter
1	- INPUT	0.04 Inches
2	CASE	0.04 Inches
3	CTRL	0.04 Inches
4	+ INPUT	0.04 Inches
5	+ V 2	0.04 Inches
6	- V 2 (COM)	0.04 Inches
7	V2 TRIM	0.04 Inches
8	+ V 1	0.04 Inches
9	- V 1 (COM)	0.04 Inches
10	V1 TRIM	0.04 Inches

