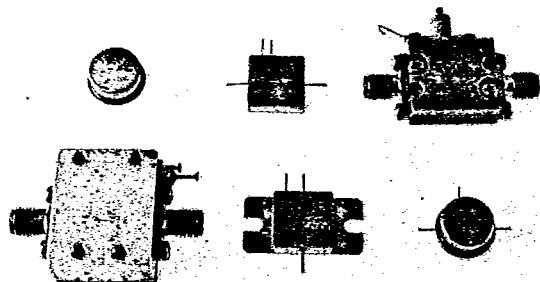


LH-2

B-74-09-51

5 to 500 MHz Cascadable Limiter



- Even Order Harmonic Suppression from Balanced Design
- Low Input VSWR in Limiting
- Voltage Variable (+ 5.0 to + 20.0V)
- Limiting Level: - 2 to + 2dBm
- Various Package Options (see photo)

Surface Mounted (SMTO-8), Flatpack with flange (FPF), Connectorized (CAH), Connectorized Flatpack (CFP), Flatpack (FP), and TO-8 (AH)

Electrical Specifications

Characteristic	Typical	Guaranteed	Maximum Ratings
Frequency (MHz):	5-500	5-500	Ambient Operating Temperature -54°C to +100°C
Insertion Loss Pin < -20.0dBm +15.0 < Bias < +20.0Vdc 5-200 MHz 200-500 MHz	+1.6dB +2.1dB	+2.0dB +2.5dB	Storage Temperature ... -62°C to +125°C
VSWR Pin < -10.0dBm +10.0 < Bias < +20.0Vdc	1.6:1	1.7:1	Maximum Case Temperature +100°C
Output VSWR Pin < +10.0dBm +10.0 < Bias < +20.0Vdc	1.6:1	2.0:1	Maximum DC Voltage +25.0V
Input Signal (Max.)		+26.0dBm	Maximum Continuous RF Input Power +200.0mW
Bias Current (mA) at +20.0Vdc at +15.0Vdc at +10.0Vdc	+9.0 +7.0 +4.5		Maximum Short Term RF Input Power +400.0mW (1 minute Max.)
			Maximum Peak Power +1.0W (3 μ seconds Max.)
			"X" Series Burn-In Temperature +125°C
			Weight +2.5 grams Max.

Limiting Characteristics (25°C)

Bias Voltage	Output Level at Limiting Threshold (1dB Comp.)		Max. Output Limiting Level (+20dBm Input)		Insertion Loss	
	Typ.	Max.	Typ.	Max.	Typ.	Max.
+20 volts	-0.5	+1.0	+2.1	+3.0	+1.7	+2.5
+15 volts	-1.4	+0.0	+1.2	+2.0	+1.8	+2.5
+10 volts	-3.8	-2.5	-1.9	+0.0	+2.2	+3.0
+5 volts	-12.0	-9.0	-6.0	—	+3.5	+4.5

Outline Drawings

(For additional package configurations, see Section 9)

Reference Outline Drawings for AH-1