

SIDC20D60C6

Fast switching diode chip in EMCON 3-Technology

FEATURES:

- 600V EMCON 3 technology 70 µm chip
- soft, fast switching
- low reverse recovery charge
- small temperature coefficient

This chip is used for:

- power module
- discrete components



Applications:

drives

Chip Type	V_R	I _F	Die Size	Package	Ordering Code
SIDC20D60C6	600V	75A	5.37 x 3.75 mm ²	sawn on foil	Q67050-A4353-
3.2 3232 333	0001	, 5, (0.07 X 0.7 0 111111	Jawii on ion	A101

MECHANICAL PARAMETER:

MESHANIOAE I ANAMETEK:					
Raster size	5.37 x 3.75				
Area total / active	20.14 / 16.66	mm ²			
Anode pad size	4.67 x 3.05				
Thickness	70	μm			
Wafer size	150	mm			
Flat position	180	deg			
Max. possible chips per wafer	701 pcs				
Passivation frontside	Photoimide				
Anode metallization 3200 nm AlSiCu					
Cathode metallization	Ni Ag -system suitable for epoxy and soft solder die bonding				
Die bond	electrically conductive glue or solder				
Wire bond	AI, ≤500μm				
Reject ink dot size	Ø 0.65mm; max 1.2mm				
Recommended storage environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C				



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Maximum Ratings

Parameter	Symbol	Condition	Value	Unit
Repetitive peak reverse voltage	V_{RRM}		600	٧
Continuous forward current limited by	1		1)	
T _{jmax}	I _F		·	
Single pulse forward current	I _{FSM}	$t_P = 10 \text{ ms sinusoidal}$	tbd	A
(depending on wire bond configuration)	1 F 2 IVI	tp = 10 me dinaddidai	100	
Maximum repetitive forward current			225	
limited by T _{jmax}	/ FRM		225	
Operating junction and storage temperature	$T_{\rm j}$, $T_{ m stg}$		-40+175	°C

¹⁾ depending on thermal properties of assembly

Static Electrical Characteristics (tested on chip), T_i =25 °C, unless otherwise specified

Parameter	Symbol	Condi	Value			Unit	
raiailletei	Syllibol	Condi	Conditions min.		Тур.	max.	Joint
Reverse leakage current	I_{R}	V _R =600V	<i>T_j</i> =25 °C			500	μΑ
Cathode-Anode breakdown Voltage	V _{Br}	I _R =0.25mA	<i>T_j</i> =25°C	600			V
Forward voltage drop	V_{F}	I _F =75A	<i>T_j</i> =25 °C	1.2	1.6	1.9	V

Dynamic Electrical Characteristics (verified by design/characterization), inductive load

 $T_{\rm j}$ = 25 °C, unless otherwise specified

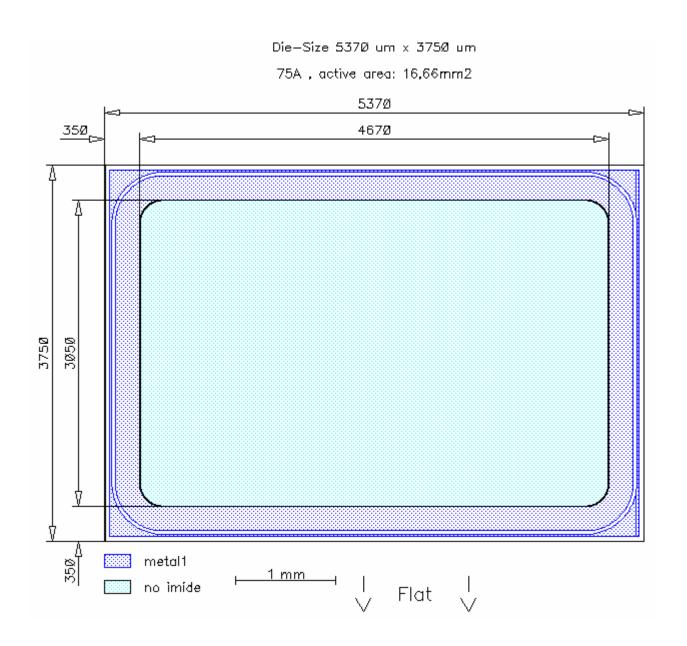
Parameter	Symbol Conditions		tion o	Value 2)			Unit	
raiailletei			tions	min.	Тур.	max.	7 0 1111	
Reverse recovery time	t _{rr1}	I _F =75A	$T_j = 25 ^{\circ}C$		tbd			
	t_{rr2}	di/dt=tbdA/ms $V_R=300V$	$T_j = 125 ^{\circ}\text{C}$		tbd		ns	
Peak recovery current	I _{RRM1}	I _F =75A	$T_j = 25 ^{\circ}\text{C}$		tbd		A	
	I _{RRM2}		$T_j = 125 {}^{\circ}\text{C}$		tbd			
Reverse recovery charge	Q _{rr1}	I _F =75A	T _j =25°C		tbd			
	Q _{rr2}	di/dt=tbdA/ms V _R =300V	T _j =125°C		tbd		μC	
Peak rate of fall of reverse recovery current	di _{rr1} /dt	I _F =75A	T _j =25°C		tbd		Λ /	
	di _{rr2} /dt	di/dt=tbdA/ms V _R =300V	T _j =125°C		tbd		A/μs	
Softness	S1	I _F =75A	<i>T_j</i> =25 °C		tbd			
	S2	di/dt=tbdA/ms V _R =300V	T _j =125°C		tbd	1	1	

 $^{^{2)}}$ values also influenced by parasitic L- and C- in measurement and package.





CHIP DRAWING:





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This chip data sheet refers to the device data sheet Description: AQL 0,65 for visual inspection according to failure catalog Electrostatic Discharge Sensitive Device according to MIL-STD 883 Test-Normen Villach/Prüffeld

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