

# SIDC02D60C6

# Fast switching diode chip in EMCON 3-Technology

## **FEATURES:**

- 600V EMCON 3 technology 70 µm chip
- soft, fast switching
- low reverse recovery charge
- small temperature coefficient

# This chip is used for:

- power module
- discrete components

# Applications:

- drives
- white goods
- resonant applications

Chip Type	$V_R$	I <sub>F</sub>	Die Size	Package	Ordering Code
SIDC02D60C6	600V	6A	1.4 x 1.65 mm <sup>2</sup>	sawn on foil	Q67050-A4349- A101

## **MECHANICAL PARAMETER:**

Raster size	1.4 x 1.65				
Area total / active	2.31 / 1.31	mm <sup>2</sup>			
Anode pad size	0.98 x 1.23				
Thickness	70	μm			
Wafer size	150	mm			
Flat position	180	deg			
Max. possible chips per wafer	6468 pcs				
Passivation frontside	Photoimide				
Anode metallization	3200 nm AlSiCu				
Cathode metallization	Ni Ag –system suitable for epoxy and soft solder die bonding				
Die bond	electrically conductive glue or solder				
Wire bond	AI, ≤500μm				
Reject ink dot size	Ø 0.65mm; max 1.2mm				
Recommended storage environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C				



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# **Maximum Ratings**

Parameter	Symbol	Condition	Value	Unit
Repetitive peak reverse voltage	$V_{RRM}$		600	٧
Continuous forward current limited by	1		1)	
T <sub>jmax</sub>	I <sub>F</sub>		·	
Single pulse forward current	I <sub>FSM</sub>	$t_P = 10 \text{ ms sinusoidal}$	tbd	A
(depending on wire bond configuration)	1 F S IVI	tp = 10 me omacordar	20	
Maximum repetitive forward current			18	
limited by T <sub>jmax</sub>	<b>/</b> FRM		10	
Operating junction and storage temperature	$T_{\rm j}$ , $T_{ m stg}$		-40+175	°C

<sup>1)</sup> depending on thermal properties of assembly

Static Electrical Characteristics (tested on chip),  $T_i$ =25 °C, unless otherwise specified

Parameter	Symbol	Conditions		Value			Unit
	Syllibol	Condi			max.		
Reverse leakage current	$I_{R}$	V <sub>R</sub> =600V	<i>T<sub>j</sub></i> =25 °C			60	μΑ
Cathode-Anode breakdown Voltage	V <sub>Br</sub>	I <sub>R</sub> =0.25mA	<i>T<sub>j</sub></i> =25°C	600			V
Forward voltage drop	$V_{F}$	I <sub>F</sub> =6A	<i>T<sub>j</sub></i> =25 °C	1.25	1.6	1.95	V

# Dynamic Electrical Characteristics (verified by design/characterization), inductive load

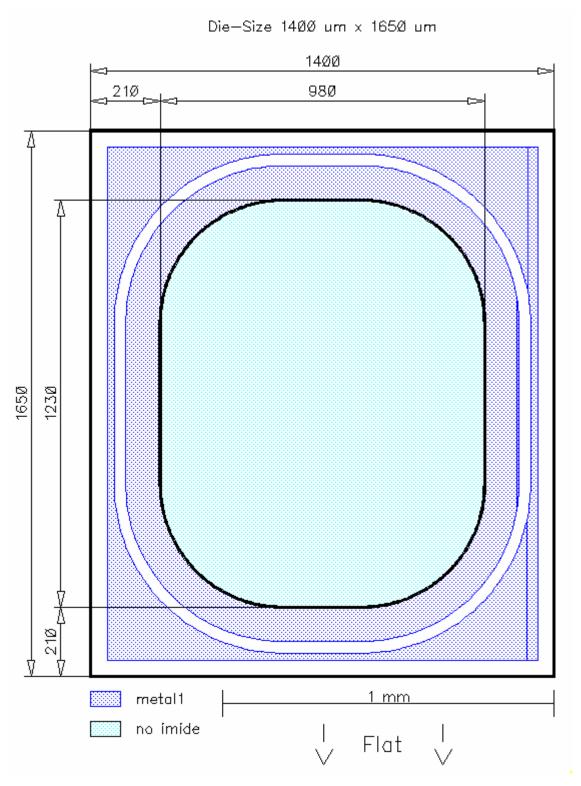
 $T_{\rm j}$  = 25 °C, unless otherwise specified

Parameter	Symbol Condition		itiono		Value 2)		Unit
Parameter			itions	min.	Тур.	max.	
Reverse recovery time	t <sub>rr1</sub>	$I_F=6A$	$T_j = 25  ^{\circ}C$		tbd		
	$t_{rr2}$	$di/dt=800 A/ms$ $V_R=300 V$	$T_j = 125  ^{\circ}\text{C}$		tbd		ns
Peak recovery current	I <sub>RRM1</sub>	I <sub>F</sub> =6A	$T_j = 25$ °C		11		_
	I <sub>RRM2</sub>	$di/dt=800 A/ms$ $V_R=300 V$	$T_j = 125  {}^{\circ}\text{C}$		11.5		A
Reverse recovery charge	Q <sub>rr1</sub>	I <sub>F</sub> =6A	T <sub>j</sub> =25°C		0.3		
	Q <sub>rr2</sub>	$di/dt=800 A/ms$ $V_R=300 V$	T <sub>j</sub> =125°C		0.5		μC
Peak rate of fall of reverse recovery current	di <sub>rr1</sub> /dt	I <sub>F</sub> =6A	T <sub>j</sub> =25°C		tbd		Δ /
	di <sub>rr2</sub> /dt	$di/dt=800 A/ms$ $V_R=300 V$	T <sub>j</sub> =125°C		tbd		- A/μs
Softness	S1	I <sub>F</sub> =6A	<i>T<sub>j</sub></i> =25 °C		tbd		
	S2	$di/dt=800 A/ms$ $V_R=300 V$	T <sub>j</sub> =125°C		tbd	1	1

<sup>&</sup>lt;sup>2)</sup> values also influenced by parasitic L- and C- in measurement and package.



# **CHIP DRAWING:**



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# FURTHER ELECTRICAL CHARACTERISTICS: This chip data sheet refers to the device data sheet Description: AQL 0,65 for visual inspection according to failure catalog Electrostatic Discharge Sensitive Device according to MIL-STD 883 Test-Normen Villach/Prüffeld

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