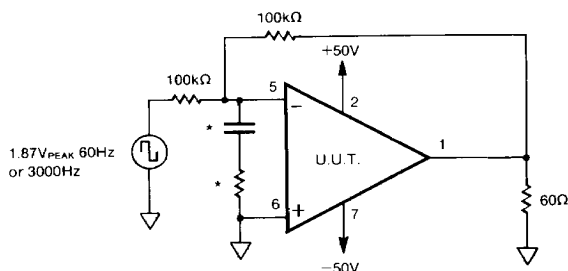


SG	PARAMETER	SYMBOL	TEMP	PWR	TEST CONDITIONS	MIN	MAX	UNITS
1	Quiescent Current	I_Q	25°C	±150V	$V_{IN} = 0, G = 100$		8.5	mA
1	Input Offset Voltage	V_{OS}	25°C	±150V	$V_{IN} = 0, G = 100$		3	mV
1	Input Offset Voltage	V_{OS}	25°C	±15V	$V_{IN} = 0, G = 100$		9.8	mV
1	Input Bias Current, +IN	$+I_B$	25°C	±150V	$V_{IN} = 0, G = 100$		50	pA
1	Input Bias Current, -IN	$-I_B$	25°C	±150V	$V_{IN} = 0, G = 100$		50	pA
1	Input Offset Current	I_{OS}	25°C	±150V	$V_{IN} = 0, G = 100$		50	pA
3	Quiescent Current	I_Q	-25°C	±150V	$V_{IN} = 0, G = 100$		10	mA
3	Input Offset Voltage	V_{OS}	-25°C	±150V	$V_{IN} = 0, G = 100$		5	mV
3	Input Offset Voltage	V_{OS}	-25°C	±15V	$V_{IN} = 0, G = 100$		10.8	mV
3	Input Bias Current, +IN	$+I_B$	-25°C	±150V	$V_{IN} = 0, G = 100$		50	pA
3	Input Bias Current, -IN	$-I_B$	-25°C	±150V	$V_{IN} = 0, G = 100$		50	pA
3	Input Offset Current	I_{OS}	-25°C	±150V	$V_{IN} = 0, G = 100$		50	pA
2	Quiescent Current	I_Q	85°C	±150V	$V_{IN} = 0, G = 100$		10	mA
2	Input Offset Voltage	V_{OS}	85°C	±150V	$V_{IN} = 0, G = 100$		5.5	mV
2	Input Offset Voltage	V_{OS}	85°C	±15V	$V_{IN} = 0, G = 100$		11.3	mV
2	Input Bias Current, +IN	$+I_B$	85°C	±150V	$V_{IN} = 0, G = 100$		10	nA
2	Input Bias Current, -IN	$-I_B$	85°C	±150V	$V_{IN} = 0, G = 100$		10	nA
2	Input Offset Current	I_{OS}	85°C	±150V	$V_{IN} = 0, G = 100$		10	nA
4	Output Voltage, $I_O = 15mA$	V_O	25°C	±80V	$R_L = 5K$	75		V
4	Current Limits	I_{CL}	25°C	±30V	$R_L = 100\Omega$	75	125	mA
4	Stability/Noise	E_N	25°C	±150V	$R_L = 5K, G = 1, C_L = 10nF$		1	mV
4	Slew Rate	SR	25°C	±150V	$R_L = 5K$	20	60	V/ μs
4	Open Loop Gain	A_{OL}	25°C	±150V	$R_L = 5K, f = 10Hz$	100		db
4	Common-mode Rejection	CMR	25°C	±30V	$R_L = 5K, f = DC, V_{CM} = \pm 20V$	82		db
6	Output Voltage, $I_O = 15mA$	V_O	-25°C	±80V	$R_L = 5K$	75		V
6	Stability/Noise	E_N	-25°C	±150V	$R_L = 5K, G = 1, C_L = 10nF$		1	mV
6	Slew Rate	SR	-25°C	±150V	$R_L = 5K$	20	60	V/ μs
6	Open Loop Gain	A_{OL}	-25°C	±150V	$R_L = 5K, f = 10Hz$	100		db
6	Common-mode Rejection	CMR	-25°C	±30V	$R_L = 5K, f = DC, V_{CM} = \pm 20V$	82		db
5	Output Voltage, $I_O = 15mA$	V_O	85°C	±80V	$R_L = 5K$	75		V
5	Stability/Noise	E_N	85°C	±150V	$R_L = 5K, G = 1, C_L = 10nF$		1	mV
5	Slew Rate	SR	85°C	±150V	$R_L = 5K$	20	60	V/ μs
5	Open Loop Gain	A_{OL}	85°C	±150V	$R_L = 5K, f = 10Hz$	100		db
5	Common-mode Rejection	CMR	85°C	±30V	$R_L = 5K, f = DC, V_{CM} = \pm 20V$	82		db

BURN IN CIRCUIT:



Internal power dissipation is approximately 2.1W at case temperature = 125°C.

*These components are used to stabilize device due to poor high frequency characteristics of burn in board.