

Silicon NPN Power Transistors

2SC3420

DESCRIPTION

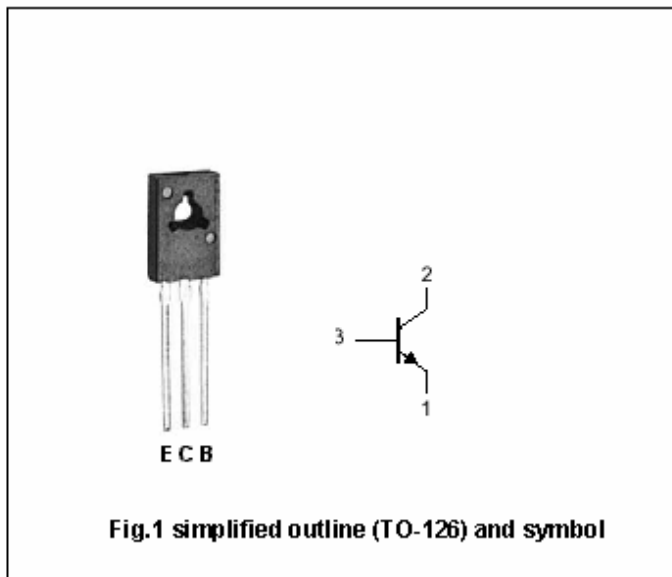
- With TO-126 package
- High DC current gain
- Low saturation voltage
- High collector power dissipation

APPLICATIONS

- Storo bo flash applications
- Medium power amplifier applications

PINNING

PIN	DESCRIPTION
1	Emitter
2	Collector;connected to mounting base
3	Base



Absolute maximum ratings(Ta=25)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	50	V
V_{CEO}	Collector- emitter voltage	Open base	20	V
V_{EBO}	Emitter-base voltage	Open collector	8	V
I_C	Collector current		5	A
I_{CM}	Collector current-peak		8	A
I_B	Base current		1	A
P_C	Collector power dissipation	$T_a=25$	1.5	W
		$T_C=25$	10	
T_j	Junction temperature		150	
T_{stg}	Storage temperature		-55 ~ +150	

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CHARACTERISTICS

T_j=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
I _{CBO}	Collector cutoff current	V _{CB} =40V; I _E =0			100	nA
I _{EBO}	Emitter cutoff current	V _{EB} =8V; I _C =0			100	nA
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =10mA ; I _B =0	20			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =4A ; I _B =0.1A			1	V
V _{BE}	Base-emitter voltage	I _C =4A ; V _{CE} =2V			1.5	V
h _{FE-1}	DC current gain	I _C =0.5A ; V _{CE} =2V	140		600	
h _{FE-2}	DC current gain	I _C =4A ; V _{CE} =2V	70			
C _{Ob}	Output capacitance	I _E =0; V _{CB} =10V; f=1MHz		40		pF
f _T	Transition frequency	I _C =0.5A ; V _{CE} =2V,		100		MHz

◆ h_{FE-1} Classifications

Y	GR	BL
140-240	200-400	300-600

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PACKAGE OUTLINE

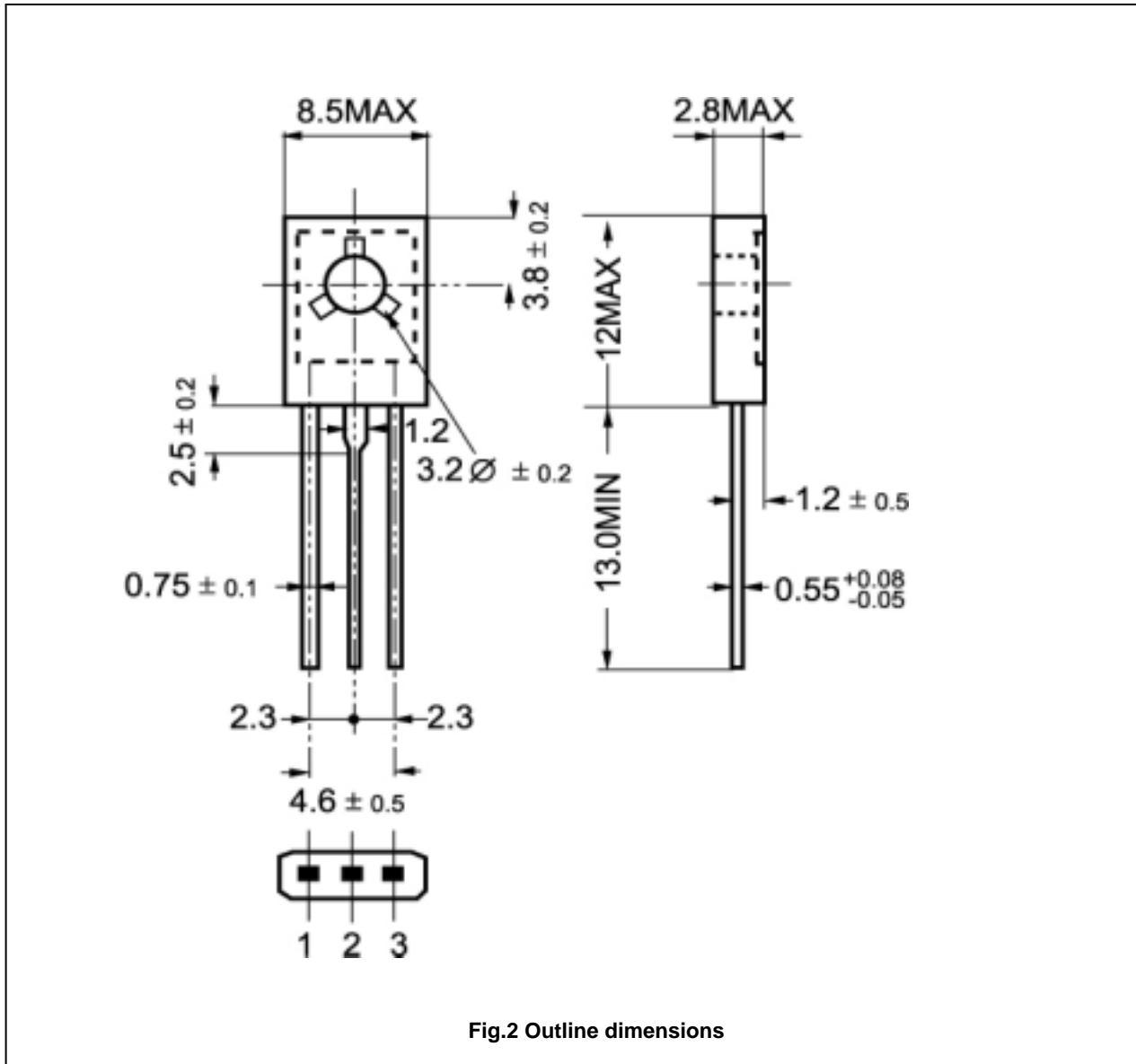


Fig.2 Outline dimensions

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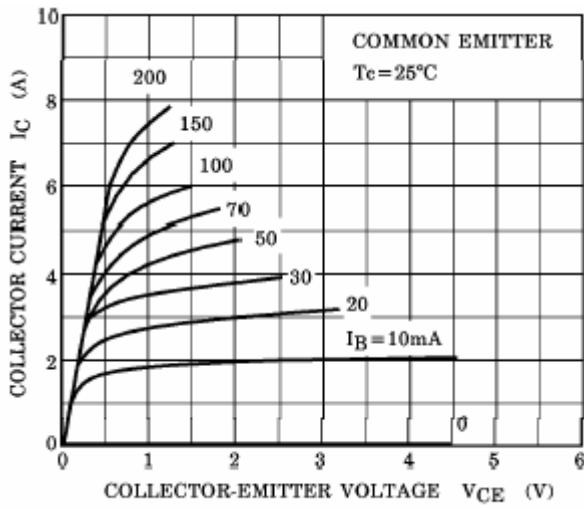


Fig.3 Static Characteristic

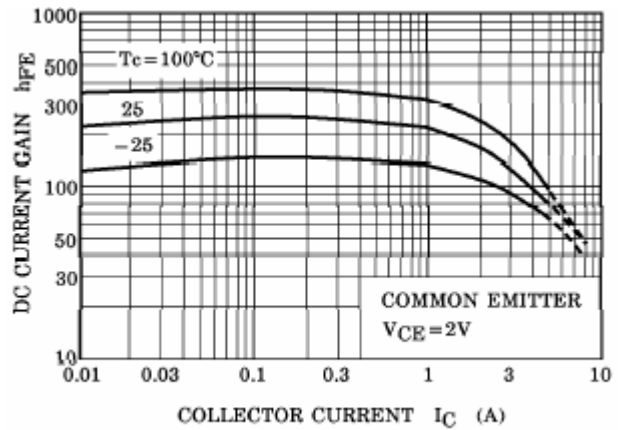


Fig.4 DC current Gain

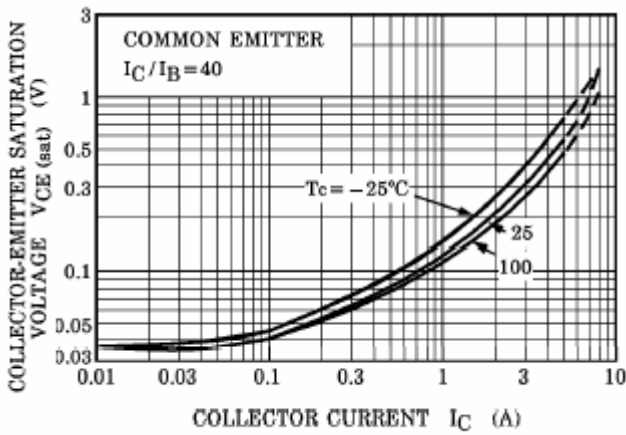


Fig.5 Collector-Emitter Saturation Voltage

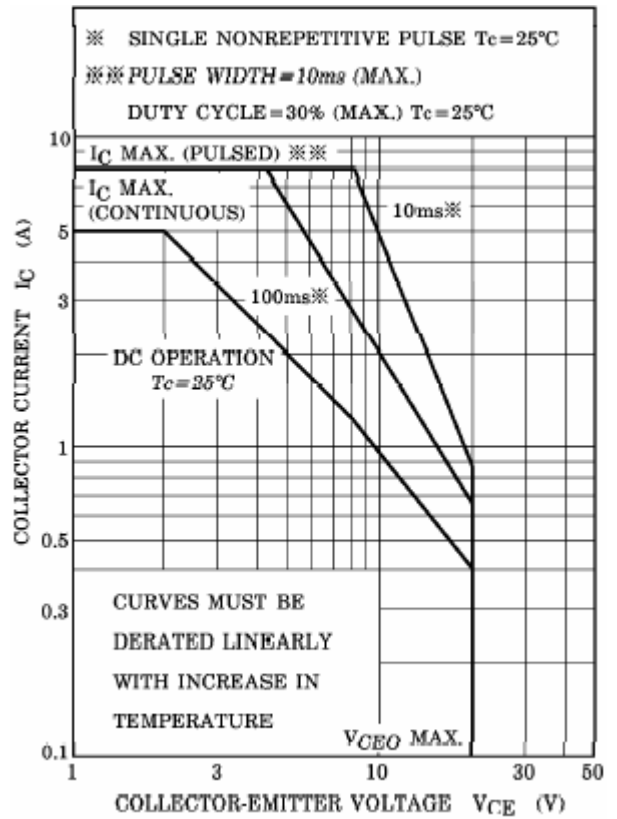


Fig.6 Safe Operating Area