

HIGH PERFORMANCE TRIACS

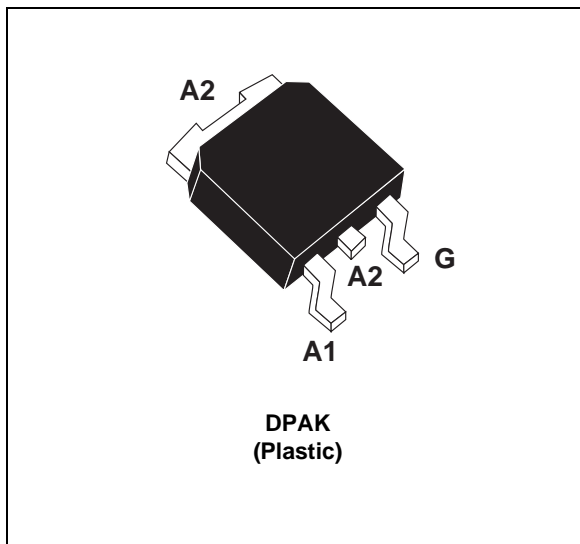
PRELIMINARY DATASHEET

FEATURES

- $I_{TRMS} = 4\text{ A}$
- $V_{DRM} = 400\text{ V to }800\text{ V}$
- $I_{GT} \leq 10\text{mA and }35\text{mA}$

DESCRIPTION

The T410/T435-B series of triacs uses a high performance TOPGLASS PNPN technology. The parts are intended for general purpose applications using mount technology.



ABSOLUT MAXIMUM RATINGS

Symbol	Parameter		Value	Unit
$I_T(RMS)$	RMS on-state current (360° conduction angle)	$T_c = 110\text{ °C}$	4	A
I_{TSM}	Non repetitive surge peak on-state current (T_j initial = 25°C)	$t_p = 8.3\text{ ms}$	35	A
		$t_p = 10\text{ ms}$	30	
I^2t	I^2t value for fusing	$t_p = 10\text{ ms}$	4.5	A ² s
di/dt	Critical rate of rise of on-state current $I_G = 500\text{mA}$ $di_G/dt = 1\text{A}/\mu\text{s}$	Repetitive $F = 50\text{ Hz}$	10	A/ μs
		Non Repetitive	50	
T_{stg} T_j	Storage temperature range Operating junction temperature range		- 40 to + 150 - 40 to + 125	°C °C
T_l	Maximum lead temperature for soldering during 10 s		260	°C

Symbol	Parameter	T410 or T435-				Unit
		400B	600B	700B	800B	
V_{DRM} V_{RRM}	Repetitive peak off-state voltage $T_j = 125\text{ °C}$	400	600	700	800	V

T410/T435-B

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
Rth (j-c)	Junction to case for DC	3.5	°C/W
Rth (j-c)	Junction to case for AC 360° conduction angle (F= 50 Hz)	2.6	°C/W

GATE CHARACTERISTICS (maximum values)

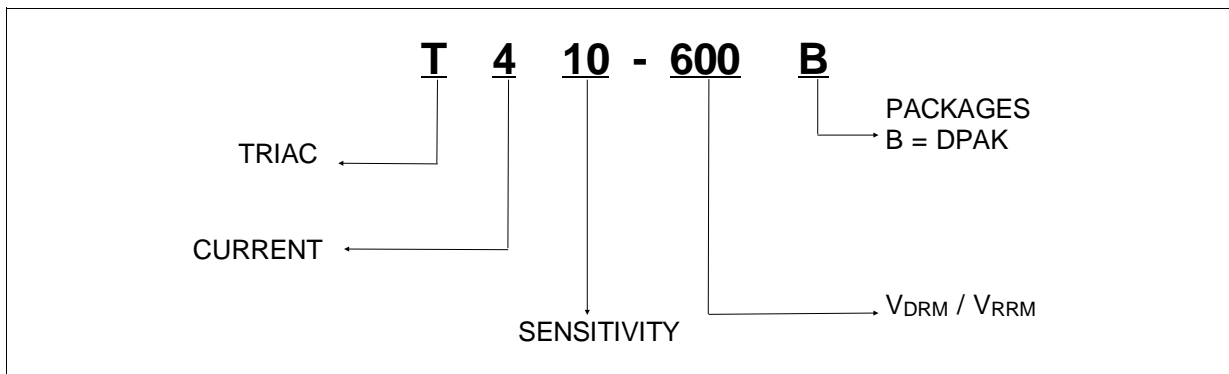
$P_{G(AV)} = 1 \text{ W}$ $P_{GM} = 10 \text{ W}$ (tp = 20 μs) $I_{GM} = 4 \text{ A}$ (tp = 20 μs) $V_{GM} = 16 \text{ V}$ (tp = 20 μs).

ELECTRICAL CHARACTERISTICS

Symbol	Test Conditions	Quadrant		Value		Unit	
				T410	T435		
I _{GT}	V _D =12V (DC) R _L =33Ω	T _j =25°C	I-II-III	MAX	10	35	mA
V _{GT}	V _D =12V (DC) R _L =33Ω	T _j =25°C	I-II-III	MAX	1.5		V
V _{GD}	V _D =V _{DRM} R _L =3.3kΩ	T _j =125°C	I-II-III	MIN	0.2		V
t _{gt}	V _D =V _{DRM} I _G = 500mA dI _G /dt = 3A/μs I _{TM} = 5.5A	T _j =25°C	I-II-III	TYP	2		μs
I _L	I _G =1.2 I _{GT}	T _j =25°C	I-II-III	MAX	30	60	mA
I _H *	I _T = 100mA gate open	T _j =25°C		MAX	15	35	mA
V _{TM} *	I _{TM} = 5.5A tp= 380μs	T _j =25°C		MAX	1.75		V
I _{DRM} I _{RRM}	V _{DRM} Rated V _{RRM} Rated	T _j =25°C		MAX	10		μA
		T _j =125°C		MAX	2		mA
dV/dt *	Linear slope up to V _D =67%V _{DRM} gate open	T _j =125°C		MIN	30	250	V/μs
(dI/dt) _c *	(dV/dt) _c = 0.1V/μs	T _j =125°C		MIN	2.7	4.4	A/ms
	(dV/dt) _c = 20V/μs				1.8	2.7	

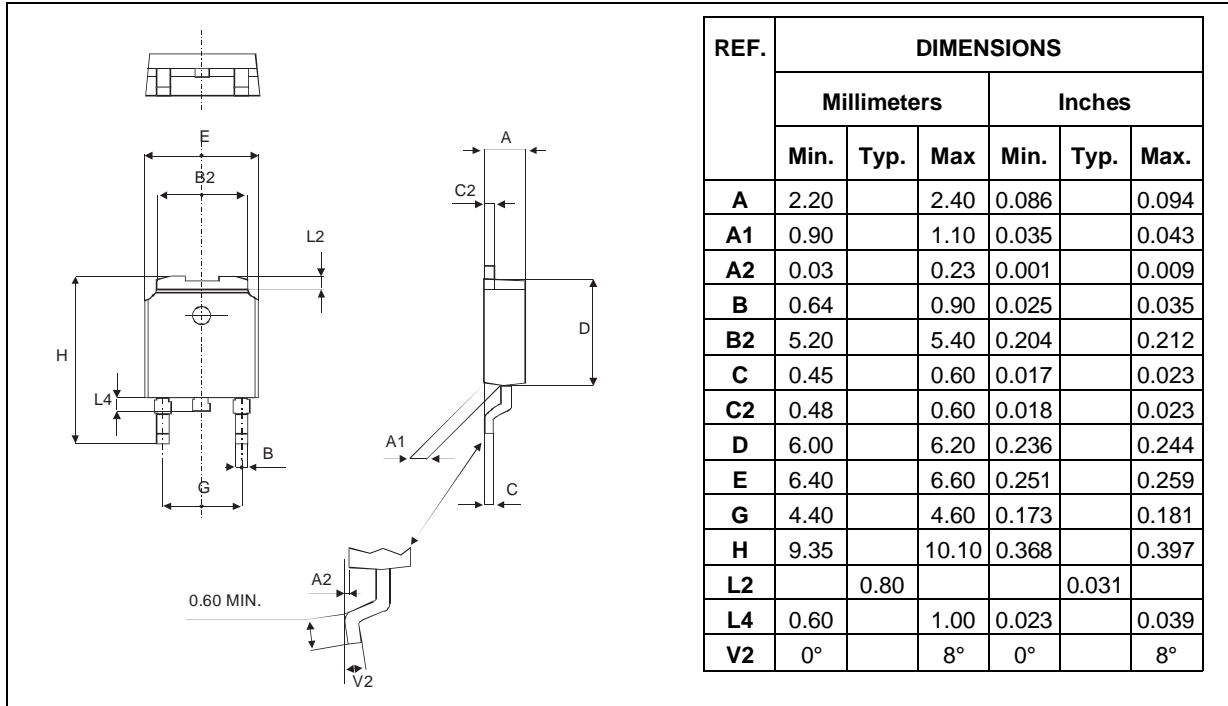
* For either polarity of electrode A₂ voltage with reference enceenceto electrode A₁.

ORDERING INFORMATION



PACKAGE MECHANICAL DATA

DPAK Plastic



MARKING : Type number

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