

SPECIFICATION FOR APPROVAL

() Preliminary Specification

(●) Final Specification

Title

23.0" WXGA TFT LCD

BUYER	General
MODEL	

SUPPLIER	LG.Philips LCD Co., Ltd.
*MODEL	LC230WX3
SUFFIX	SLA1

*When you obtain standard approval, please use the above model name without suffix

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Record of Revisions

Revision No.	Revision Date	Page	Description
Ver 1.0	Nov.22,2005	-	Final Specification

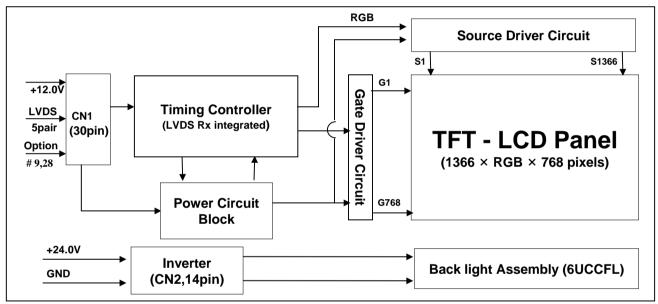


1. General Description

LC230WX3-SLA1 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp(UCCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. It has a 23 inch diagonally measured active display area with WXGA resolution (768 vertical by 1366 horizontal pixel array) Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 8-bit gray scale signal for each dot, thus presenting a palette of more than 16.7M(true) colors.

It has been designed to apply the 8-bit 1 port LVDS interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



General Features

Active Screen Size	23 inches (582.96mm) diagonal
Outline Dimension	546.0mm(H) x 318.3mm(V) x 42.1mm(D) (Typ.)
Pixel Pitch	0.124mm x 0.372mm x RGB
Pixel Format	1366 horiz. by 768 vert. pixels RGB stripe arrangement
Color Depth	8-bit, 16.7 M colors
Luminance, White	450 cd/m ² (Center 1 point Typ.)
Viewing Angle (CR>10)	Viewing angle free (R/L 178(Typ.), U/D 178(Typ.))
Power Consumption	Total 66.84Watt (Typ.) (Logic=3.24W, Lamp=63.6W [Lamp current=6.5mA])
Weight	3,800 g (Typ.) 4,000g(Max)
Display Operating Mode	Transmissive mode, normally black
Surface Treatment	Hard coating(3H), Anti-glare treatment of the front polarizer



2. Absolute Maximum Ratings

The followings are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1.	ABSOLUTE	MAXIMUM RATINGS
----------	----------	-----------------

Parameter		Symbol	Value		Unit	Remark
		Symbol	Min	Max	Offic	Remark
Power Input	LCM	VLCD	-0.3	14.0	Vdc	at 25 ± 2 °C
Voltage	Backlight inverter	VBL	-0.3	27.0	Vdc	
ON/OFF Contr	rol Voltage	VON/OFF	-0.30	5.25	Vdc	
Brightness Co	Brightness Control Voltage		0.0	5.25	Vdc	
Operating Ten	nperature	ТОР	0	50	Ĵ	1
(Ambient Tem	p.)	Tsur	0	65	Ĵ	2
Storage Temp	perature	TST	-20	65	Ĵ	1
Operating Ambient Humidity		HOP	10	90	%RH	1
Storage Humic	dity	HST	10	90	%RH	1

Note : 1. Temperature and relative humidity range are shown in the figure below.

- Wet bulb temperature should be 39 °C Max. and no condensation.
- 2. Abnormal visual problems by panel surface temperature can be occurred in specific range (60 °C ~ 65 °C), But materials(ex : polarizer) are not damaged permanently in this range, Tsur.
- 3. Ambient illuminancd should be more than 10 lux.

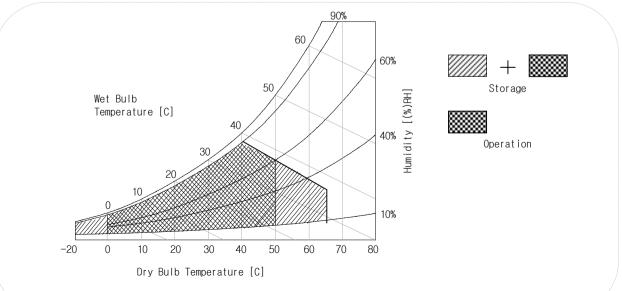


Figure 2. Temperature and relative humidity

3. Electrical Specifications

3-1. Electrical Characteristics

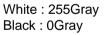
It requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The other input power for the UCCFL/Backlight is to power inverter.

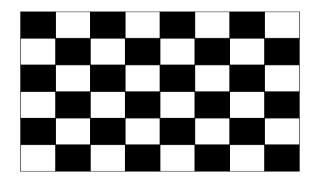
Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol		Value	Unit	Note	
		Min	Тур	Max		noto
Power Input Voltage	VLCD	11.4	12.0	12.6	Vdc	
Power Input Current	ILCD	-	270	351	mA	1
Power Input Current		-	340	442	mA	2
Power Consumption	PLCD	-	3.24	4.42	Watt	1
Rush current	Irush	-	-	3.0	А	3

Note :

- 1. The specified current and power consumption are under the V_{LCD}=12.0V, 25 \pm 2°C, f_V=60Hz condition whereas mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.
- 2. The current is specified at the maximum current pattern.
- 3. The duration of rush current is about 2ms and rising time of power Input is 1ms(min.).





Mosaic Pattern(8 x 6)

Table 3. Inverter Electrical Characteristics

Parameter	Symbol	Condition		Linit	Note		
Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit	note
Input Voltage	V _{BL}		21.6	24.0	25.2	V _{DC}	
Input Current	I _{BL}	V_{BR} -A(max) = 3.3V	2.38	2.65	2.92	А	1, 3
Inrush Current	I _{RUSH}	V _{BL} =21.6V V _{BR} -A=3.3V EXTV _{BR} -B=100%	-	-	3.82	A	5
Power Consumption	P _{BL}	V _{BR} -A =3.3V	-	63.6	70.1	Watt	1, 3
B/L on/off control	V _{ON/OFF}	Lamp ON = High	2.0	-	5.0	V _{DC}	
	- ON/OFF	Lamp OFF =Low	0.0		0.8	V _{DC}	
Brightness Adj (Analog mode)	V _{BR} -A		0	-	3.3	V _{DC}	
Brightness Adj (Burst mode)	EXTV _{BR} -B	External Input (PWM)	20	-	100	%	4
Frequency (PWM) (Burst mode)	f-B	V _{BL} =24V EXTV _{BR} -B=PWM	-	180	-	Hz	6
Pulse Duty(PWM)		High Level	2.5	-	5.0	V _{DC}	
(Burst mode)		Low Level	0.0	-	0.6	V _{DC}	
Lamp Life Time			50,000	60,000		Hrs	2

Notes :

1. The specified current and power consumption are under the typical supply Input voltage, 24.0V. The ripple voltage of the power supply input voltage is under 0.5 Vp-p.

Inrush current of the power supply input current is under +10% of the typical current

2. Specified values are for a single lamp which is aligned horizontally.

The life is determined as the time at which luminance of the lamp is 50% compared to that of initial value at the typical lamp current on condition of continuous operating at $25 \pm 2^{\circ}$ C. Specified value is when lamp is aligned horizontally.

- 3. Electrical characteristics are determined after the unit has been 'ON' and stable for approximately 2Hrs in a dark environment at 25 °C \pm 2°C.
- 4. Burst mode is controlled by TV system.
- 5. Rush current is determined at turning on after the unit has been 'OFF' for 2Hrs at 25 °C \pm 2°C.
- 6. Synchronize PWM by triple with Vsync signal of system.



3-2. Interface Connections

This LCD employs two kinds of interface connection, a 30-pin connector is used for the module electronics and two 12-pin Connectors are used for the integral backlight system.

3-2-1. LCD Module

- LCD Connector(CN1) : FI-X30SSL-HF (Manufactured by JAE).

- Mating Connector : FI-X30C2L (Manufactured by JAE) or Equivalent

Table 4. MODULE CONNECTOR(CN1) PIN CONFIGURATION

Pin No.	Symbol	Description	Note
1	VLCD	Power Supply +12.0V	
2	VLCD	Power Supply +12.0V	
3	VLCD	Power Supply +12.0V	
4	VLCD	Power Supply +12.0V	
5	GND	Ground	
6	GND	Ground	
7	GND	Ground	
8	GND	Ground	
9	Select	Select LVDS Data format	1
10	NC	NC	
11	GND	Ground	
12	RA-	LVDS Receiver Signal(-)	
13	RA+	LVDS Receiver Signal(+)	
14	GND	Ground	
15	RB-	LVDS Receiver Signal(-)	
16	RB+	LVDS Receiver Signal(+)	
17	GND	Ground	
18	RC-	LVDS Receiver Signal(-)	
19	RC+	LVDS Receiver Signal(+)	
20	GND	Ground	
21	RCLK-	LVDS Receiver Clock Signal(-)	
22	RCLK+	LVDS Receiver Clock Signal(+)	
23	GND	Ground	
24	RD-	LVDS Receiver Signal(-)	
25	RD+	LVDS Receiver Signal(+)	
26	GND	Ground	
27	NC	NC	
28	NC	NC	Ī
29	GND	Ground	
30	NC(GND)	LCD test	2

Note: 1. If the pin no. 9 is Ground, Interface format is "LG", and if the pin no. 9 is Vcc(3.3V), Interface format is "DISM". See page 9 and 10.

- 2. If pin30 is 3.3V, no-signal is AGP pattern, and if pin30 is ground, no-signal is Black pattern
- 3. All GND (ground) pins should be connected together, which should be also connected to the LCD module's metal frame.
- 4. All VLCD (power input) pins should be connected together.
- 5. Input Levels of LVDS signals are based on the IEA 664 Standard.



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Table 5. REQUIRED SIGNAL ASSIGNMENT FOR LVDS TRANSMITTER (Pin9="L" or "Open")

Host System 24 Bit	DS90C385 or Compatible	· FI->	FI-X30SSL-HF		
RED0		48			
RED1	—52 TxOUT0-	47	12	100Ω ≶	RxIN0-
RED2	54 TxOUT04	47	13	10032 5	RxIN0+
RED3					
RED4	56	46			
RED5	—3 TxOUT1-	45	15	100Ω ≷	RxIN1-
RED6		+ 40	16	10038 5	RxIN1+
RED7	2				
GREEN0	4	42			
GREEN1	6 TxOUT2-	41	18	100Ω ≷	RxIN2-
GREEN2	7 TxOUT2+	-	19		RxIN2+
GREEN3	11				
GREEN4	12	40			
GREEN5	14 TxCLKOUT-	39	21	100Ω ≷	RxCLKIN-
GREEN6	8 TxCLKOUT+	- 39	22	10038 5	RxCLKIN+
GREEN7	10				
BLUE0	15	38			
BLUE1	19 TxOUT3-	37	24	100Ω ≶	RxIN3-
BLUE2	20 TxOUT34		25	10036 5	RxIN3+
BLUE3	22				
BLUE4	23		9		LG / DISM
BLUE5	24		30		LCD Test
BLUE6	16				
BLUE7	18				
Hsync	27			l	
Vsync	28	GND			L
Data Enable	30	5 5		LCD M	lodule
CLOCK	31				

Note: 1. The LCD Module uses a 100 Ohm[Ω] resistor between positive and negative lines of each receiver input.

- 2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (DS90C385 or Compatible)
- 3. '7' means MSB and '0' means LSB at R,G,B pixel data.



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Table 6. REQUIRED SIGNAL ASSIGNMENT FOR LVDS TRANSMITTER (Pin9="H")

Host System 24 Bit	DS90C385 or Compatible	FI-X30S	SL-HF	Timing Controller
RED0 RED1 RED2	50 2 TxOUT0- 51 TxOUT0+	48 12 47 13	1000 >	RxIN0- RxIN0+
RED3 RED4 RED5 RED6 RED7	52 54 55 TxOUT1- 56 TxOUT1+ 3	46 15 45 16	1000 >	RxIN1- RxIN1+
GREEN0 GREEN1 GREEN2 GREEN3	8 10 TxOUT2- 4 TxOUT2+ 6	42 41 18	1009 >	RxIN2- RxIN2+
GREEN4 GREEN5 GREEN6 GREEN7	7 11 TxCLKOUT- 12 TxCLKOUT+ 14	40 21 39 22	1000 >	
BLUE0 BLUE1 BLUE2 BLUE3	16 18 TxOUT3- 15 TxOUT3+ 19	38 37 24 25	$100\Omega <$	RxIN3- RxIN3+
BLUE4 BLUE5 BLUE6	20 22 23	9 30		LG / DISM LCD Test
BLUE7 Hsync Vsync Data Enable CLOCK	24 27 28 30 31	GND	LCD M	odule

Note: 1. The LCD Module uses a 100 $Ohm[\Omega]$ resistor between positive and negative lines of each receiver input.

- 2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (DS90C385 or Compatible)
- 3. '7' means MSB and '0' means LSB at R,G,B pixel data.

3-2-2. Backlight Inverter

LG.PHILIPS LCD

- Inverter Connector : S14B-PH-SM3 Side entry type (manufactured by JST).
- Mating Connector : PHR-14 or Equivalent

Table 7. INVERTER CONNECTOR PIN CONFIGULATION

Pin	Symbol	Signal assignment	Note
1	VBL	24V Power Input	
2	VBL	24V Power Input	
3	VBL	24V Power Input	
4	VBL	24V Power Input	
5	VBL	24V Power Input	
6	GND	GROUND	
7	GND	GROUND	
8	GND	GROUND	1
9	GND	GROUND	
10	GND	GROUND	
11	V_{BR} -A	Analog dimming	0V(min) ~ 3.3V(max)
12	ON / OFF	Backlight ON/OFF control	ON : 2.4 ~ 5.0V OFF : 0.0 ~ 0.6V
13	EXTV _{BR} -B	External PWM signal	Rising edge : Lamp on Falling edge : Lamp off
14	Error out	Backlight status output	Normal : 0V Abnormal : External pull-up

Note :

1. GND is connected to the LCD module's metal frame.



3-3. Signal Timing Specifications

This is the signal timing required at the input of LVDS Transmitter. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

Table 8. TIMMING TABLE

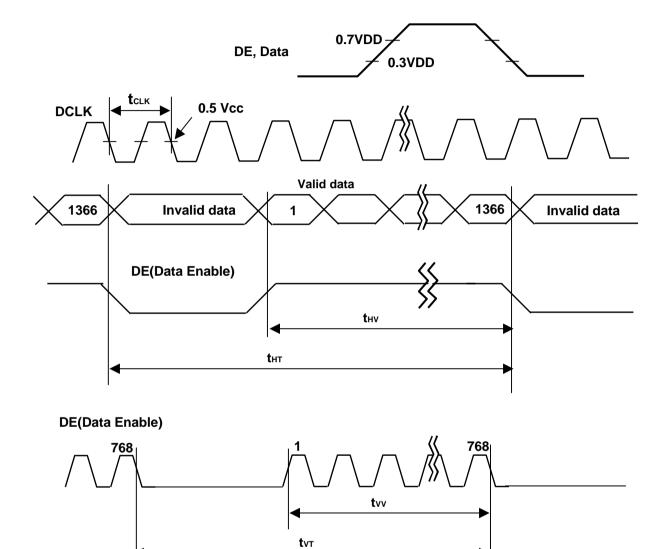
	ITEM	Symbol	Min.	Тур.	Max.	Unit	Note
Clock	Frequency	f _{CLK}	68	72.3	82	MHz	
Hsync	Frequency	f _H	45	47.4	53	KHZ	3
	Display Valid	t _{HV}	1366	1366	1366	Clks	
	Blank	t _{HT-} t _{HV}	90	162	410	Clks	
	Total	t _{HT}	1456	1528	1776	Clks	
	Frequency	f _V	47	60	66	ΗZ	3
Maria	Display Valid	t _{vv}	768	768	768	Lines	PAL :
Vsync	Blank	t _{VT} -t _{VV}	7	22	295	Lines	47~53Hz, NTSC :
	Total	t _{VT}	775	790	1063	Lines	57~63Hz

Notes:

- 1. The performance of the electro-optical characteristics are may be influenced by variance of the vertical refresh rates.
- 2. Above timing table is only valid for DE Mode.
- 3. H sync ,V sync don't care.(for only DE Mode)



3-4. Signal Timing Waveforms



3-5. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 8-bit gray scale data input for the color the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 9. COLOR DATA REFERENCE

													Inpu	it Co	olor	Data	a									
	Color					RE	D							GRE	EN							BL	UE			
			MS								MS							SB								SB
	1								R1																	
	Black		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (255)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue (255)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (000)	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (001)		0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED																										
	RED (254)		1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (255)		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (000)	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (001)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN																										
	GREEN (254)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (255)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE (000)	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE																										
	BLUE (254)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (255)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1



3-6. Power Sequence

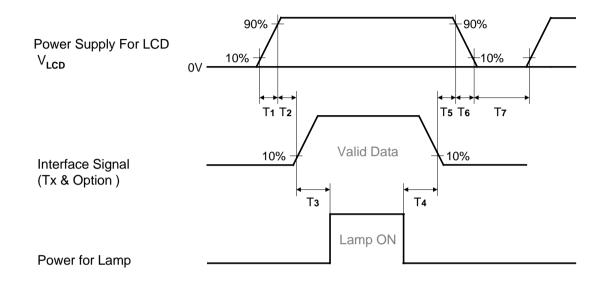


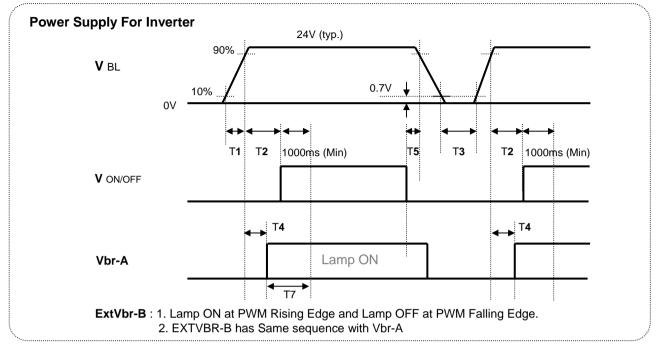
Table 10. POWER SEQUENCE

Deremeter		Value		Unit
Parameter	Min	Тур	Max	Unit
T1	0.01	-	20	ms
T2	0.01	-	50	ms
Т3	200	-	-	ms
T4	200	-	-	ms
Т5	0.01	-	50	ms
Т6	-	-	-	ms
Τ7	2.0	-	-	s

- Note: 1. Please avoid floating state of interface signal at invalid period.
 - 2. When the interface signal is invalid, be sure to pull down the power supply V_{LCD} to 0V.
 - 3. The case when the T2/T5 exceed maximum specification, it operates protection pattern(Black pattern) till valid signal inputted. There is no reliability problem.
 - 4. The T3/T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.



3-6-2. Sequence for Inverter



3-6-3. Deep condition for Inverter

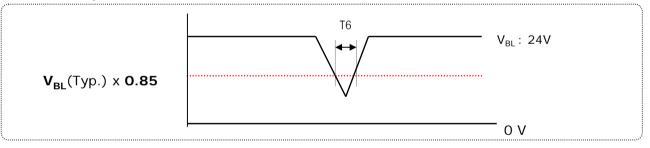


Table 11. Power Sequence for Inverter

Parameter		Values		Units	Remarks
Falameter	Min	Тур	Max	Units	Remarks
T1	20	-	-	ms	1
T2	500	-	-	ms	
Т3	200	-	-	ms	2
T4	0		-	ms	4, 5
T5	10	-	-	ms	
Т6	-	-	10	ms	V_{BL} (Тур) х 0.85
Τ7	1000	-	-	ms	

Note : 1. T1 describes rising time of 0V to 24V and is not applied at restarting time.

- 2. When the Inverter is shut-down by ARC protection, T3 need 3.3sec
- 3. When V_{BL} (24V) is supplied always, there is no reliability problem.
- 4. T4(max) is less than T2.
- 5. In T7 section, ExtVbr-B should be duty 100% and Vbr-A is recommended 3.3V.



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' for 30min in a dark environment at $25\pm2^{\circ}$ C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0 °.

It is presented additional information concerning the measurement equipment and method in FIG. 1.

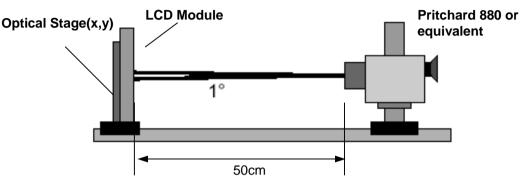


FIG. 1 Optical Characteristic Measurement Equipment and Method

Table 12. OPTICAL CHARACTERISTICS

Ta= $25\pm2^{\circ}$ C, V_{LCD}=12.0V, fv=60Hz, Dclk=72MHz, Vbr-A =Max, ExtVbr=100%

							VDI - 10076
Dor	ameter	Symbol		Value		Unit	Note
Par	ameter	Symbol	Min	Тур	Max	Unit	Note
Contrast Ratio		CR	600	800			1
Surface Luminance, white		L _{WH}	350	450		cd/m ²	2
Luminance Variation		δ _{WHITE} 5P			1.6		3
Response Time	Rise Time	Tr _R	-	9	16	ms	4
Response Time	Decay Time	Tr _D	-	9	16	1115	4
	RED	Rx		0.637			
Color Coordinates		Ry]	0.337			
	GREEN	Gx	1	0.276			
	es	Gy	Тур	0.605	Typ +0.03		
[CIE1931]	BLUE	Bx	-0.03	0.146			
		Ву		0.062			
	WHITE	Wx		0.272			
		Wy	1	0.278			
Viewing Angle	CR>10)		1				
x	axis, right(∳=0°)	θr	85	89	-		
x	axis, left (_{\$=180°})	θΙ	85	89	-		_
У	axis, up (φ=90°)	θu	85	89	-	degree	5
У	axis, down (φ=270°)	θd	85	89	-		
Gray Scale				2.2			6



Notes 1. Contrast Ratio(CR) is defined mathematically as :

Surface Luminance with all white pixels

Contrast Ratio =

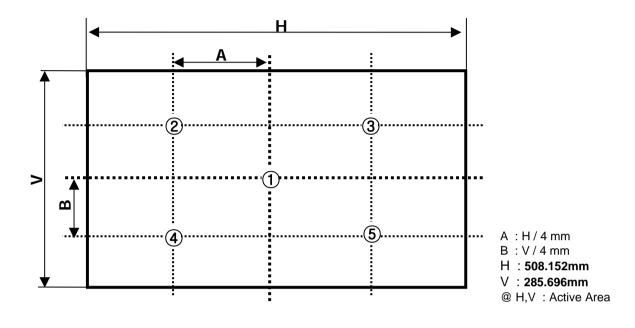
Surface Luminance with all black pixels It is measured at the center point(1).

- 2. Surface luminance is luminance value at the center point across the LCD surface 50cm from the surface with all pixels displaying white. For more information, see FIG 2.
- 3. The variation in surface luminance , δ WHITE is defined as :
 $$\begin{split} &\delta \text{ WHITE(5P)} = \text{Maximum}(L_{\text{on1}}, L_{\text{on2}}, L_{\text{on3}}, \ldots \ldots, L_{\text{on5}}) \, / \, \text{Minimum}(L_{\text{on1}}, L_{\text{on2}}, L_{\text{on3}}, \ldots \ldots, L_{\text{on5}}) \\ &\text{Where } L_{\text{on1}} \text{ to } L_{\text{on5}} \text{ are the luminance with all pixels displaying white at 5 locations }. \end{split}$$
 For more information, see FIG 2.
- 4. Response time is the time required for the display to transition from black to white (Rise Time, Tr_p) and from white to black (Decay Time, Tr_D). For additional information, see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information, see FIG 4.
- 6. Gray scale specification Gamma Value is approximately 2.2. For more information, see Table 13.

Gray Level	Luminance [%] (Typ)	
LO	0.13	
L15	0.24	
L31	0.78	
L47	1.81	
L63	3.46	
L79	6.04	
L95	9.48	
L111	13.60	
L127	18.14	
L143	24.46	
L159	32.38	
L175	41.63	
L191	51.64	
L207	63.18	
L223	75.52	
L239	88.89	
L255	100	

Table 13. GRAY SCALE SPECIFICATION

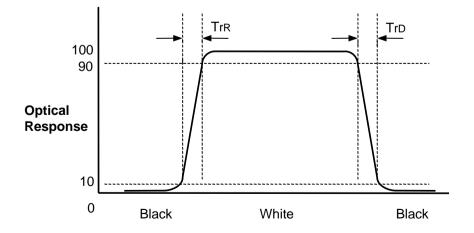




Measuring point for surface luminance & measuring point for luminance variation

FIG. 2 Measure Point for Luminance

The response time is defined as the following figure and shall be measured by switching the input signal for Black to White and White to Black.







Dimension of viewing angle range

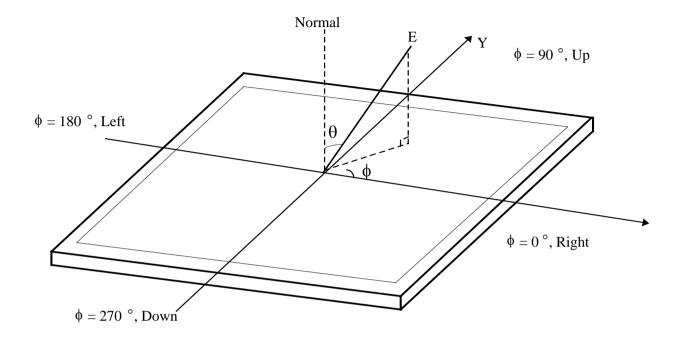


FIG. 4 Viewing angle

5. Mechanical Characteristics

The following items provide general mechanical characteristics. In addition the figures in the next page are detailed mechanical drawing of the LCD module.

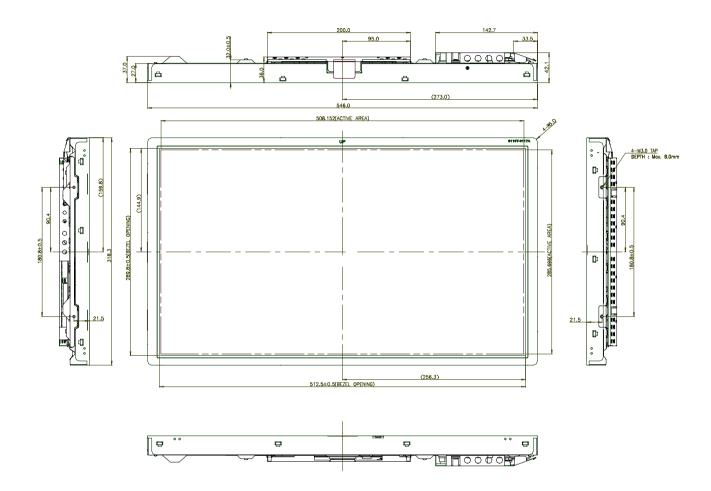
Table 14.	MECHANICAL	CHARACTERISTICS
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	Horizontal	546.0mm			
Outline Dimension	Vertical	318.3mm			
	Depth	42.1mm			
Bezel Area	Horizontal	512.5mm			
Dezel Alea	Vertical	289.8mm			
Active Display Area	Horizontal	508.152mm			
Active Display Area	Vertical	285.696mm			
Weight	(Typ.) 3,800 (g), (Max.) 4,000(g)				
Surface Treatment	Hard coating(3H) Anti-glare treatment of the front polarizer				

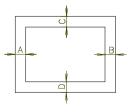
Note : Please refer to a mechanic drawing in terms of tolerance at the next page.

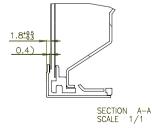


<FRONT VIEW>



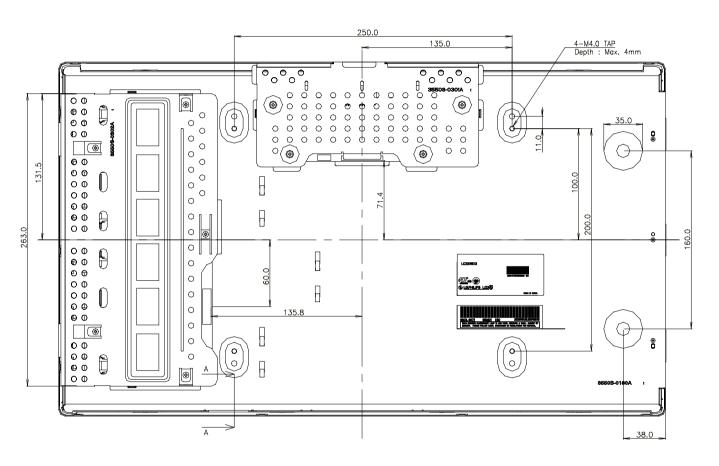
- NOTES 1. UNSPECIFIED DIMENSIONAL TOLERANCES TO BE ±1.0mm. 2. GAP BETWEEN TOP CASE AND GLASS IS 0.4⁺C³ 3. MAXIMUM ALLOWABLE DEPTH OF REAR SIDE USER MOUNT HOLE IS 5.0mm 4. TILT AND A PARTIAL DISPOSITION TOLERANCE OF DISPLAY AREA ARE AS FOLLOW. 1) X-DIRECTION : |A-B| < 1.0mm 2) Y-DIRECTION : |C-D| < 1.0mm







<REAR VIEW>





6. Reliability

Table 15. ENVIRONMENT TEST CONDITION

No	Test Item	Condition
1	High temperature storage test	60°C, 500h
2	Low temperature storage test	-20°C, 500h
3	High temperature operation test	50°C, 80%RH, 500h 60°C, 500h (2000h)
4	Low temperature operation test	0°C, 500h (1000h)
5	Heat cycle test	-20°C ~ 60°C, 30min/5min/30min, 100cycles
6	Soldering heat cycle test	-40°C ~ 80°C, 30min/5min/30min, 200cycles
7	Vibration test (non-operating)	Wave form : random Vibration level : 1.0Grms Bandwidth : 10-500Hz Duration : X,Y,Z, 10 min One time each direction
8	Shock test (non-operating)	Shock level : 100Grms Waveform : half sine wave, 2ms Direction : ±X, ±Y, ±Z One time each direction
9	ESD test	Condition : 150pF,330 ohm Case , air Evaluation : ±15kV
10	Humidity Storage test	Ta= 40 °C ,70%RH(240hr)

Note : After Reliability Test , display function should be kept as the result evaluation.



7. International standards

7-1. Safety

- a) UL 60950-1:2003, First Edition, Underwriters Laboratories, Inc., Standard for Safety of Information Technology Equipment.
- b) CAN/CSA C22.2, No. 60950-1-03 1st Ed. April 1, 2003, Canadian Standards Association, Standard for Safety of Information Technology Equipment.
- c) EN 60950-1:2001, First Edition, European Committee for Electrotechnical Standardization(CENELEC) European Standard for Safety of Information Technology Equipment.

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998 (Including A1: 2000)



8. Packing

8-1. Designation of Lot Mark



A,B,C : SIZE(INCH) E : MONTH

G : ASSEMBLY CODE

D : YEAR F : FACTORY CODE H ~ M : SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	А	В	С

3. FACTORY CODE

Factory Code	LPL Gumi	LPL Nanjing	HEESUNG	
Mark	К	С	D	

4. SERIAL NO.

Mark 100001~199999, 200001~299999, 300001~399999,, A00001~A99999,, Z00001~Z99999

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

- a) Package quantity in one box : 4 pcs
- b) Box Size : 530mm X 310mm X 327mm



9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 Do not touch the surface of polarizer for here here here any sloth (Same competies are detrimented).

Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)

- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :
 - $V=\pm 200 mV(Over and under shoot voltage)$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw. (if not, it causes metallic foreign material and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.



9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.It is recommended that they be stored in the container in which they were shipped.

9-6. Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ionblown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normalhexane.