

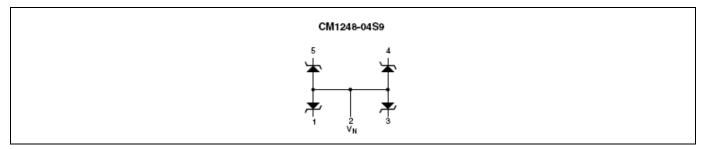
# Low Capacitance Transient Voltage Suppressors / ESD Protectors

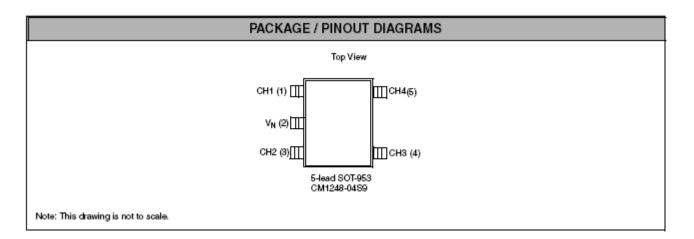
# CM1248-04S9

## Features

- Low I/O capacitance at 10pF at 0V
- In-system ESD protection to ±15kV contact discharge, per the IEC 61000-4-2 international standard
- · Compact SMT package saves board space and facilitates layout in space-critical applications
- Each I/O pin can withstand over 1000 ESD strikes

## **Block Diagram**





PIN DESCRIPTIONS					
Pins	NAME	DESCRIPTION			
(Refer to package / pinout diagrams)	CHx	The cathode of the respective TVS diode, which should be connected to the node requiring transient voltage protection.			
(Refer to package / pinout diagrams)	V <sub>N</sub>	The anode of the TVS diodes.			

## **Ordering Information**

PART NUMBERING INFORMATION					
			Lead-free Finish		
Pins	Channels	Package	Ordering Part Number <sup>1</sup>	Part Marking	
5	4	SOT-953	CM1248-04S9	L8	

Note 1: Parts are shipped in Tape & Reel form unless otherwise specified.

## Specifications

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	RATING	UNITS				
Storage Temperature Range	-65 to +150	°C				

STANDARD OPERATING CONDITIONS					
PARAMETER	RATING	UNITS			
Operating Temperature	-40 to +85	°C			

## CM1248-04S9

	ELECTRICAL OPERATING CHARACTERISTICS (NOTE 1)								
SYMBOL	PARAMETER	CONDITIONS	MIN	ТҮР	MAX	UNITS			
C <sub>IN</sub>	Channel Input Capacitance	T <sub>A</sub> = 25 ℃, 0VDC, 1MHz		10	13	pF			
$\Delta C_{in}$	Differential Channel I/O to GND Capacitance	T <sub>A</sub> = 25 ℃, 2.5VDC, 1MHz		0.19		pF			
V <sub>rso</sub>	Reverse Stand-off Voltage	I <sub>R</sub> =10μA, T <sub>A</sub> = 25 °C	5.5			V			
		I <sub>R</sub> =1mA, T <sub>A</sub> = 25 ℃	6.1			V			
I <sub>leak</sub>	Leakage Current	V <sub>IN</sub> =5.0VDC, T <sub>A</sub> = 25 ℃			0.75	μA			
V <sub>sig</sub>	Small Signal Clamp Voltage Positive Clamp Negative Clamp	I = 10mA, T <sub>A</sub> = 25 ℃ I = -10mA, T <sub>A</sub> = 25 ℃		6.8 -0.89		V V			
V <sub>ESD</sub>	ESD Withstand Voltage Contact Discharge per IEC 61000- 4-2 standard Human Body Model, MIL-STD-883, Method 3015	Notes 3 and 4; $T_A = 25 \degree C$ Notes 2 and 4; $T_A = 25 \degree C$	<u>+</u> 15 <u>+</u> 30			kV kV			
R <sub>D</sub>	Diode Dynamic Resistance Forward Conduction Reverse Conduction	T <sub>A</sub> = 25 ℃; Note 2		0.57 1.36		Ω Ω			

Note 1: All parameters specified at  $T_A = -40$  °C to +85 °C unless otherwise noted. Note 2: Human Body Model per MIL-STD-883, Method 3015,  $C_{\text{Discharge}} = 100\text{pF}$ ,  $R_{\text{Discharge}} = 1.5\text{K}\Omega$ ,  $V_N$  grounded. Note 3: Standard IEC 61000-4-2 with  $C_{\text{Discharge}} = 150\text{pF}$ ,  $R_{\text{Discharge}} = 330\Omega$ ,  $V_N$  grounded. Note 4: These measurements performed with no external capacitor on  $CH_x$ .

### **Performance Information**

#### **Diode Capacitance**

Typical diode capacitance with respect to positive TVS cathode voltage (reverse voltage across the diode) is given in Diode Capacitance vs. Reverse Voltage .

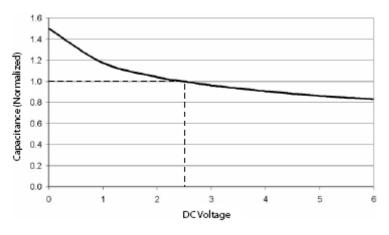
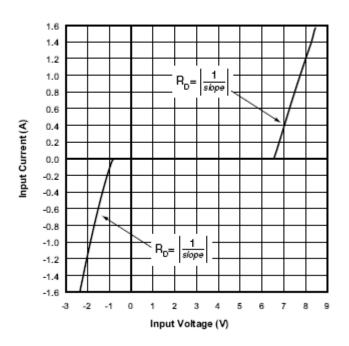


Figure 1. Diode Capacitance vs. Reverse Voltage

#### **Typical High Current Diode Characteristics**

Measurements are made in pulsed mode with a nominal pulse width of 0.7ms.

Typical Input VI Characteristics (Pulse-mode measurements, pulse width = 0.7ms nominal)



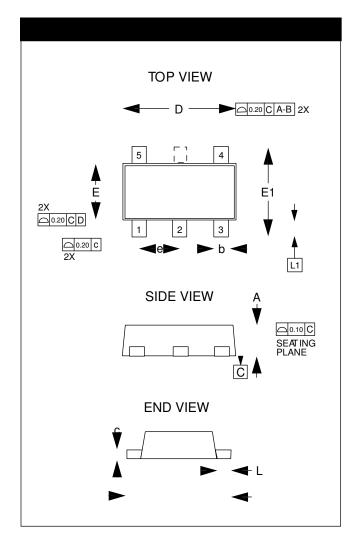
Rev. 2 | Page 4 of 7 | www.onsemi.com

## **Mechanical Details**

### SOT-953 Mechanical Specifications, 5 pin

The 5-pin SOT-953 package dimensions are shown below.

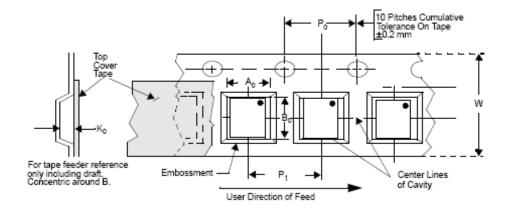
PACKAGE DIMENSIONS								
Package	SOT-953							
Leads			:	5				
Dim.	Millimeters			Inches				
Dini.	Min	Nom	Max	Min	Nom	Max		
Α	0.400	0.450	0.500	0.016	0.018	0.020		
b	0.100	0.150	0.200	0.004	0.006	0.008		
с	0.050	0.100	0.150	0.002	0.004	0.006		
D	0.950	1.000	1.050	0.037	0.039	0.041		
E	0.750	0.800	0.850	0.029	0.031	0.033		
E1	0.950	1.000	1.050	0.037	0.039	0.041		
е	0.350 BSC			0.014 BSC				
L	0.050	0.100	0.150	0.002	0.004	0.006		
L1	0.125	0.150	0.175	0.005	0.006	0.007		
# per tape and reel	8000 pieces							
	Controlling dimension: millimeters							



Package Dimensions for SOT-953

### Tape and Reel Specifications

PART NUMBER	PACKAGE SIZE (mm)	POCKET SIZE (mm) B <sub>o</sub> X A <sub>o</sub> X K <sub>o</sub>	TAPE WIDTH W	REEL DIAMETER	QTY PER REEL	P₀	P <sub>1</sub>
CM1248-04S9	1.00 X 0.80 X 0.45	1.16 X 1.16 X 0.63	8mm	178mm (7")	8000	4mm	4mm



## CM1248-04S9

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specificatly disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights or ther gifts of or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application. Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

#### PUBLICATION ORDERING INFORMATION

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800-282-9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81-3-5773-3850 ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative