

AT28C64

T-46-13-27

Features

- Fast Read Access Time - 150ns
- Fast Byte Write - 200µs or 1 ms
- Self-Timed Byte Write Cycle
  - Internal Address and Data Latches
  - Internal Control Timer
  - Automatic Clear Before Write
- Direct Microprocessor Control
  - READY/BUSY Open Drain Output
  - DATA Polling
- Low Power
  - 30mA Active Current
  - 100µA CMOS Standby Current
- High Reliability
  - Endurance: 10<sup>4</sup> or 10<sup>5</sup> Cycles
  - Data Retention: 10 years
- 5V ± 10% Supply
- CMOS and TTL Compatible Inputs and Outputs
- JEDEC Approved Byte-Wide Pinout
- Full Military, Commercial, and Industrial Temperature Ranges



64K (8K x 8)  
CMOS  
E<sup>2</sup>PROM

Description

The AT28C64 is a low-power, high-performance 8,192 words x 8 bit non-volatile Electrically Erasable and Programmable Read Only Memory with popular, easy to use features. The device is manufactured with Atmel's reliable non-volatile technology.

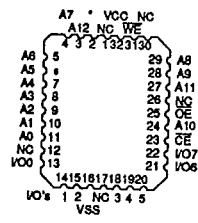
The AT28C64 is accessed like a Static RAM for the read or write cycles without the need for external components. During a byte write, the address and data are latched internally, freeing the microprocessor address and data bus for other operations. Following the initiation of a write cycle, the device will go to a busy state and automatically clear and write the latched data using an internal control timer. The device includes two methods for detecting the end of a write cycle, level detection of RDY/BUSY (unless pin 1 is N.C.) and DATA polling of I/O7. Once the end of a write cycle has been detected, a new access for a read or write can begin.

The CMOS technology offers fast access times of 150ns at low power dissipation. When the chip is deselected the standby current is less than 100µA.

Atmel's 28C64 has additional features to ensure high quality and manufacturability. The device utilizes error correction internally for extended endurance and for improved data retention characteristics. An extra 32 bytes of E<sup>2</sup>PROM are available for device identification or tracking.

Pin Configurations

Pin	Pin Name	Function
1	RDY/BUSY (or NC)	
2	A12	Addresses
3	A7	Addresses
4	A6	Addresses
5	A5	Addresses
6	A4	Addresses
7	A3	Addresses
8	A2	Addresses
9	A1	Addresses
10	A0	Addresses
11	IO0	Data Inputs/Outputs
12	IO1	Data Inputs/Outputs
13	IO2	Data Inputs/Outputs
14	GND	
15	IO3	Data Inputs/Outputs
16	IO4	Data Inputs/Outputs
17	IO5	Data Inputs/Outputs
18	IO6	Data Inputs/Outputs
19	IO7	Data Inputs/Outputs
20	CE	Chip Enable
21	A10	Addresses
22	OE	Output Enable
23	A11	Addresses
24	A9	Addresses
25	A8	Addresses
26	NC	No Connect
27	WE	Write Enable
28	VCC	



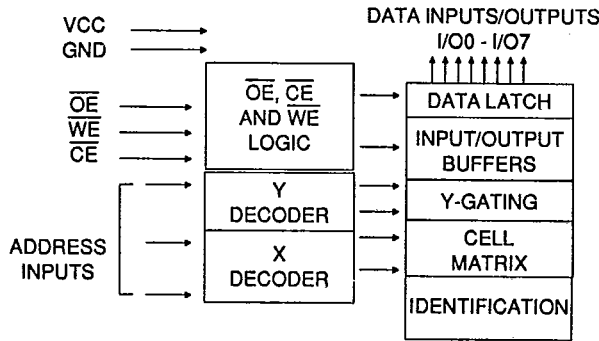
\* = RDY/BUSY (or NC)  
Note: PLCC package pins 1 and 17 are DON'T CONNECT.





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**Block Diagram**



**Operating Modes**

Mode	$\overline{OE}$	$\overline{CE}$	$\overline{WE}$	I/O
Read	V <sub>IL</sub>	V <sub>IL</sub>	V <sub>IH</sub>	DOUT
Write <sup>(2)</sup>	V <sub>IL</sub>	V <sub>IH</sub>	V <sub>IL</sub>	DIN
Standby/Write Inhibit	V <sub>IH</sub>	X <sup>(1)</sup>	X	High Z
Write Inhibit	X	X	V <sub>IH</sub>	
Write Inhibit	X	V <sub>IL</sub>	X	
Output Disable	X	V <sub>IH</sub>	X	High Z
Chip Erase	V <sub>IL</sub>	V <sub>IH</sub> <sup>(3)</sup>	V <sub>IL</sub>	High Z

Notes: 1. X can be V<sub>IL</sub> or V<sub>IH</sub>.  
 2. Refer to A.C. Programming Waveforms.  
 3. V<sub>IH</sub> = 12.0V ± 0.5V.

**Device Operation**

**READ:** The AT28C64 is accessed like a Static RAM. When CE and OE are low and WE is high, the data stored at the memory location determined by the address pins is asserted on the outputs. The outputs are put in a high impedance state whenever CE or OE is high. This dual line control gives designers increased flexibility in preventing bus contention.

**BYTE WRITE:** Writing data into the AT28C64 is similar to writing into a Static RAM. A low pulse on the WE or CE input with OE high and CE or WE low (respectively) initiates a byte write. The address location is latched on the falling edge of WE (or CE); the new data is latched on the rising edge. Internally, the device performs a self-clear before write. Once a byte write has been started, it will automatically time itself to completion.

**FAST BYTE WRITE:** The AT28C64F offers a byte write time of 200µs maximum. This feature allows the entire device to be rewritten in 1.6 seconds.

**READY/BUSY:** Pin 1 is an open drain READY/BUSY output that can be used to detect the end of a write cycle. RDY/BUSY is actively pulled low during the write cycle and is released at the completion of the write. The open drain connection allows for OR-tying of several devices to the same RDY/BUSY line. Pin 1 is not connected for the AT28C64X.

**DATA POLLING:** The AT28C64 provides DATA POLLING to signal the completion of a write cycle. During a write cycle, an attempted read of the data being written results in the complement of that data for I/O7 (the other outputs are indeterminate). When the write cycle is finished, true data appears on all outputs.

**WRITE PROTECTION:** Inadvertent writes to the device are protected against in the following ways. (a) Vcc sense— if Vcc is below 3.8V (typical) the write function is inhibited. (b) Vcc power on delay— once Vcc has reached 3.8V the device will automatically time out 5ms (typical) before allowing a byte write. (c) Write Inhibit— holding any one of OE low, CE high or WE high inhibits byte write cycles.

**CHIP CLEAR:** The contents of the entire memory of the AT28C64 may be set to the high state by the CHIP CLEAR operation. By setting CE low and OE to 12 volts, the chip is cleared when a 10 msec low pulse is applied to WE.

**DEVICE IDENTIFICATION:** An extra 32 bytes of E<sup>2</sup>PROM memory are available to the user for device identification. By raising A9 to 12 ± 0.5V and using address locations 1FE0H to 1FFFH the additional bytes may be written to or read from in the same manner as the regular memory array.

**AT28C64**

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**Absolute Maximum Ratings\***

Temperature Under Bias.....	-55°C to +125°C
Storage Temperature.....	-65°C to +150°C
All Input Voltages (Including N.C. Pins) with Respect to Ground .....	-0.6V to +6.25V
All Output Voltages with Respect to Ground .....	-0.6V to Vcc +0.6V
Voltage on $\overline{OE}$ and A9 with Respect to Ground .....	-0.6V to +13.5V

\*NOTICE: Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



**D.C. and A.C. Operating Range**

		AT28C64-15	AT28C64-20	AT28C64-25
Operating Temperature (Case)	Com.	0°C - 70°C	0°C - 70°C	0°C - 70°C
	Ind.	-40°C - 85°C	-40°C - 85°C	-40°C - 85°C
	Mil.	-55°C - 125°C	-55°C - 125°C	-55°C - 125°C
Vcc Power Supply		5V±10%	5V±10%	5V±10%

**D.C. Characteristics**

Symbol	Parameter	Condition	Min	Max	Units
I <sub>LI</sub>	Input Load Current	V <sub>IN</sub> =0V to Vcc + 1V		10	µA
I <sub>LO</sub>	Output Leakage Current	V <sub>IO</sub> =0V to Vcc		10	µA
I <sub>SB1</sub>	Vcc Standby Current CMOS	$\overline{CE}$ =Vcc-0.3V to Vcc + 1.0V		100	µA
I <sub>SB2</sub>	Vcc Standby Current TTL	$\overline{CE}$ =2.0V to Vcc + 1.0V	Com.	2	mA
			Ind., Mil.	3	mA
I <sub>CC</sub>	Vcc Active Current A.C.	f=5MHz; I <sub>OUT</sub> =0mA $\overline{CE}$ =V <sub>IL</sub>	Com.	30	mA
			Ind., Mil.	45	mA
V <sub>IL</sub>	Input Low Voltage			0.8	V
V <sub>IH</sub>	Input High Voltage		2.0		V
V <sub>OL</sub>	Output Low Voltage	I <sub>OL</sub> =2.1mA =4.0mA for RDY/ $\overline{BUSY}$		.45	V
V <sub>OH</sub>	Output High Voltage	I <sub>OH</sub> =-400µA	2.4		V

**Pin Capacitance (f=1MHz T=25°C) <sup>(4)</sup>**

	Typ	Max	Units	Conditions
C <sub>IN</sub>	4	6	pF	V <sub>IN</sub> = 0V
C <sub>OUT</sub>	8	12	pF	V <sub>OUT</sub> = 0V



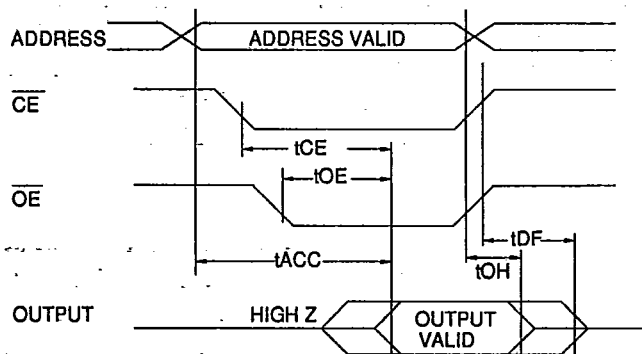


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**A.C. Read Characteristics**

Symbol	Parameter	AT28C64-15		AT28C64-20		AT28C64-25		Units
		Min	Max	Min	Max	Min	Max	
$t_{ACC}$	Address to Output Delay		150		200		250	ns
$t_{CE}^{(1)}$	$\overline{CE}$ to Output Delay		150		200		250	ns
$t_{OE}^{(2)}$	$\overline{OE}$ to Output Delay	10	70	10	80	10	100	ns
$t_{DF}^{(3,4)}$	$\overline{CE}$ or $\overline{OE}$ High to Output Float	0	50	0	55	0	60	ns
$t_{OH}$	Output Hold from $\overline{OE}$ , $\overline{CE}$ or Address, whichever occurred first	0		0		0		ns

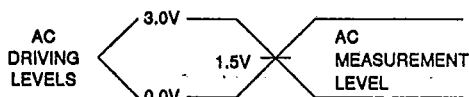
**A.C. Read Waveforms**



**Notes:**

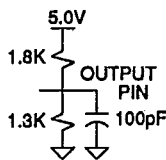
- $\overline{CE}$  may be delayed up to  $t_{ACC} - t_{CE}$  after the address transition without impact on  $t_{ACC}$ .
- $\overline{OE}$  may be delayed up to  $t_{CE} - t_{OE}$  after the falling edge of  $\overline{CE}$  without impact on  $t_{CE}$  or by  $t_{ACC} - t_{OE}$  after an address change without impact on  $t_{ACC}$ .
- $t_{DF}$  is specified from  $\overline{OE}$  or  $\overline{CE}$  whichever occurs first ( $C_L = 5pF$ ).
- This parameter is characterized and is not 100% tested.

**Input Test Waveforms and Measurement Level**



$t_r, t_f < 20ns$

**Output Test Load**



**AT28C64**

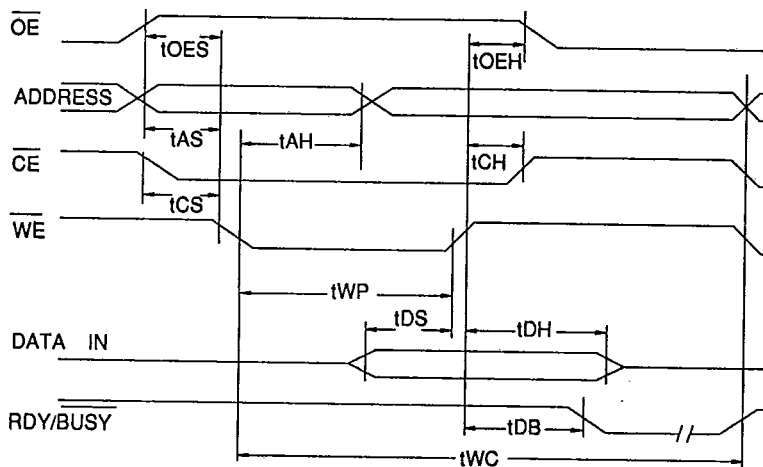
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**A.C. Write Characteristics**

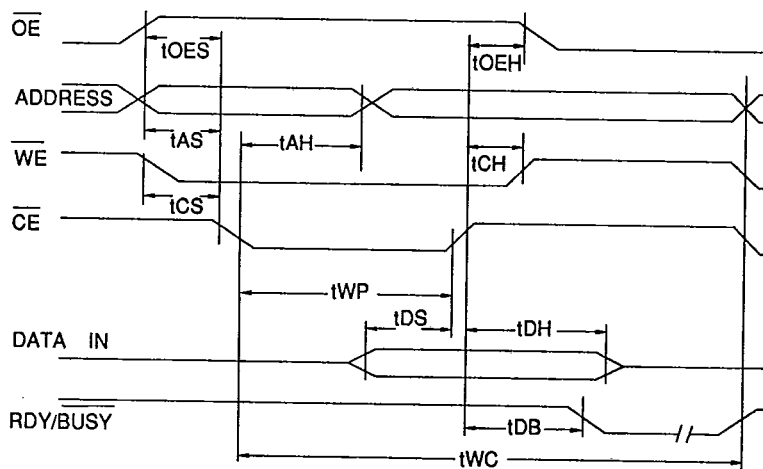
Symbol	Parameter	Min	Typ	Max	Units
tAS, tOES	Address, $\overline{OE}$ Set-up Time	10			ns
tAH	Address Hold Time	50			ns
tWP	Write Pulse Width ( $\overline{WE}$ or $\overline{CE}$ )	100		1000	ns
tDS	Data Set-up Time	50			ns
tDH, tOEH	Data, $\overline{OE}$ Hold Time	10			ns
tDB	Time to Device Busy			50	ns
twc	Write Cycle Time	AT28C64	0.5	1.0	ms
		AT28C64E/F	100	200	$\mu$ s



**A.C. Write Waveforms-  $\overline{WE}$  Controlled**



**A.C. Write Waveforms-  $\overline{CE}$  Controlled**





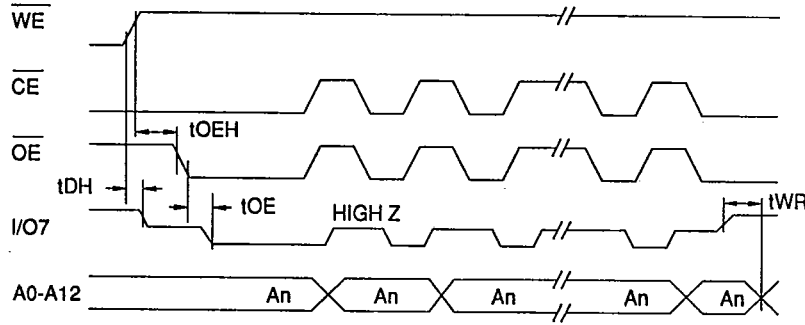
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**Data Polling Characteristics<sup>(1)</sup>**

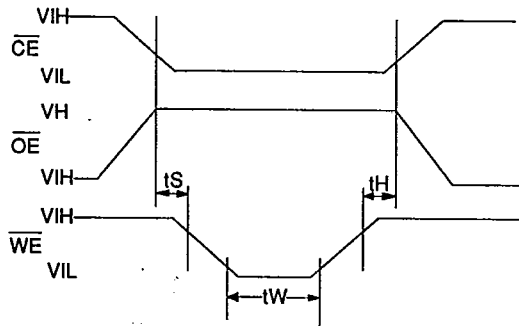
Symbol	Parameter	Min	Typ	Max	Units
t <sub>DH</sub>	Data Hold Time	10			ns
t <sub>OEH</sub>	$\overline{\text{OE}}$ Hold Time	10			ns
t <sub>OE</sub>	$\overline{\text{OE}}$ to Output Delay			100	ns
t <sub>WR</sub>	Write Recovery Time	0			ns

Note: 1. These parameters are characterized and not 100% tested.

**Data Polling Waveforms**



**Chip Erase Waveforms**

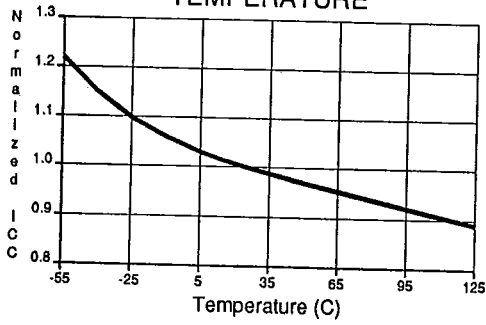


$t_{\text{S}} = t_{\text{H}} = 1\mu\text{sec (min.)}$   
 $t_{\text{W}} = 10\text{msec (min.)}$   
 $V_{\text{H}} = 12.0\text{V} \pm 0.5\text{V}$

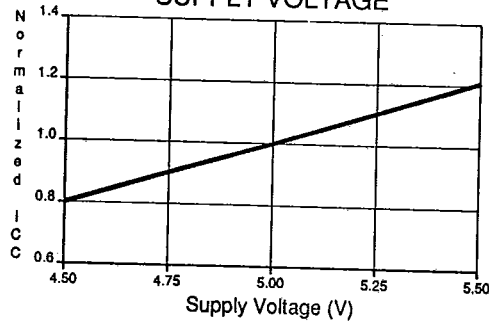
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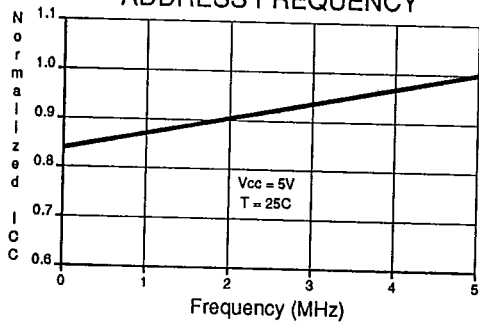
NORMALIZED SUPPLY CURRENT vs. TEMPERATURE



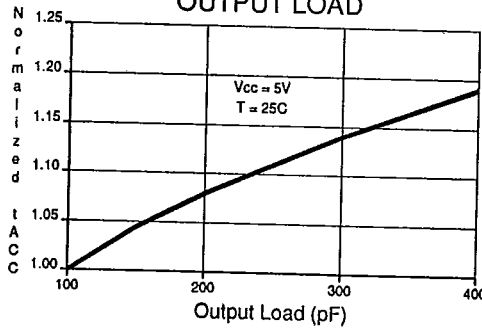
NORMALIZED SUPPLY CURRENT vs. SUPPLY VOLTAGE



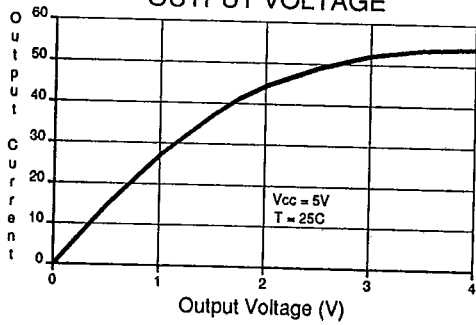
NORMALIZED SUPPLY CURRENT vs. ADDRESS FREQUENCY



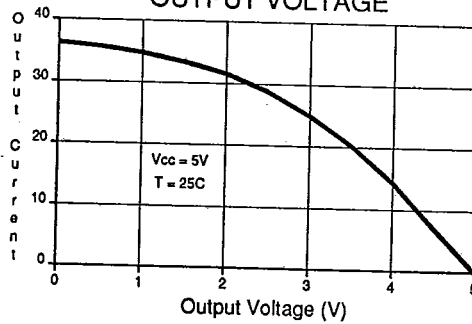
NORMALIZED ACCESS TIME vs. OUTPUT LOAD



OUTPUT SINK CURRENT vs. OUTPUT VOLTAGE



OUTPUT SOURCE CURRENT vs. OUTPUT VOLTAGE





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## Ordering Information

t <sub>acc</sub> (ns)	I <sub>cc</sub> (mA)		Ordering Code	Package	Operation Range				
	Active	Standby							
150	30	0.1	AT28C64(E,F)-15DC	28D6	Commercial (0°C to 70°C)				
			AT28C64(E,F)-15FC	28F					
			AT28C64(E,F)-15JC	32J					
			AT28C64(E,F)-15LC	32L					
			AT28C64(E,F)-15PC	28P6					
			AT28C64(E,F)-15SC	28S					
150	45	0.1	AT28C64(E,F)-15DI	28D6	Industrial (-40°C to 85°C)				
			AT28C64(E,F)-15FI	28F					
			AT28C64(E,F)-15JI	32J					
			AT28C64(E,F)-15LI	32L					
			AT28C64(E,F)-15PI	28P6					
			AT28C64(E,F)-15SI	28S					
					AT28C64(E,F)-15DM	28D6	Military (-55°C to 125°C)		
					AT28C64(E,F)-15FM	28F			
					AT28C64(E,F)-15LM	32L			
							AT28C64(E,F)-15DM/883	28D6	Military/883C Class B, Fully Compliant (-55°C to 125°C)
							AT28C64(E,F)-15FM/883	28F	
							AT28C64(E,F)-15LM/883	32L	
200	30	0.1	AT28C64(E,F)-20DC	28D6	Commercial (0°C to 70°C)				
			AT28C64(E,F)-20FC	28F					
			AT28C64(E,F)-20JC	32J					
			AT28C64(E,F)-20LC	32L					
			AT28C64(E,F)-20PC	28P6					
			AT28C64(E,F)-20SC	28S					
200	45	0.1	AT28C64(E,F)-20DI	28D6	Industrial (-40°C to 85°C)				
			AT28C64(E,F)-20FI	28F					
			AT28C64(E,F)-20JI	32J					
			AT28C64(E,F)-20LI	32L					
			AT28C64(E,F)-20PI	28P6					
			AT28C64(E,F)-20SI	28S					
					AT28C64(E,F)-20DM	28D6	Military (-55°C to 125°C)		
					AT28C64(E,F)-20FM	28F			
					AT28C64(E,F)-20LM	32L			
							AT28C64(E,F)-20DM/883	28D6	Military/883C Class B, Fully Compliant (-55°C to 125°C)
							AT28C64(E,F)-20FM/883	28F	
							AT28C64(E,F)-20LM/883	32L	
250	30	0.1	AT28C64(E,F)-25DC	28D6	Commercial (0°C to 70°C)				
			AT28C64(E,F)-25FC	28F					
			AT28C64(E,F)-25JC	32J					
			AT28C64(E,F)-25LC	32L					
			AT28C64(E,F)-25PC	28P6					
			AT28C64(E,F)-25SC	28S					
			AT28C64-W	DIE					



**AT28C64/X**

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**Ordering Information**

tACC (ns)	Icc (mA)		Ordering Code	Package	Operation Range
	Active	Standby			
250	45	0.1	AT28C64(E,F)-25DI AT28C64(E,F)-25FI AT28C64(E,F)-25JI AT28C64(E,F)-25LI AT28C64(E,F)-25PI AT28C64(E,F)-25SI	28D6 28F 32J 32L 28P6 28S	Industrial (-40°C to 85°C)
			AT28C64(E,F)-25DM AT28C64(E,F)-25FM AT28C64(E,F)-25LM	28D6 28F 32L	Military (-55°C to 125°C)
			AT28C64(E,F)-25DM/883 AT28C64(E,F)-25FM/883 AT28C64(E,F)-25LM/883	28D6 28F 32L	Military/883C Class B, Fully Compliant (-55°C to 125°C)
300	45	0.1	AT28C64(E,F)-30DM/883 AT28C64(E,F)-30FM/883 AT28C64(E,F)-30LM/883	28D6 28F 32L	Military/883C Class B, Fully Compliant (-55°C to 125°C)
350	45	0.1	AT28C64(E,F)-35DM/883 AT28C64(E,F)-35FM/883 AT28C64(E,F)-35LM/883	28D6 28F 32L	Military/883C Class B, Fully Compliant (-55°C to 125°C)
450	45	0.1	AT28C64(E,F)-45DM/883 AT28C64(E,F)-45FM/883 AT28C64(E,F)-45LM/883	28D6 28F 32L	Military/883C Class B, Fully Compliant (-55°C to 125°C)
150	45	0.1	5962-87514 17 UX 5962-87514 17 XX 5962-87514 17 YX	32K 28D6 32L	Military/883C Class B, Fully Compliant (-55°C to 125°C)
200	45	0.1	5962-87514 16 UX 5962-87514 16 XX 5962-87514 16 YX	32K 28D6 32L	Military/883C Class B, Fully Compliant (-55°C to 125°C)
250	45	0.1	5962-87514 15 UX 5962-87514 15 XX 5962-87514 15 YX 5962-87514 15 ZX	32K 28D6 32L 28F	Military/883C Class B, Fully Compliant (-55°C to 125°C)
300	45	0.1	5962-87514 14 UX 5962-87514 14 XX 5962-87514 14 YX	32K 28D6 32L	Military/883C Class B, Fully Compliant (-55°C to 125°C)
350	45	0.1	5962-87514 13 UX 5962-87514 13 XX 5962-87514 13 YX 5962-87514 13 ZX	32K 28D6 32L 28F	Military/883C Class B, Fully Compliant (-55°C to 125°C)





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## Ordering Information

Package Type	
<b>28D6</b>	28 Lead, 0.600" Wide, Non-Windowed, Ceramic Dual Inline Package (Cerdip)
<b>28F</b>	28 Lead, Non-Windowed, Ceramic Bottom-Brazed Flat Package (Flatpack)
<b>32J</b>	32 Lead, Plastic J-Leaded Chip Carrier (PLCC)
<b>32K</b>	32 Lead, Non-Windowed, Ceramic J-Leaded Chip Carrier (JLCC)
<b>32L</b>	32 Pad, Non-Windowed, Ceramic Leadless Chip Carrier (LCC)
<b>28P6</b>	28 Lead, 0.600" Wide, Plastic Dual Inline Package (PDIP)
<b>28S</b>	28 Lead, 0.300" Wide, Plastic Gull Wing, Small Outline (SOIC)
<b>W</b>	Die
Options	
<b>Blank</b>	Standard Device: Endurance = 10K Write Cycles; Write Time = 1 ms
<b>E</b>	High Endurance Option: Endurance = 100K Write Cycles; Write Time = 200 $\mu$ s
<b>F</b>	Fast Write Option: Write Time = 200 $\mu$ s

**AT28C64/X**

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**Ordering Information**

t <sub>acc</sub> (ns)	I <sub>cc</sub> (mA)		Ordering Code	Package	Operation Range	
	Active	Standby				
150	30	0.1	AT28C64X-15DC	28D6	Commercial (0°C to 70°C)	
			AT28C64X-15FC	28F		
			AT28C64X-15JC	32J		
			AT28C64X-15LC	32L		
			AT28C64X-15PC	28P6		
			AT28C64X-15SC	28S		
150	45	0.1	AT28C64X-15DI	28D6	Industrial (-40°C to 85°C)	
			AT28C64X-15FI	28F		
			AT28C64X-15JI	32J		
			AT28C64X-15LI	32L		
			AT28C64X-15PI	28P6		
			AT28C64X-15SI	28S		
				AT28C64X-15DM	28D6	Military (-55°C to 125°C)
				AT28C64X-15FM	28F	
				AT28C64X-15LM	32L	
		AT28C64X-15DM/883	28D6	Military/883C Class B, Fully Compliant (-55°C to 125°C)		
		AT28C64X-15FM/883	28F			
		AT28C64X-15LM/883	32L			
200	30	0.1	AT28C64X-20DC	28D6	Commercial (0°C to 70°C)	
			AT28C64X-20FC	28F		
			AT28C64X-20JC	32J		
			AT28C64X-20LC	32L		
			AT28C64X-20PC	28P6		
			AT28C64X-20SC	28S		
200	45	0.1	AT28C64X-20DI	28D6	Industrial (-40°C to 85°C)	
			AT28C64X-20FI	28F		
			AT28C64X-20JI	32J		
			AT28C64X-20LI	32L		
			AT28C64X-20PI	28P6		
			AT28C64X-20SI	28S		
				AT28C64X-20DM	28D6	Military (-55°C to 125°C)
				AT28C64X-20FM	28F	
				AT28C64X-20LM	32L	
		AT28C64X-20DM/883	28D6	Military/883C Class B, Fully Compliant (-55°C to 125°C)		
		AT28C64X-20FM/883	28F			
		AT28C64X-20LM/883	32L			
250	30	0.1	AT28C64X-25DC	28D6	Commercial (0°C to 70°C)	
			AT28C64X-25FC	28F		
			AT28C64X-25JC	32J		
			AT28C64X-25LC	32L		
			AT28C64X-25PC	28P6		
			AT28C64X-25SC	28S		





T-46-13-27

## Ordering Information

t <sub>ACC</sub> (ns)	I <sub>CC</sub> (mA)		Ordering Code	Package	Operation Range
	Active	Standby			
250	45	0.1	AT28C64X-25DI AT28C64X-25FI AT28C64X-25JI AT28C64X-25LI AT28C64X-25PI AT28C64X-25SI	28D6 28F 32J 32L 28P6 28S	Industrial (-40°C to 85°C)
			AT28C64X-25DM AT28C64X-25FM AT28C64X-25LM	28D6 28F 32L	Military (-55°C to 125°C)
			AT28C64X-25DM/883 AT28C64X-25FM/883 AT28C64X-25LM/883	28D6 28F 32L	Military/883C Class B, Fully Compliant (-55°C to 125°C)
300	45	0.1	AT28C64X-30DM/883 AT28C64X-30FM/883 AT28C64X-30LM/883	28D6 28F 32L	Military/883C Class B, Fully Compliant (-55°C to 125°C)
350	45	0.1	AT28C64X-35DM/883 AT28C64X-35FM/883 AT28C64X-35LM/883	28D6 28F 32L	Military/883C Class B, Fully Compliant (-55°C to 125°C)
450	45	0.1	AT28C64X-45DM/883 AT28C64X-45FM/883 AT28C64X-45LM/883	28D6 28F 32L	Military/883C Class B, Fully Compliant (-55°C to 125°C)
150	45	0.1	5962-87514 22 UX 5962-87514 22 XX 5962-87514 22 YX	32K 28D6 32L	Military/883C Class B, Fully Compliant (-55°C to 125°C)
200	45	0.1	5962-87514 21 UX 5962-87514 21 XX 5962-87514 21 YX	32K 28D6 32L	Military/883C Class B, Fully Compliant (-55°C to 125°C)
250	45	0.1	5962-87514 20 UX 5962-87514 20 XX 5962-87514 20 YX 5962-87514 20 ZX	32K 28D6 32L 28F	Military/883C Class B, Fully Compliant (-55°C to 125°C)
300	45	0.1	5962-87514 19 UX 5962-87514 19 XX 5962-87514 19 YX	32K 28D6 32L	Military/883C Class B, Fully Compliant (-55°C to 125°C)
350	45	0.1	5962-87514 18 UX 5962-87514 18 XX 5962-87514 18 YX	32K 28D6 32L	Military/883C Class B, Fully Compliant (-55°C to 125°C)

## Package Type

28D6	28 Lead, 0.600" Wide, Non-Windowed, Ceramic Dual Inline Package (Cerdip)
28F	28 Lead, Non-Windowed, Ceramic Bottom-Brazed Flat Package (Flatpack)
32J	32 Lead, Plastic J-Leaded Chip Carrier (PLCC)
32K	32 Lead, Non-Windowed, Ceramic J-Leaded Chip Carrier (JLCC)
32L	32 Pad, Non-Windowed, Ceramic Leadless Chip Carrier (LCC)
28P6	28 Lead, 0.600" Wide Plastic Dual Inline Package (PDIP)
28S	28 Lead, 0.300" Wide, Plastic Gull Wing Small Outline (SOIC)