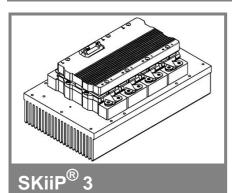
# SKiiP 2013GB122-4DL



2-pack-integrated intelligent Power System

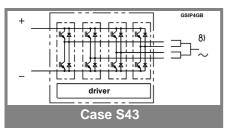
#### **Power section**

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### **Power section features**

- SKiiP technology inside
- SPT (Soft Punch Trough) IGBTs
- CAL diode technology
- Integrated current sensor
- Integrated temperature sensor
- Integrated heat sink
- IEC 60721-3-3 (humidity) class 3K3/IE32 (SKiiP<sup>®</sup> 3 System)
- IEC 60068-1 (climate) 40/125/56
- UL recognized File no. E63532
- with assembly of suitable MKP capacitor per terminal
- AC connection busbars must be connected by the user; copper busbars available on request



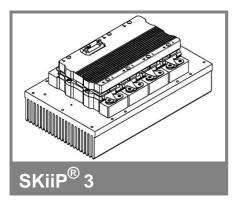
Absolute	Maximum Ratings	$\Gamma_{s} = 25^{\circ}C$ unless otherwise specified				
Symbol	Conditions	Values	Units			
IGBT						
V <sub>CES</sub>		1200	V			
V <sub>CC</sub> <sup>1)</sup>	Operating DC link voltage	900	V			
V <sub>GES</sub>		± 20	V			
I <sub>C</sub>	T <sub>s</sub> = 25 (70) °C	2000 (1500)	А			
Inverse diode						
I <sub>F</sub> = - I <sub>C</sub>	T <sub>s</sub> = 25 (70) °C	1760 (1340)	А			
I <sub>FSM</sub>	T <sub>j</sub> = 150 °C, t <sub>p</sub> = 10 ms; sin.	13500	A			
I²t (Diode)	Diode, T <sub>j</sub> = 150 °C, 10 ms	911	kA²s			
T <sub>j</sub> , (T <sub>stg</sub> )		- 40 + 150 (125)	°C			
V <sub>isol</sub>	rms, AC, 1 min, main terminals to heat sink	3000	V			
I <sub>AC-terminal</sub>	per AC terminal, rms, T <sub>s</sub> = 70 °C,	400	А			
	T <sub>terminal</sub> <115 °C					

Characteristics T <sub>s</sub> = 25°C unless otherwise specif						specifie		
Symbol	Conditions			min.	typ.	max.	Units	
IGBT								
V <sub>CEsat</sub>	I <sub>C</sub> = 1200 A measured at t	A, T <sub>j</sub> = 25 erminal	(125) °C;			2,3 (2,5)	2,6	V
V <sub>CEO</sub>	T <sub>i</sub> = 25 (12	25) °C; at t	erminal			1,1 (1)	1,3 (1,2)	V
r <sub>CE</sub>	$T_{i} = 25 (12)$					1 (1,2)	1,1 (1,4)	mΩ
I <sub>CES</sub>	V <sub>GE</sub> = 0 V, T <sub>i</sub> = 25 (12		ES'			4,8 (144)		mA
E <sub>on</sub> + E <sub>off</sub>	$I_{\rm C} = 1200$	A, V <sub>CC</sub> = 6	00 V			360		mJ
	T <sub>j</sub> = 125 °C	C, V <sub>CC</sub> = 9	00 V			635		mJ
R <sub>CC+EE</sub>	terminal ch	nip, T <sub>i</sub> = 25	5 °C			0,13		mΩ
L <sub>CE</sub>	top, botton	n				3		nH
C <sub>CHC</sub>	per phase	AC-side				6,8		nF
Inverse o	diode							
V <sub>F</sub> = V <sub>EC</sub>	I <sub>F</sub> = 1200 / measured at t		(125) °C			1,95 (1,7)	2,1	V
V <sub>TO</sub>	T <sub>i</sub> = 25 (12	25) °C				1,1 (0,8)	1,2 (0,9)	V
r <sub>T</sub>	$T_{i} = 25 (12)$	25) °C				0,7 (0,8)	0,8 (0,9)	mΩ
E <sub>rr</sub>	$I_{\rm C} = 1200$	A, V <sub>CC</sub> = 6	00 V			96		mJ
	T <sub>j</sub> = 125 °C	C, V <sub>CC</sub> = 9	00 V			122		mJ
Mechani	cal data							
M <sub>dc</sub>	DC termin	,			6		8	Nm
М <sub>ас</sub>	AC termina				13		15	Nm
W	SKiiP® 3 S	ystem w/c	heat sink			3,1		kg
W	heat sink					9,7		kg
						SKF 16B- mperature		s"
R <sub>th(j-s)I</sub>	per IGBT				1	•	0,015	K/W
R <sub>th(j-s)D</sub>	per diode						0,029	K/W
Z <sub>th</sub>	R <sub>i</sub> (mK/W) (max. values)				<u>.</u>	1		
	1	2	3	4	1	2	3	4
Z <sub>th(j-r)I</sub>	5,6	6	6,4	0	363	0,18	0,04	1
Z <sub>th(j-r)D</sub>	10	8,4	14,8	14,8	50	5	0,25	0,04
Z <sub>th(r-a)</sub>	3,1	17,3	3,7	0,9	230	78	13	0,4

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#### 05-07-2007 HER

# SKiiP 2013GB122-4DL



## 2-pack-integrated intelligent Power System

2-pack integrated gate driver SKiiP 2013GB122-4DL

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### Gate driver features

- CMOS compatible inputs
- Wide range power supply
- Integrated circuitry to sense phase current, heat sink temperature and DC-bus voltage (option)
- Short circuit protection
- Over current protection
- Over voltage protection (option)
- Power supply protected against under voltage
- Interlock of top/bottom switch
- Isolation by transformers
- Fibre optic interface (option for GB-types only)
- IEC 60068-1 (climate) 40/85/56
- UL recognized file no. 242581

Absolute Maximum Ratings		T <sub>a</sub> = 25°C unless otherwise specified		
Symbol	Conditions	Values	Units	
V <sub>S2</sub>	unstabilized 24 V power supply	30	V	
V <sub>i</sub>	input signal voltage (high)	15 + 0,3	V	
dv/dt	secondary to primary side	75	kV/µs	
V <sub>isollO</sub>	input / output (AC, rms, 2s)	3000	V	
VisoIPD	partial discharge extinction voltage, rms, $Q_{PD} \leq 10 \text{ pC}$ ;	1170	V	
V <sub>isol12</sub>	output 1 / output 2 (AC, rms, 2s)	1500	V	
f <sub>sw</sub>	switching frequency	8	kHz	
f <sub>out</sub>	output frequency for I <sub>peak(1)</sub> =I <sub>C</sub>	8	kHz	
T <sub>op</sub> (T <sub>stg</sub> )	operating / storage temperature	- 40 + 85	°C	

Characte	ristics	(T <sub>a</sub>			= 25°C)
Symbol	Conditions	min.	typ.	max.	Units
V <sub>S2</sub>	supply voltage non stabilized	13	24	30	V
I <sub>S2</sub>	V <sub>S2</sub> = 24 V	324+39*f/kHz+0,00011*(I <sub>AC</sub> /A) <sup>2</sup>			mA
V <sub>iT+</sub>	input threshold voltage (High)			12,3	V
V <sub>iT-</sub>	input threshold voltage (Low)	4,6			V
R <sub>IN</sub>	input resistance		10		kΩ
CIN	input capacitance		1		nF
t <sub>d(on)IO</sub>	input-output turn-on propagation time		1,3		μs
t <sub>d(off)IO</sub>	input-output turn-off propagation time		1,3		μs
t <sub>pERRRESET</sub>	error memory reset time		9		μs
t <sub>TD</sub>	top / bottom switch interlock time		3,3		μs
I <sub>analogOUT</sub>	max. 5mA; 8 V corresponds to 15 V supply voltage for external components		2000		A
I <sub>s1out</sub>	max. load current			50	mA
I <sub>TRIPSC</sub>	over current trip level				
	$(I_{analog} OUT = 10 V)$		2500		А
T <sub>tp</sub>	over temperature protection	110		120	°C
	U <sub>DC</sub> -protection ( U <sub>analog OUT</sub> = 9 V);	i	not implemente	d	V
	(option for GB types)				

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