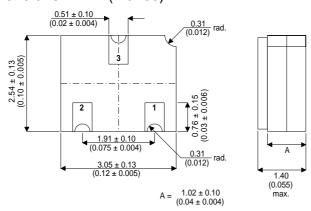


## 1N5311CSM

## **MECHANICAL DATA**

Dimensions in mm(inches)



# LCC1 PACKAGE (SOT23 Compatible)

### **Underside View**

Pad 1 – Anode Pad 2 – N/C

Pad 3 - Cathode

# VOLTAGE REGULATOR DIODE IN A CERAMIC SURFACE MOUNT PACKAGE FOR HI-REL APPLICATIONS

## **FEATURES**

- HERMETIC CERAMIC SURFACE MOUNT PACKAGE
- SCREENING OPTIONS AVAILABLE

## **ABSOLUTE MAXIMUM RATINGS**

$P_{TOT}$	Power Dissipation	$T_{AMB} = 25^{\circ}C$	400mW
	Derate above 25°C		3.2mW/°C
$T_OP$	Maximum Operating Ambient Temperature		−65 to +150°C
$T_{STG}$	Storage Temperature Range		−65 to +175°C
$R_{\theta JA}$	Thermal Resistance Junction to Ambier	nt	312°C/W

# **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise stated)

	Parameter	Test Conditions	Min.	Тур.	Max.	Units
$V_Z$	Zener Voltage	I <sub>PINCH</sub> (mA) @ V <sub>S</sub> = 25.0V	3.24	3.60	3.96	V
$V_{L}$	Maximum Limiting Voltage	$I_L = 0.8 I_{PINCH(min)}$			2.50	
Z <sub>S</sub> *	Minimum Dynamic Impedance	V <sub>S</sub> = 25.0V	0.265			m()
$Z_k^*$	Minimum Knee Impedance	V <sub>K</sub> = 6.0V	0.020			mΩ

 $<sup>^{\</sup>star}$  Z $_{
m S}$  is dervived by superimposing a 90Hz RMS signal equal to 10% of V $_{
m S}$  onto V $_{
m S}$ .

Similarly,  $Z_K$  is dervived by superimposing a 90Hz RMS signal equal to 10% of  $V_S$  onto  $V_K$ .

Semelab Plc reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by Semelab is believed to be both accurate and reliable at the time of going to press. However Semelab assumes no responsibility for any errors or omissions discovered in its use. Semelab encourages customers to verify that datasheets are current before placing orders.

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