

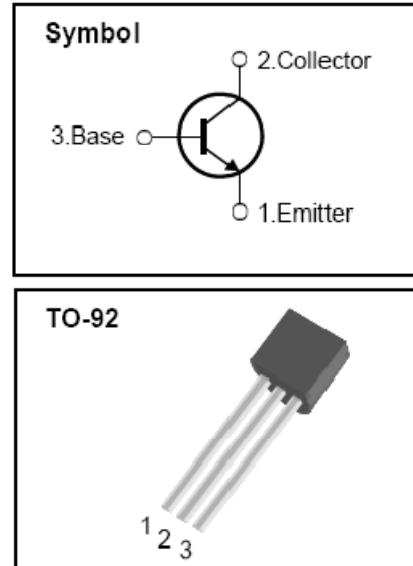
## *High Voltage Fast-Switching NPN Power Transistor*

### Features

- ◆ Very High Switching Speed
- ◆ High Voltage Capability
- ◆ Wide Reverse Bias SOA

### General Description

This Device is designed for high voltage, High speed switching characteristics required such as lighting system, switching mode power supply.



### Absolute Maximum Ratings

Symbol	Parameter	Test Conditions	Value	Units
$V_{CES}$	Collector-Emitter Voltage	$V_{BE} = 0$	600	V
$V_{CEO}$	Collector-Emitter Voltage	$I_B = 0$	400	V
$V_{EBO}$	Emitter-Base Voltage	$I_C = 0$	9.0	V
$I_C$	Collector Current		1.25	A
$I_{CP}$	Collector pulse Current		2.5	A
$I_B$	Base Current		-	A
$I_{BM}$	Base Peak Current	$t_P = 5ms$	-	A
$P_c$	Total Dissipation at $T_c = 25^\circ C$		12	W
	Total Dissipation at $T_a = 25^\circ C$		0.8	
$T_J$	Operation Junction Temperature		- 40 ~ 150	°C
$T_{STG}$	Storage Temperature		- 40 ~ 150	°C

Tc: Case temperature (good cooling)

Ta: Ambient temperature (without heat sink)

# SBN13002

## Electrical Characteristics ( $T_C=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Value			Units
			Min	Typ	Max	
$BV_{CBO}$	Collector-Base Breakdown Voltage	$I_C=0.5\text{mA}, I_E=0$	600			V
$BV_{CEO}$	Collector-Emitter Breakdown Voltage	$I_C=10\text{mA}, I_B=0$	400	-	-	V
$V_{CE(\text{sat})}$	Collector-Emitter Saturation Voltage	$I_C=200\text{mA}, I_B=100\text{mA}$	-	-	1.6	V
$V_{BE(\text{sat})}$	Base-Emitter Saturation Voltage	$I_C=200\text{mA}, I_B=100\text{mA}$	-	-	1.2	V
$I_{CBO}$	Collector-Base Cutoff Current	$V_{CB}=550V, I_E=0\text{mA}$	-	-	10	$\mu A$
$I_{CEO}$	Collector-Emitter Cutoff Current	$V_{CE}=400V, I_B=0\text{mA}$	-	-	20	$\mu A$
$I_{EBO}$	Emitter- Base Cutoff Current	$V_{EB}=9V, I_C=0\text{mA}$	-	-	20	$\mu A$
$h_{FE}$	DC Current Gain	$V_{CE}=20V, I_C=20\text{mA}$ $V_{CE}=5V, I_C=1\text{mA}$	10 9		40	
$t_s$ $t_f$	Storage Time Fall Time	$V_{CC}=250V$ $I_C=5 I_B$ $I_{B1}=- I_{B2}=0.04A$	2 -	-	- 0.8	$\mu s$

**Note:**

Pulse Test : Pulse width 300, Duty cycle 2%

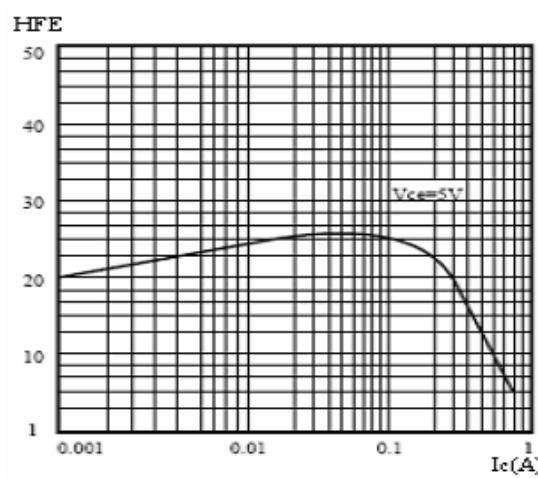


Fig. 1 DC Current Gain

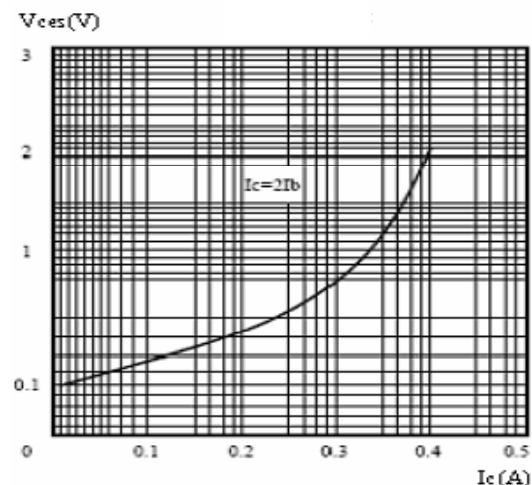


Fig. 2 Saturation Voltage

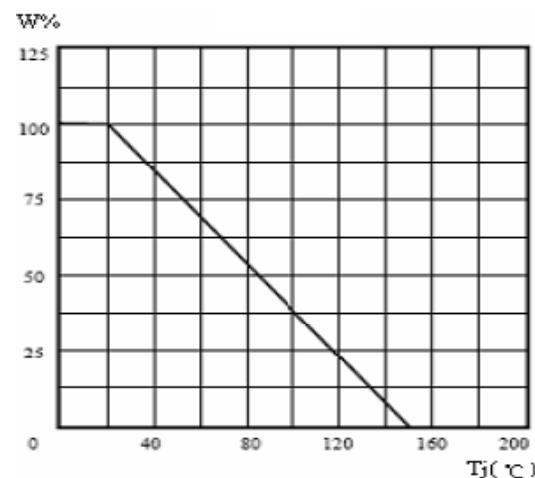


Fig. 3 Power Derating

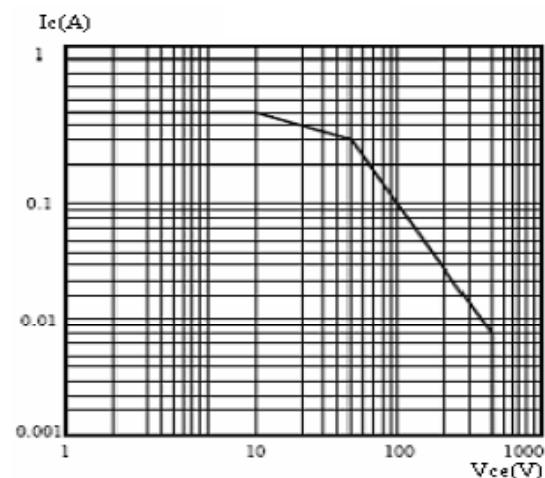


Fig. 4 Safe Operation Area

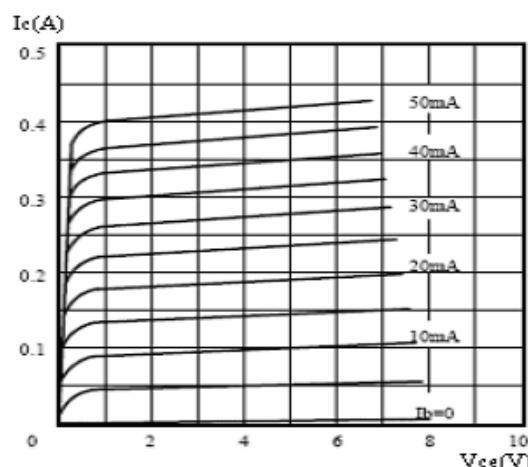
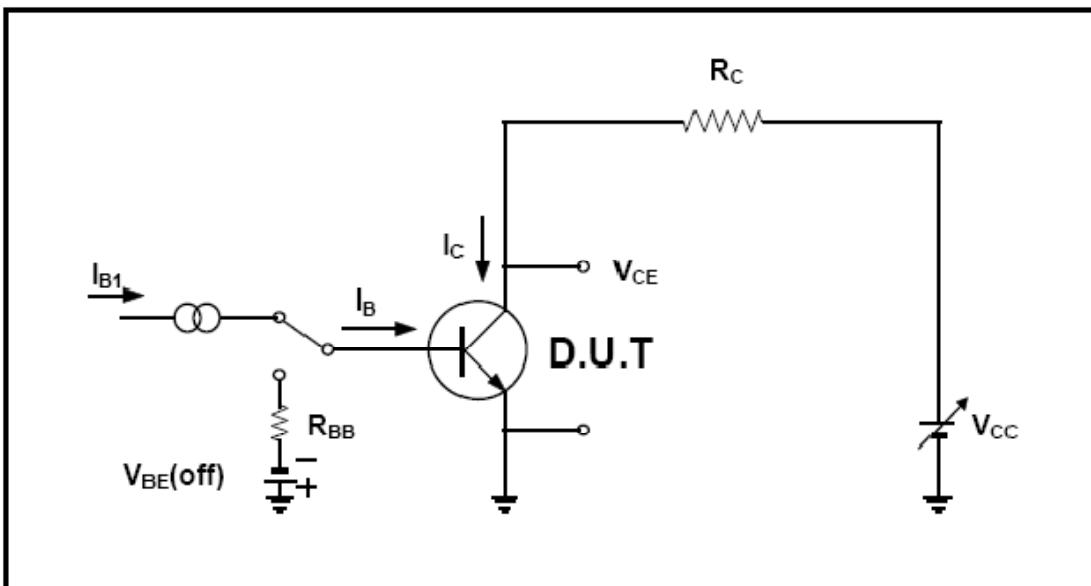
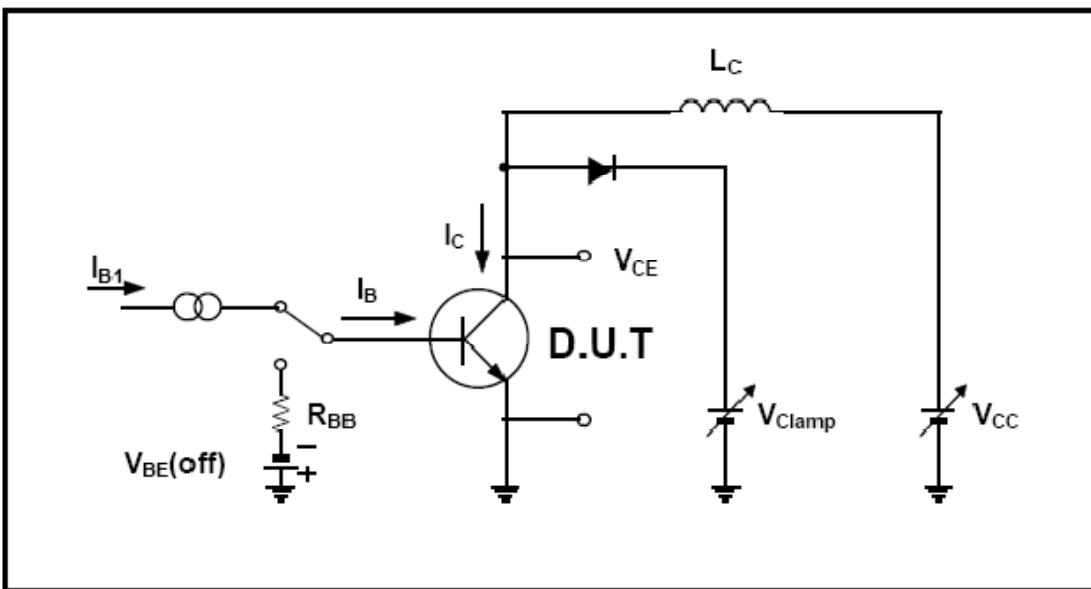


Fig. 5 Static Characteristics

## Resistive Load Switching Test Circuit



## Inductive Load Switching & RBSOA Test Circuit



**TO-92 Package Dimension**

Dim.	mm			Inch		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A		4.2			0.165	
B			3.7			0.146
C	4.43		4.83	0.174		0.190
D	14.07		14.87	0.554		0.585
E			0.4			0.016
F	4.43		4.83	0.174		0.190
G			0.45			0.017
H		1.27			0.050	
I		1.27			0.050	
J	0.33		0.48	0.013		0.019

