

TÜV MANAGEMENT SERVICE

An ISO/TS16949 and ISO 9001 Certified Company

NPN SILICON PLANAR TRANSISTORS



CIL351/352

TO-18 Metal Can Package

ABSOLUTE MAXIMUM RATINGS

DESCRIPTION	SYMBOL	VALUE	UNIT
Collector Emitter Voltage	V_{CEO}	70	V
Collector Base Voltage	V_{CBO}	75	V
Emitter Base Voltage	V_{EBO}	6.0	V
Collector Current Continuous	I _C	200	mA
Power Dissipation @ T _a =25°C	P_{D}	300	mW
Derate Above 25°C		1.72	mW/ ºC
Power Dissipation @ T _c =25°C	P_{D}	750	mW
Derate Above 25°C		4.29	mW/ ºC
Operating And Storage Junction Temperature Range	T _j , T _{stg}	- 65 to +200	°C

THERMAL CHARACTERISTICS

Junction to Ambient in free air	R _{th (j-a)}	583	°C/W
Junction to Case	R _{th (j-c)}	233	°C/W

ELECTRICAL CHARACTERISTICS (T_a=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Collector Emitter Voltage	V_{CEO}	$I_C=1$ mA, $I_B=0$	70			V
Collector Base Voltage	V_{CBO}	$I_C = 100 \mu A, I_E = 0$	75			٧
Emitter Base Voltage	V_{EBO}	$I_E=100\mu A, I_C=0$	6.0			V
Collector Cut Off Current	I _{CBO}	$V_{CB}=20V$, $I_{E}=0$			25	nA
DC Current Gain	h _{FE}	$I_C=1$ mA, $V_{CE}=10$ V				
		CIL351	100		250	
		CIL352	200		480	
Collector Emitter Saturation Voltage	*V _{CE (sat)}	$I_C=10$ mA, $I_B=0.5$ mA			0.25	V
		$I_C=100$ mA, $I_B=5$ mA			0.60	V
Base Emitter on Voltage	V _{BE (on)}	$I_C=10$ mA, $V_{CE}=5$ V			1.0	V

DYNAMIC CHARACTERISTICS

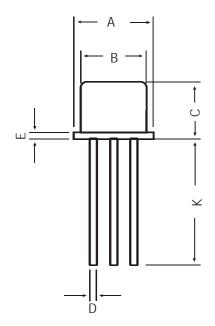
DESCRIPTION	SYMBOL	TEST CONDITION MIN		TYP	MAX	UNIT
Transition Frequency	f _T	$I_C=10$ mA, $V_{CE}=5$ V, $f=100$ MHz		100		MHz

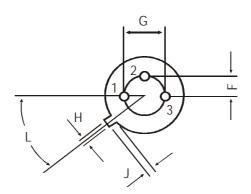
*Pulse Condition: Pulse Width <300ms, Duty Cycle <2%

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	DIM	MIN	MAX	
	Α	5.24	5.84	
	В	4.52	4.97	
	С	4.31	5.33	
	D	0.40	0.53	
	E		0.76	
diminsions in mm.	F		1.27	
	G		2.97	
)ns	Н	0.91	1.17	
nsic	J	0.71	1.21	
Jimi	K	12.70	_	
W (L	45 DEG		



PIN CONFIGURATION

- 1. EMITTER
- 2. BASE3. COLLECTOR

Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-18	1K/polybag	350 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	34 kgs

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Customer Notes CIL351/352

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Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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