

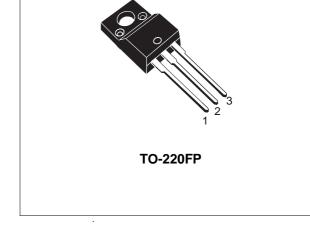
BUL138FP

HIGH VOLTAGE FAST-SWITCHING NPN POWER TRANSISTOR

- STMicroelectronics PREFERRED SALESTYPE
- NPN TRANSISTOR
- HIGH VOLTAGE CAPABILITY
- LOW SPREAD OF DYNAMIC PARAMETERS
- MINIMUM LOT-TO-LOT SPREAD FOR RELIABLE OPERATION
- VERY HIGH SWITCHING SPEED
- FULLY CHARACTERIZED AT 125°C
- FULLY INSULATED PACKAGE (U.L. COMPLIANT) FOR EASY MOUNTING

APPLICATIONS

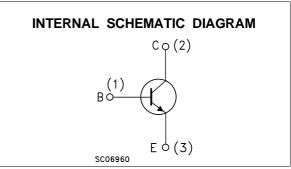
- ELECTRONIC BALLASTS FOR FLUORESCENT LIGHTING
- FLYBACK AND FORWARD SINGLE TRANSISTOR LOW POWER CONVERTERS



DESCRIPTION

The BUL138FP is manufactured using high voltage Multi Epitaxial Planar technology for high switching speeds and high voltage capability. It uses a Cellular Emitter structure with planar edge termination to enhance switching speeds.

The BUL series is designed for use in lighting applications and low cost switch-mode power supplies.



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit	
V _{CES}	Collector-Emitter Voltage (V _{BE} = 0)	800	V	
V _{CEO}	Collector-Emitter Voltage (I _B = 0)	400	V	
V _{EBO}	Emitter-Base Voltage (I _C = 0)	9	V	
Ic	Collector Current	5	A	
Ісм	Collector Peak Current (tp < 5 ms)	10	Α	
Ι _Β	Base Current	2	Α	
I _{BM}	Base Peak Current (t _p < 5 ms)	4	A	
P _{tot}	Total Dissipation at T _c = 25 °C	33	W	
V _{isol}	Insulation Withstand Voltage (RMS) from All Three Leads to Exernal Heatsink	1500	V	
T _{stg}	Storage Temperature	-65 to 150	°C	
Tj	Max. Operating Junction Temperature	150	°C	

September 2003 1/6

THERMAL DATA

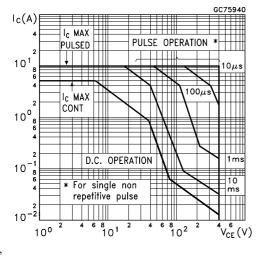
R _{thj-case}	Thermal Resistance Junction-case	Max	3.8	°C/W
$R_{thj-amb}$	Thermal Resistance Junction-ambient	Max	62.5	°C/W

ELECTRICAL CHARACTERISTICS (T_{case} = 25 °C unless otherwise specified)

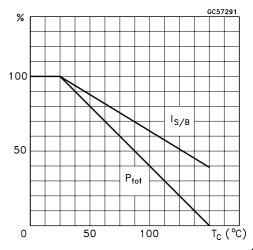
Symbol	Parameter Test Conditions		Min.	Тур.	Max.	Unit
I _{CES}	Collector Cut-off Current (V _{BE} = 0)	V _{CE} = 800 V V _{CE} = 800 V T _j = 125 °C			100 500	μΑ μΑ
ICEO	Collector Cut-off Current (I _B = 0)	V _{CE} = 400 V			250	μΑ
V _{CEO(sus)*}	Collector-Emitter Sustaining Voltage (I _B = 0)	I _C = 100 mA L = 25 mH	400			V
V _{EBO}	Emitter-Base Voltage	I _E = 10 mA	9			V
V _{CE(sat)} *	Collector-Emitter Saturation Voltage	$\begin{array}{llllllllllllllllllllllllllllllllllll$		0.7	0.5 0.7 1 1	V V V V
V _{BE(sat)} *	Base-Emitter Saturation Voltage	I _C = 1 A			1.1 1.3 1.5	V V V
h _{FE} *	DC Current Gain	I _C = 2 A	8 10		40	
t _s	INDUCTIVE LOAD Storage Time Fall Time	$ \begin{aligned} &I_{C} = 2 \; A & &I_{B1} = 0.4 \; A \\ &V_{BE(off)} = \text{-5 V} & &R_{BB} = 0 \; \Omega \\ &V_{CL} = 250 \; V & &L = 200 \; \mu H \end{aligned} $		0.7 50	1.4 100	μs ns
ts tf	INDUCTIVE LOAD Storage Time Fall Time	$\begin{array}{ll} I_{C} = 2 \; A & I_{B1} = 0.4 \; A \\ V_{BE(off)} = \text{-5V} & R_{BB} = 0 \; \Omega \\ V_{CL} = 250 \; V & L = 200 \; \mu\text{H} \\ T_{j} = 125 \; ^{\circ}\text{C} \end{array}$		1 75		μs ns

^{*} Pulsed: Pulse duration = 300 μs, duty cycle 1.5 %

Safe Operating Areas

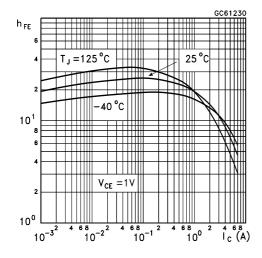


Derating Curve

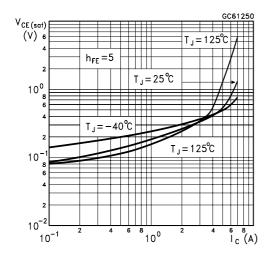


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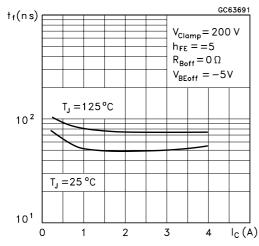
DC Current Gain



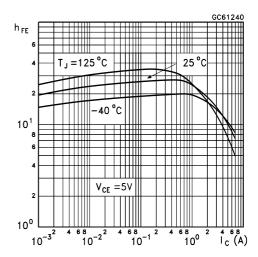
Collector-Emitter Saturation Voltage



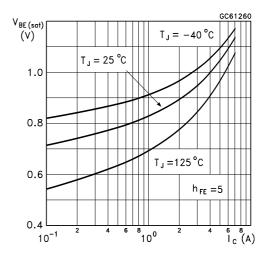
Inductive Fall Time



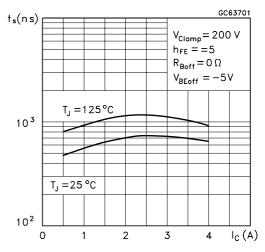
DC Current Gain



Base-Emitter Saturation Voltage

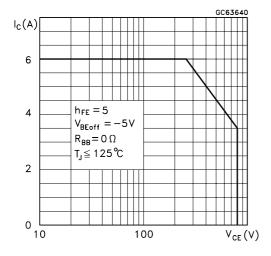


Inductive Storage Time

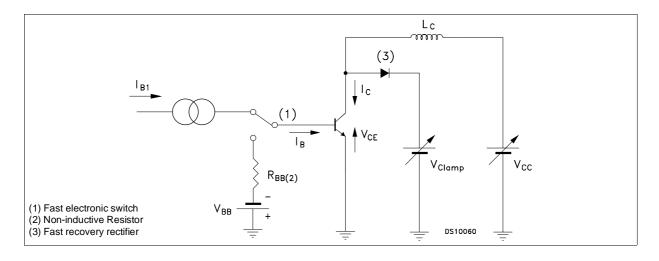


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Reverse Biased SOA



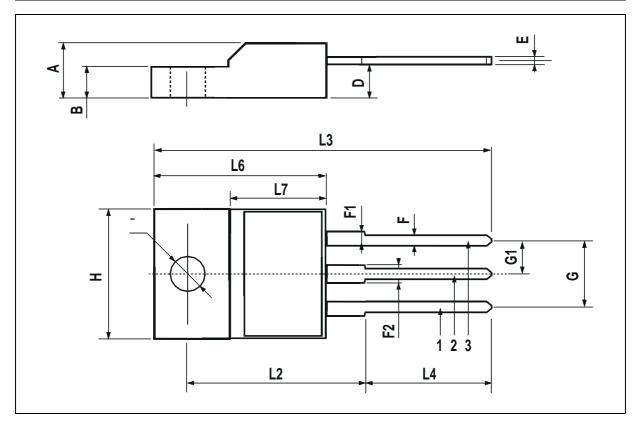
Inductive Load Switching Test Circuit



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TO-220FP MECHANICAL DATA

DIM.		mm			inch	
DIIVI.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
А	4.4		4.6	0.173		0.181
В	2.5		2.7	0.098		0.106
D	2.5		2.75	0.098		0.108
E	0.45		0.7	0.017		0.027
F	0.75		1	0.030		0.039
F1	1.15		1.7	0.045		0.067
F2	1.15		1.7	0.045		0.067
G	4.95		5.2	0.195		0.204
G1	2.4		2.7	0.094		0.106
Н	10		10.4	0.393		0.409
L2		16			0.630	
L3	28.6		30.6	1.126		1.204
L4	9.8		10.6	0.385		0.417
L6	15.9		16.4	0.626		0.645
L7	9		9.3	0.354		0.366
Ø	3		3.2	0.118		0.126



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