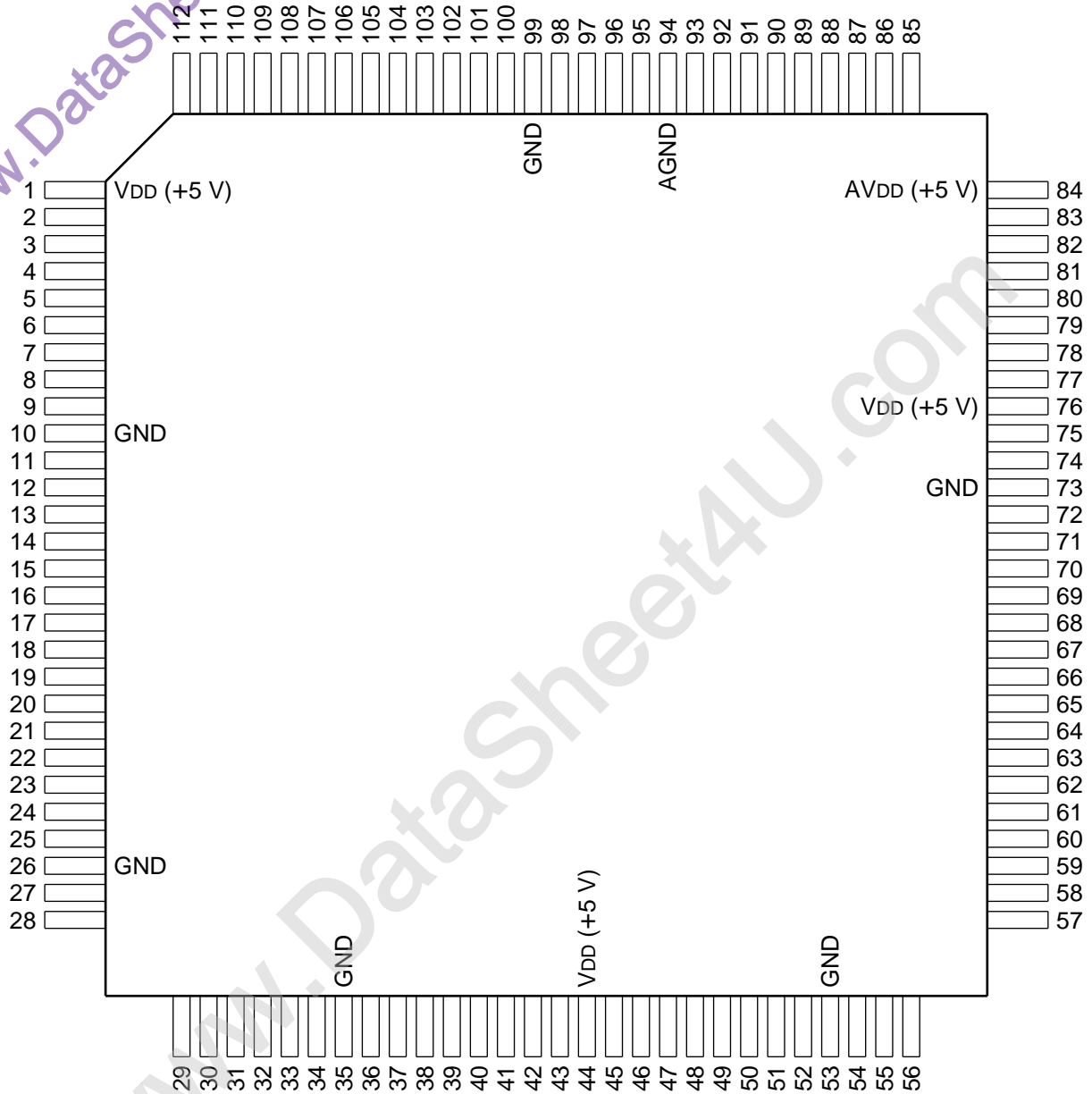


C-MOS 16-BIT 1-CHIP MICROCOMPUTER

—TOP VIEW—



(V_{DD}, AV_{DD} = +5 V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	—	V _{DD}	29	I/O	P42/D2	57	O	A11	85	—	VREF
2	I/O	PB0/TP8/TIOCA3	30	I/O	P43/D3	58	O	A12	86	I	P70/AN0
3	I/O	PB1/TP9/TIOCB3	31	I/O	P44/D4	59	O	A13	87	I	P71/AN1
4	I/O	PB2/TP10/TIOCA4	32	I/O	P45/D5	60	O	A14	88	I	P72/AN2
5	I/O	PB3/TP11/TIOCB4	33	I/O	P46/D6	61	O	A15	89	I	P73/AN3
6	I/O	PB4/TP12/TOCXA4	34	I/O	P47/D7	62	O	A16	90	I	P74/AN4
7	I/O	PB5/TP13/TOCXB4	35	—	GND	63	O	A17	91	I	P75/AN5
8	I/O	PB6/TP14/DREQ ₀	36	I/O	D8	64	O	A18	92	I	P76/AN6
9	I/O	PB7/TP15/DREQ1/ADTRG	37	I/O	D9	65	O	A19	93	I	P77/AN7
10	—	GND	38	I/O	D10	66	I/O	P60/WAIT	94	—	AGND
11	I/O	PC0	39	I/O	D11	67	I/O	P61/BREQ	95	I/O	P54/A20
12	I/O	PC1	40	I/O	D12	68	I/O	P62/BACK	96	I/O	P55/A21
13	I/O	PC2/TEND2/CS4	41	I/O	D13	69	O	∅	97	I/O	P56/A22
14	I/O	PC3/DREQ2/CS5	42	I/O	D14	70	I	STBY	98	I/O	P57/A23
15	I/O	PC4/TEND3/CS6	43	I/O	D15	71	I	RES	99	—	GND
16	I/O	PC5/DREQ3/CS7	44	—	V _{DD}	72	I	NMI	100	I/O	P80/RFSH/IRQ0
17	I/O	PC6/IRQ6	45	O	A0	73	—	GND	101	I/O	P81/CS3/IRQ1
18	I/O	PC7/IRQ7	46	O	A1	74	I	EXTAL	102	I/O	P82/CS2/IRQ2
19	O	RESO	47	O	A2	75	I	XTAL	103	I/O	P83/CS1/IRQ3
20	I/O	P90/TXD0	48	O	A3	76	—	V _{DD}	104	I/O	P84/CS0
21	I/O	P91/TXD1	49	O	A4	77	O	AS	105	I/O	PA0/TP0/TEND0/TCLKA
22	I/O	P92/RXD0	50	O	A5	78	O	RD	106	I/O	PA1/TP1/TEND1/TCLKB
23	I/O	P93/RXD1	51	O	A6	79	O	HWR	107	I/O	PA2/TP2/TIOCA0/TCLKC
24	I/O	P94/SCK0/IRQ4	52	O	A7	80	O	LWR	108	I/O	PA3/TP3/TIOCB0/TCLKD
25	I/O	P95/SCK1/IRQ5	53	—	GND	81	I	MD0	109	I/O	PA4/TP4/TIOCA1
26	—	GND	54	O	A8	82	I	MD1	110	I/O	PA5/TP5/TIOCB1
27	I/O	P40/D0	55	O	A9	83	I	MD2	111	I/O	PA6/TP6/TIOCA2
28	I/O	P41/D1	56	O	A10	84	—	AV _{DD}	112	I/O	PA7/TP7/TIOCB2

INPUT

$\overline{\text{ADTRG}}$: A/D CONVERTER EXTERNAL TRIGGER
AN0 - AN7	: ANALOG
$\overline{\text{BREQ}}$: BUS REQUEST
$\overline{\text{DREQ0}} - \overline{\text{DREQ3}}$: DMA REQUEST
EXTAL	: CRYSTAL OSCILLATOR AND EXTERNAL CLOCK
$\overline{\text{IRQ0}} - \overline{\text{IRQ7}}$: INTERRUPT REQUEST
MD0 - MD2	: MODE
NMI	: NON-MASKABLE INTERRUPT
P70 - P77	: PORT 7
$\overline{\text{RES}}$: RESET
RXD0, RXD1	: RECEIVE DATA
$\overline{\text{STBY}}$: STANDBY
TCLKA - TCLKD	: CLOCK INPUT
VREF	: A/D AND D/A CONVERTER REFERENCE VOLTAGE
$\overline{\text{WAIT}}$: WAIT
XTAL	: CRYSTAL OSCILLATOR

OUTPUT

Ø	: SYSTEM CLOCK
A0 - A23	: ADDRESS BUS
$\overline{\text{AS}}$: ADDRESS STROBE
$\overline{\text{BACK}}$: BUS REQUEST ACKNOWLEDGE
$\overline{\text{CS0}} - \overline{\text{CS7}}$: CHIP SELECT
$\overline{\text{HWR}}$: HIGH WRITE
$\overline{\text{LWR}}$: LOW WRITE
$\overline{\text{RD}}$: READ
$\overline{\text{RESO}}$: RESET OUTPUT
$\overline{\text{RFSH}}$: REFRESH
$\overline{\text{TEND0}} - \overline{\text{TEND3}}$: DMA END
TOXA4	: OUTPUT COMPARE XA4
TOXB4	: OUTPUT COMPARE XB4
TP0 - TP15	: TIMING PATTERN CONTROLLER
TXD0, TXD1	: TRANSMIT DATA

INPUT/OUTPUT

D0 - D15	: DATA BUS
P40 - P47	: PORT 4
P50 - P57	: PORT 5
P60 - P62	: PORT 6
P80 - P84	: PORT 8
P90 - P95	: PORT 9
PA0 - PA7	: PORT A
PB0 - PB7	: PORT B
PC0 - PC7	: PORT C
SCK0, SCK1	: SERIAL CLOCK
TIOCA0 - TIOCA4	: INPUT CAPTURE/OUTPUT COMPARE A0 - A4
TIOCB0 - TIOCB4	: INPUT CAPTURE/OUTPUT COMPARE B0 - B4

