SPECIFICATION FOR LCD MODULE

Model No. <u>TM10032ACIWG</u>

Prepared by:	Date:
Checked by :	Date:
Verified by :	Date:
Approved by:	Date:

TIANMA MICROELECTRONICS CO., ETC www.bata Ver.1.0

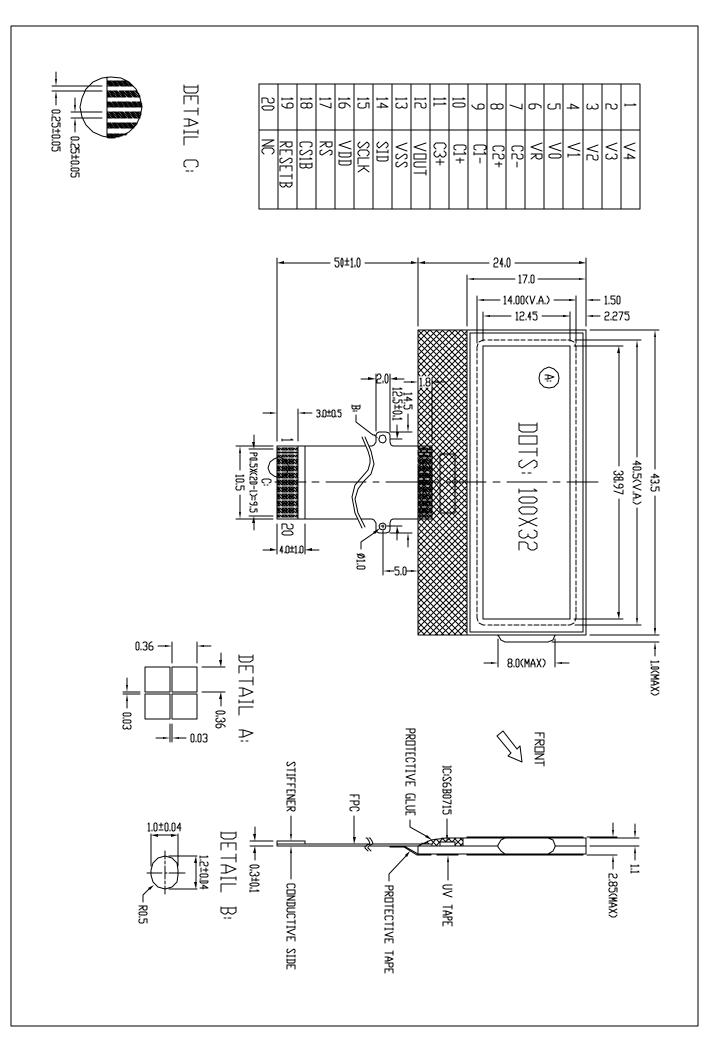
REVISION RECORD

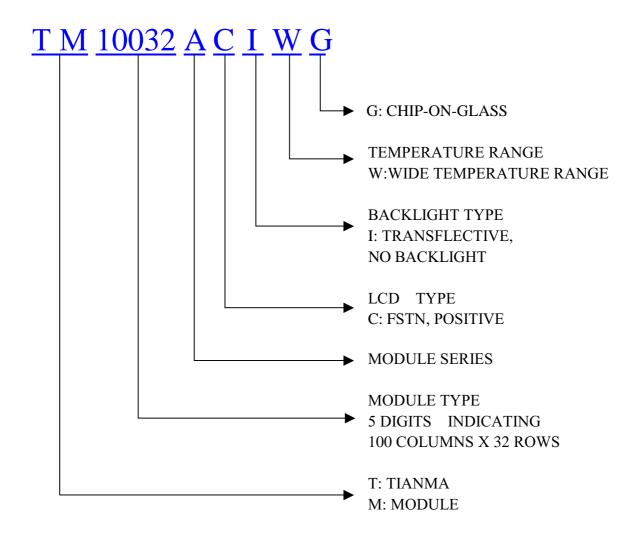
Date	Ref. Page	Revision No.	Revision Items	Check & Approval

1. General Specifications:

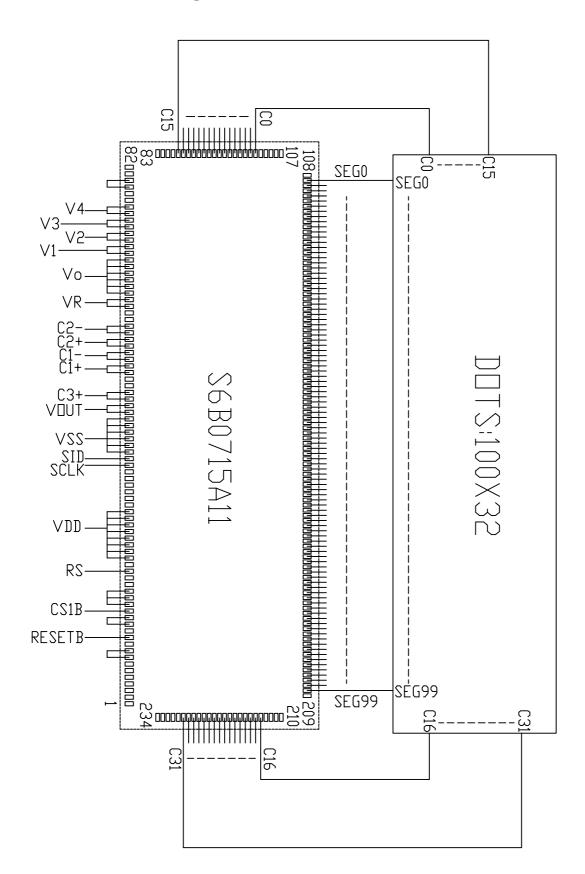
1.1 Display type:	FSTN
1.2 Display color*:	
Display color:	Blue-Black
Background:	White
1.3 Polarizer mode:	Transflective/Positive
1.4 Viewing Angle:	6:00
1.5 Driving Method:	1/32Duty 1/6 Bias
1.6 Lcd operation vol	tage: $7.0V$ Vdd= $3.0V$
1.7 Without Backligh	ıt
1.8 Controller:	S6B0715A11-B0CZ
1.9 Data Transfer:	Serial
1.10 Operating Temp	erature: -20+70°℃
Storage Tempera	ature: -30+80℃
1.11 Outline Dimensi	ons: Refer to outline drawing on next page
1.12 Dot Matrix:	100 X 32
1.13 Dot Size:	0.36X0.36(mm)
1.14 Dot Pitch:	0.39X0.39 (mm)
1.15 Weight:	20g

* Color tone is slightly changed by temperature and driving voltage.





4 Circuit Block Diagram



5 Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit	Remark
Power Supply Voltage	Vdd-Vss	2.4	3.6	v	
LCD Driving Voltage	VLCD	4.5	15.0	v	
Operating Temperature Range	Тор	-20	+70	°C	No
Storage Temperature Range	Тѕт	-30	+80		Condensation

6 Electrical Specifications and Instruction Code

6.1 Electrical characteristics

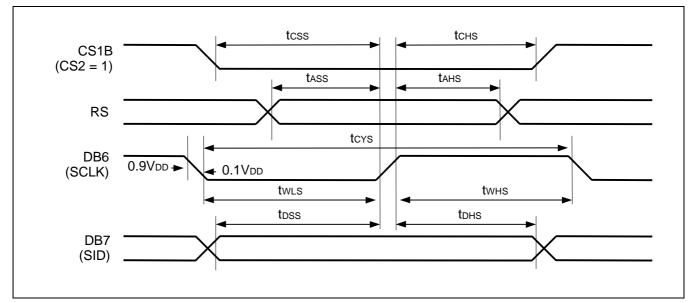
Ite	m	Symbol	Min.	Тур.	Max.	Unit
Supply (Log	•	Vdd-Vss	2.8	3.0	3.2	V
Supply V (LCD I	-	Vlcd	6.5	7.0	7.5	v
Input	High	V _{IH} (V _{DD} =3.0)	$0.8 \mathrm{V_{DD}}$	-	V _{DD}	V
Signal Voltage Low	Low	V_{IL} ($V_{DD}=3.0$)	0	-	$0.2 \ V_{DD}$	V
Supply (Log (Display c	gic)	I_{DD} (V _{DD} - V _{SS} =3.0V)	-	-	400	uA

6.2 Interface Signals

Pin No.	Symbol	Level	Description
1	V4	-	Power supply voltage for LCD
2	V3	-	Power supply voltage for LCD
3	V2	-	Power supply voltage for LCD
4	V1	-	Power supply voltage for LCD
5	V 0	7.0V	Power supply voltage for LCD
6	VR	-	Voltage adjust terminal
7	C2-	-	Capacitor pin for voltage converter
8	C2+	-	Capacitor pin for voltage converter
9	C1-	-	Capacitor pin for voltage converter
10	C1+	-	Capacitor pin for voltage converter
11	C3+	-	Capacitor pin for voltage converter
12	VOUT	-	DC/DC voltage converter output
13	VSS	0V	Ground
14	SID	H/L	Serial data input pin
15	SCLK	H/L	Serial clock input pin
16	VDD	3.0V	Power supply voltage for logic
17	RS	H/L	Register select input pin
18	CS1	H/L	Chip select input pin
19	/RES	H/L	Reset input pin
20	NC	-	No connection

6.3 Interface Timing Chart

Serial Interface Characteristics



Serial Interface Characteristics

					(Vdd = 2	2.4 to 3.6V	′, Ta = -40 to +85°C)
Item	Signal	Symbol	Min	Тур	Max	Unit	Remark
Serial clock cycle SCLK high pulse width	DB6	tCYS tWHS	450 180	-	-	ns	
SCLK low pulse width	(SCLK)	twLS	135	-	-	_	
Address setup time Address hold time	RS	tASS tAHS	90 360	-	-	ns	
Data setup time Data hold time	DB7 (SID)	tDSS tDHS	90 90	-	-	ns	
CS1B setup time CS1B hold time	CS1B	tcss tcнs	55 180	-	-	ns	

6.4 Instruction Code

INSTRUCTION DESCRIPTION

Instruction Table

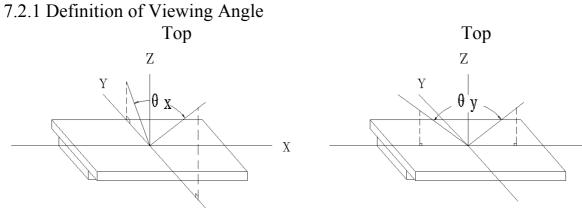
	×: Don't c									×: Don't care	
Instruction	RS	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description
Read display data	1	1				Read	l data			•	Read data from DDRAM
Write display data	1	0				Write	e data				Write data into DDRAM
Read status	0	1	BUSY	ADC	ONOFF	RESETB	0	0	0	0	Read the internal status
Display ON / OFF	0	0	1	0	1	0	1	1	1	DON	Turn ON / OFF LCD panel When DON = 0: display OFF When DON = 1: display ON
Initial display line	0	0	0	1	ST5	ST4	ST3	ST2	ST1	ST0	Specify DDRAM line for COM0
Set reference voltage mode	0	0	1	0	0	0	0	0	0	1	Set reference voltage mode
Set reference voltage register	0	0	1	0	0	SV4	SV3	SV2	SV1	SV0	Set reference voltage register
Set page address	0	0	1	0	1	1	P3	P2	P1	P0	Set page address
Set column address MSB	0	0	0	0	0	1	0	Y6	Y5	Y4	Set column address MSB
Set column address LSB	0	0	0	0	0	0	Y3	Y2	Y1	Y0	Set column address LSB
ADC select	0	0	1	0	1	0	0	0	0	ADC	Select SEG output direction When ADC = 0: normal direction (SEG0→SEG99) When ADC = 1: reverse direction (SEG99→SEG0)
Reverse display ON / OFF	0	0	1	0	1	0	0	1	1	REV	Select normal / reverse display When REV = 0: normal display When REV = 1: reverse display
Entire display ON / OFF	0	0	1	0	1	0	0	1	0	EON	Select normal/ entire display ON When EON = 0: normal display. When EON = 1: entire display ON
LCD bias select	0	0	1	0	1	0	0	0	1	BIAS	Select LCD bias
Set modify-read	0	0	1	1	1	0	0	0	0	0	Set modify-read mode
Reset modify-read	0	0	1	1	1	0	1	1	1	0	release modify-read mode
Reset	0	0	1	1	1	0	0	0	1	0	Initialize the internal functions
SHL select	0	0	1	1	0	0	SHL	×	×	×	Select COM output direction When SHL = 0: normal direction (COM0→COM31) When SHL = 1: reverse direction (COM31→COM0)
Power control	0	0	0	0	1	0	1	VC	VR	VF	Control power circuit operation
Set static indicator register	0	0	1	0	1	0	1	1	0	SI	Set static indicator register SI = 0 (OFF), SI = 1 (ON)
Power save	-	-	-	-	-	-	-	-	-	-	Compound instruction of display OFF and entire display ON
Test instruction	0	0	1	1	1	1	×	×	×	×	Don't use this instruction.

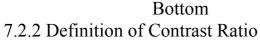
7 Optical Characteristics

7.1 Optical Characteristics

Ta=25℃ Symbol Condition Min. Item Тур. Max. Unit $\theta_{\rm V}=0^{\circ}$ $\theta_{\!X}$ -35 20 ---Viewing Angle $Cr \ge 2$ Deg $\theta_x = 0^{\circ}$ θ_{y} -30 30 ___ $\theta_x = 0^{\circ}$ **Contrast Ratio** Cr 4.0 _ - $\theta_{\rm y}=0^{\circ}$ Turn Ton 250 -on $\theta_x = 0^{\circ}$ Response ms $\theta_y = 0^{\circ}$ Time Turn Toff 250 -off

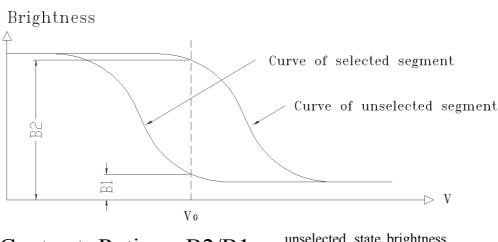
7.2 Definition of Optical Characteristics







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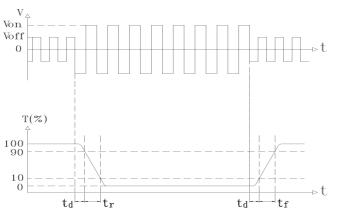




Measuring Conditions:

1) Ambient Temperature: 25° ; 2) Frame frequency: 64Hz

7.2.3 Definition of Response time



Turn on time: $t_{on} = t_d + t_r$ Turn off time: $t_{off} = t_d + t_f$ Measuring Condition:

1) Operating Voltage: 7.0V 2) Frame frequency: 64Hz

8 Reliability

8.1 0	Content of Reliability	/ Test	Ta=25°C			
No.	Test Item	Content of Test	Test condition			
1	High Temperature	Endurance test applying the high	80℃			
	Storage	storage temperature for a long time	96H			
2	Low Temperature	Endurance test applying the low	-30°C			
	Storage	storage temperature for a long time	96H			
		Endurance test applying the				
3	High Temperature	electric stress (voltage & current)	70 ℃			
5	Operation	and the thermal stress to the	96H			
		element for a long time	2011			
	Low Temperature	Endurance test applying the	-20 ℃			
4	Operation	Operation electric stress under low				
-	-1	temperature for a long time	96H			
_	High Temperature	Endurance test applying the high	40°℃			
5	/Humidity Storage	temperature and high humidity	90%RH			
		storage for a long time	96H			
		Endurance test applying the low				
	Temperature	and high temperature cycle	-20° ℃/60°℃			
6	Cycle	$\begin{array}{rcl} -20^{\circ}\text{C} &\longrightarrow 25^{\circ}\text{C} &\longrightarrow 60^{\circ}\text{C} &\longrightarrow 25^{\circ}\text{C} \\ 30\text{min} & 5\text{min} & 30\text{min} & 5\text{min} \end{array}$				
		←───→	10 cycles			
		1 cycle				
	Vibration Test	Endurance test applying the	10Hz~150Hz,			
7	(package state)	vibration during transportation	$50 {\rm m/s}^2$,			
	(package state)	vioration during transportation	40min			
	Shock Test	Endurance test applying the shock	Half- sine wave,			
8	(package state)	during transportation	100m/s^2 ,			
	(T		11ms			
	Atmospheric	Endurance test applying the	40kPa			
9	Pressure Test	atmospheric pressure during	16H			
		transportation by air				

8.2 Failure Judgment Criterion

Criterion			Te	est	Iter	n N	0.			Failura Indeamant Critarian
Item	1	2	3	4	5	6	7	8	9	Failure Judgement Criterion
Basic Specification								\checkmark	\checkmark	Out of the basic Specification
Electrical specification										Out of the electrical specification
Mechanical Specification								\checkmark		Out of the mechanical specification
Optical Characteristic									\checkmark	Out of the optical specification
Note	Fc	For test item refer to 8.1								
Remark Basic specification = Optical specification + Mechanical specification										

9 QUALITY LEVEL

Examination	At T _a =25°C	Inspection								
or Test	(unless otherwise stated)	Min.	Max.	Unit	IL	AQL				
External Visual Inspection	Under normal illumination and eyesight condition, the distance between eyes and LCD is 25cm.	See Appendix A			II	Major 1.0 Minor 2.5				
Display Defects	Undernormalilluminationandeyesightcondition,display on inspection.	See Ap	pendix B		II	Major 1.0 Minor 2.5				
Note: Major defects: Open segment or common, Short, Serious damages, Leakage Miner defects: Others Sampling standard conforms to GB2828										

10 Precautions for Use of LCD Modules

- 10.1 Handling Precautions
- 10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
 - Isopropyl alcohol
 - Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- 10.1.6 Do not attempt to disassemble the LCD Module.
- 10.1.7 If the logic circuit power is off, do not apply the input signals.
- 10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - a. Be sure to ground the body when handling the LCD Modules.
 - b. Tools required for assembly, such as soldering irons, must be properly ground.
 - c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
 - d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

10.2 Storage precautions

10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

10.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0° C 40° CRelatively humidity: $\leq 80\%$

- 10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.
- 10.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

Appendix A

Inspection items and criteria for appearance defects

Items	Contents	Criteria			
Protective Glue		No clear defects			
Cover Tape		Covering all of the chip and no clear crimple			
Leakage		Not permitted			
Rainbow		According to the limit specimen			
	Wrong polarizer attachment	Not permitted			
	Bubble between polarizer and glass	Not counted		Max. 3 defects allowed	
Polarizer		ф<0.3mm		0.3mm≤¢≤0.5mm	
	Scratches of polarizer	According to the limit specimen			
Black spot (in viewing area)		Not counted	Max	. 3 spots allowed	
		X<0.2mm	0.2mm≪X≪0.5mm		Max. 3
		X=(a+b)/2			spots (lines)
Black line (in viewing area)		Not counted	Max. 3 lines allowed		allowed
		a<0.02mm	0.02mm≤a≤0.05mm b≤2.0mm		-
Progressive cracks		Not permitted			

Appendix A

Inspection item and criteria for appearance defects (continued)

Items	Contents	Criteria					
	Cracks on pads	a	b		с	Max. 2	
		≤3mm	$\leqslant v$	V/5	≪T/2	cracks allowed	
		≤2mm	≪V	V/5	T/2 <c<t< td=""><td>anowed</td></c<t<>	anowed	
	Cracks on contact side	a			b		
		≪3m	≤ 3 mm $\leq T/2$		$\leq T/2$	-	
Glass Cracks		≤2m	m]	r/2 <b<t< td=""><td></td><td></td></b<t<>		
		C shall be not reach the seal area				Max. 2 cracks	Max. 5 cracks allowed
	Cracks on non-contact side	а		b		allowed	
		≤3m	m		≤T/2		
		≤2mm		T/2 <b<t< td=""><td></td><td></td></b<t<>			
		C≪0.5mm					
	""	d≤SW/3					-
	Corner cracks	e<2.0mm ²				Max. 3 cracks allowed	
	f-	f<2.0mm ²					
	e-49						

Appendix B

Inspection items and criteria for display defects

Items		Contents	Critera			
Open segment or open common			Not permitted			
Short			Not permitted			
Wrong viewing angle			Not permitted			
Contrast radio uneven			According to the limit specimen			
Crosstalk		According to the limit specimen				
			Not counted	Max.3 dots allowed		
		X<0.1mm	0.1mm≤X≤0.2mm			
Pin holes		X=(a+b)/2	Max.3 dots			
and cracks in segment		Not counted	Max.2 dots allowed	allowed		
(DOT)		A<0.1mm	0.1mm≪A≪0.2mm D<0.25mm			
Black spot (in viewing area)		Not counted	Max.3 spots allowed			
		X<0.1mm	0.1mm≪X≪0.2mm	_		
		X=(a+b)/2	Max.3 spots			
Black line		Not counted	Max.3 lines allowed	(lines) allowed		
(in viewing area)		a<0.02mm	0.02mm≤a≤0.05mm b≤0.5mm			

Appendix B

Inspection items and criteria for display defects (continued)

Items	Content	Critera			
		Not counted	Max. 2 defects allowed		
		x<0.1mm	0.1mm≪x≪0.2mm		
		x=(a+b)/2	·		
				Max.3	
	<u>D-+</u> ++-a	Not counted	Max. 1 defects allowed	defects allowed	
Transfor- mation of segment		a<0.1mm	0.1mm≪a≪0.2mm D>0		
		Max.2 defects 0.8W≤a≤1.2 a=measured va W=nominal va			