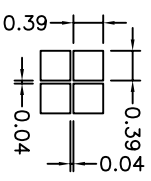


128\*128  
DOTS



PIN	SYMBOL	Level	Function	
1	VSS	0V	GND	
2	VDD	+5V	Power supply for logic	
3	V0	--	Operating voltage for LCD	
4	D/I	H/L	H: Data L: Instruction code	
5	R/W	H/L	H: READ L: WRITE	
6	E	H,H->L	Enable signal	
7	/RST	L	Reset signal ,Active "L"	
8	DB0	H/L	Data bus line	
9	DB1	H/L		
10	DB2	H/L		
11	DB3	H/L		
12	DB4	H/L		
13	DB5	H/L		
14	DB6	H/L		
15	DB7	H/L		
16	CS1	H		Chip selection for IC1,Active "H"
17	CS2	H		Chip selection for IC2,Active "H"
18	CS3	H		Chip selection for IC3,Active "H"
19	CS4	H	Chip selection for IC3,Active "H"	
20	VEE	--	LCD negative voltage supply	
21	LED+	4.2V	LED Positive voltage supply	
22	LED-	0V	LED Negative voltage supply	

NOTE:

DISPLAY TYPE : STN TRANSFLECTIVE-POSITIVE Y/G

VIEWING DIRECTION : 6: 00

OPERATING VOLT. : VOP=13.0V

OPERATING TEMP. : -20℃ ~ +70℃

STORAGE TEMP. : -30℃ ~ +80℃

DRIVE METHOD : 1/64 DUTY 1/9 BIAS VDD:5.0V

DRIVER : S6B0108;S6B0107

BACKLIGHT : LED Y/G 4.2V 120mA

TOLERANCE		HTDISPLAY CO., LTD.		DRAWING TITLE: LCD MODULE	
>0~6	±0.10	DESIGN	LI JINTANG	06.03.25	STANDARD
>6~80	±0.20	CHECKED			TECHNOLOGY
>80~240	±0.30	APPROVED			Q.A
>240~400	±0.50				
// L ⊙ ±0.10					
DRAWING No. : HG12812801C-YF62L-VA		REV. :	SCALE : -		
PROJECTION:		UNIT : MM			
SHEET : 1/1					

REV.	CHANGE CONTENT	BY	DATE