

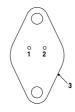
LM120A-05 LM120-05 LM7905A LM7905

# 1.5 AMP **NEGATIVE VOLTAGE REGULATOR**

# 0

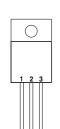
Pin 1 - Ground Pin 2 – V<sub>OUT</sub> Case - V<sub>IN</sub>

K Package - TO-3



Pin 1 – Ground Pin 2 – V<sub>OUT</sub> Case - V<sub>IN</sub>

R Package - TO-66



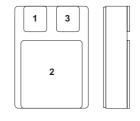
Pin 1 - Ground

Pin  $2 - V_{IN}$ 

Pin 3 – V<sub>OUT</sub>

Case - V<sub>IN</sub>

G Package - TO-257 IG Package- TO-257\* \* isolated Case on IG package



Pin 1 - Ground Pin 2 – V<sub>IN</sub>

Pin 3 – V<sub>OUT</sub>

SMD Package - SMD1 Ceramic Surface Mount

### **FEATURES**

- OUTPUT VOLTAGE OF -5V
- 0.7% / V LINE REGULATION AVAILABLE
- 0.5% / A LOAD REGULATION AVAILABLE
- THERMAL OVERLOAD PROTECTION
- SHORT CIRCUIT PROTECTION
- OUTPUT TRANSISTOR SOA PROTECTION
- 1% VOLTAGE TOLERANCE OPTION (-A VERSIONS)

### DESCRIPTION

The A suffix devices provide 0.7% / V line regulation, 0.5% / A load regulation and ±1% output voltage tolerance at room temperature.

Protection features include Safe Operating Area current limiting and thermal shutdown.

## ABSOLUTE MAXIMUM RATINGS (T<sub>case</sub> = 25°C unless otherwise stated)

$\overline{V_{I}}$	DC Input Voltage	35V
$P_{D}$	Power Dissipation	Internally limited
$T_j$	Operating Junction Temperature Range	−55 to 150°C
T <sub>stg</sub>	Storage Temperature	−65 to 150°C

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Document Number 2643

Issue: 1



LM120A-05 LM120-05 LM7905A LM7905

				LM7905A LM120A-05		LM7905 , LM120-05 LM120-05					
Paran	neter	Test Conditions		Min.	Тур.	Max.	Min.	Тур.	Max.	Units	
		I <sub>O</sub> = 500mA	$V_{IN} = -10V$	-4.95	-5	-5.05	-4.9	-5	-5.1		
Vo	Output Voltage	$I_O = 5$ mA to $I_{MAX}$	$I_{MAX}$ $V_{IN} = -7.5V \text{ to } -20V$			-5.15	-4.8		-5.2	V	
		$P_D \le P_{MAX}$	$T_J = -55 \text{ to } 150^{\circ}\text{C}$	-4.85		-5.15	-4.0		-5.2		
ΔV <sub>O</sub>		I <sub>O</sub> = 0.5 I <sub>MAX</sub>	V <sub>IN</sub> = -7V to -25V		3	10		3	25		
			$V_{IN} = -7.5V \text{ to } -20V$		3			3	50	1	
	Line Regulation		$T_{J} = -55 \text{ to } 150^{\circ}\text{C}$		3	10		3	50	mV	
		V <sub>IN</sub> = -8V to -12V			1	4		1	25		
		I <sub>O</sub> ≤ I <sub>MAX</sub>	$T_{J} = -55 \text{ to } 150^{\circ}\text{C}$		1	12		2	50	1	
ΔV <sub>O</sub>	Load Regulation	V <sub>IN</sub> = -10V	$I_{O} = 5 \text{mA to } 1.5 \text{A}$		25	35		25	100		
			$I_O = 5mA \text{ to } I_{MAX}$	0.5		05		05	100	mV	
			$T_{J} = -55 \text{ to } 150^{\circ}\text{C}$		25	35		25	100		
IQ	Quiescent Current	I <sub>O</sub> ≤ 0.5 I <sub>MAX</sub>			1	1.9		1	1.9	mA	
	Quiescent Garrent	V <sub>IN</sub> = -10V	$T_{J} = -55 \text{ to } 150^{\circ}\text{C}$		1	2		1	2		
۸۱۵	Quiescent Current	$I_O = 5$ mA to $I_{MAX}$		0.2		0.4		0.2	0.4	T	
$\Delta I_Q$	Change	V <sub>IN</sub> = -10V	$T_{J} = -55 \text{ to } 150^{\circ}\text{C}$		0.2	0.5		0.2	0.5	mA	
V <sub>N</sub>	Output Noise	f = 10Hz to 100kHz			100			100		μV	
N N	Voltage	V <sub>IN</sub> = -10V	100			100			μν		
$\frac{\Delta V_{IN}}{\Delta V_{O}}$	Ripple Rejection	f = 120Hz	I <sub>O</sub> ≤ I <sub>MAX</sub>	58			54			٩D	
		$V_{IN} = -8V \text{ to } -18V$	I <sub>O</sub> ≤ 0.5 I <sub>MAX</sub>	58			54			dB	
40			$T_{J} = -55 \text{ to } 150^{\circ}\text{C}$								
	Dropout Voltage	$I_{O} = I_{MAX}$			1.4			1.4		V	
R <sub>O</sub>	Output Resistance	f = 1 kHz			5			5		mΩ	
I <sub>sc</sub>	Short Circuit	V <sub>IN</sub> = -35V			0.6	1.0		0.0	1.0		
	Current				0.6	1.2		0.6	1.2		
,	Peak Output	V 10V		0.4	0.0		0.4	0.0	A		
I <sub>pk</sub>	Current Average	V <sub>IN</sub> = -10V			2.4	3.3		2.4	3.3		
Temperature		J. 5 A			0.0			0.0		mV_	
Coefficient of V <sub>O</sub>		I <sub>O</sub> = 5mA			0.2			0.2		∫ °c	
Input	Voltage required to			7.0			7.0			V	
main	tain line regulation	I <sub>O</sub> ≤ I <sub>MAX</sub>		-7.3			-7.3			"	
							•				

- 1) All characteristics are measured with a capacitor across the input of 0.22μF and a capacitor across the output of 0.1μF. All characteristics except noise voltage and ripple rejection ratio are measured using pulse techniques ( $t_p \le 10$ ms,  $\delta \le 5$ %). Output voltage changes due to changes in internal temperature must be taken into account separately.
- 2) Test Conditions unless otherwise stated:  $P_{MAX} = 10W$  for SMD ,  $P_{MAX} = 20W$  for all other package devices

$$I_{MAX} = 1.0A$$
,  $T_{J} = 25^{\circ}C$ 

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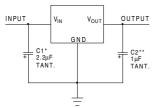
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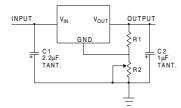
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### **APPLICATIONS INFORMATION**



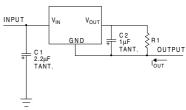
### **Fixed Output Regulator**

- \* Required if the regulator is located far from the power supply.
- \*\* Required for stability.  $25\mu F$  electrolytic may be substituted.



### **Adjustable Output Regulator**

$$V_{OUT} \approx V_{REG} \frac{(R1+R2)}{R1}$$



### **Current Regulator**

$$I_{OUT} = \frac{V_{REG}}{R1} + I_{Q}$$

### **Order Information**

Part	K-Pack	R-Pack	G/IG-Pack	SMD-Pack	Temp.	Note:	
Number	(TO-3)	(TO-66)	(TO-257)	SMD1	Range	To order, add the	
LM7905A	~	<b>V</b>	<b>V</b>	~	-55 to +150°C	package identifier to the	
LM7905	~	<b>✓</b>	<b>✓</b>	~	"	part number.	
LM120A-05	<b>/</b>	<b>V</b>	<b>V</b>	<b>/</b>	"	eg. LM7905AK	
LM120-05	<b>&gt;</b>	<b>✓</b>	<b>✓</b>	<b>✓</b>	<b>33</b>	LM120SMD-05	

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