# SPECIFICATION FOR LCD MODULE

**TM160240ACBWG** Model No.

Prepared by: Date: Checked by: Date: Verified by: Date: **Approved by:** Date:

TIANMA MICROELECTRONICS CO., LTD

## **REVISION RECORD**

Date	Ver.	Ref. Page	Revision No.	Revision Item

## 1. General Specifications:

1.1 Display type: FSTN

1.2 Display color\*<sup>1</sup>:

Display color: Blue-black

Background\*2: White

1.3 Polarizer mode: Transflective/Positive

1.4 Viewing Angle: 6:00

1.5 Driving Method: 1/240 Duty 1/13 Bias

1.6 Lcd operating voltage: 21.5V

1.7 Backlight: EL

1.8 Driver: S6B0794X01-B0CZ + S6B0796X01-B0CZ

1.9 Data Transfer: 4 Bit Parallel 1.10 Operating Temperature: -20----+70 ℃ Storage Temperature: -30----+80 ℃

1.11 Outline Dimensions: Refer to outline drawing on next page

 1.12 Dot Matrix:
 160 X 240Dots

 1.13 Dot Size:
 0.22X0.22 (mm)

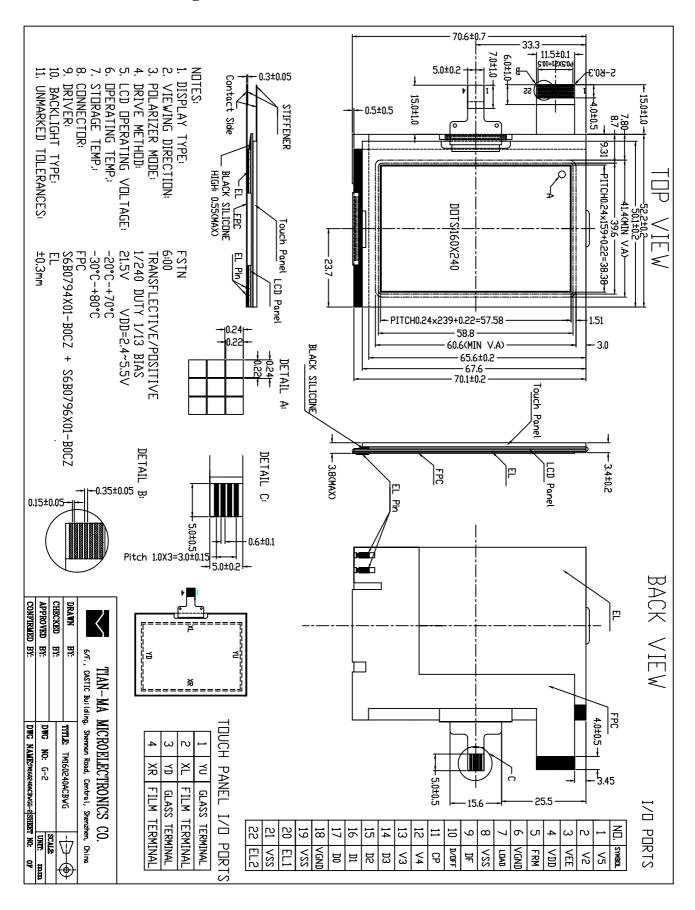
 1.14 Dot Pitch:
 0.24X0.24 (mm)

 1.15 Weight:
 Approx 150g

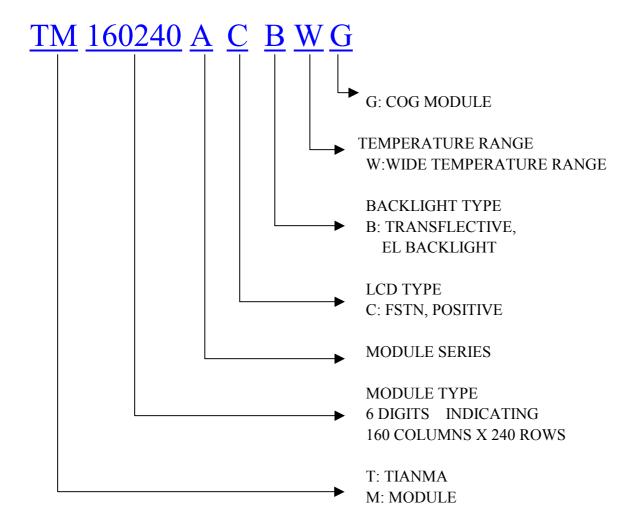
<sup>\*1</sup> Color tone is slightly changed by temperature and driving voltage.

<sup>\*2</sup> Color tone will be changed by backlight.

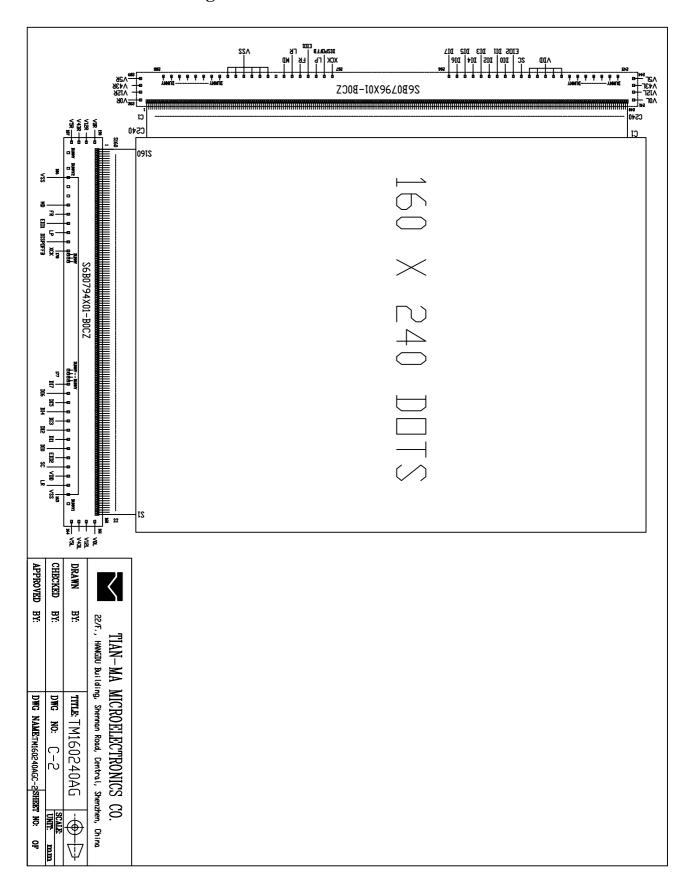
#### 2. Outline Drawing



## 3. LCD Module Part Numbering System



## 4. Circuit Block Diagram



# **5. Absolute Maximum Ratings**

Item	Symbol	Min.	Max.	Unit	Remark	
Power Supply Voltage	V <sub>DD</sub> -V <sub>SS</sub>	-0.3	6.0	V		
LCD Driving Voltage	VLCD	-	28.0	v		
Operating Temperature Range	Тор	-20	+70	°C	No	
Storage Temperature Range	Тѕт	-30	+80		Condensation	

# **6. Electrical Specifications and Instruction Code**

# 6.1 Electrical characteristics

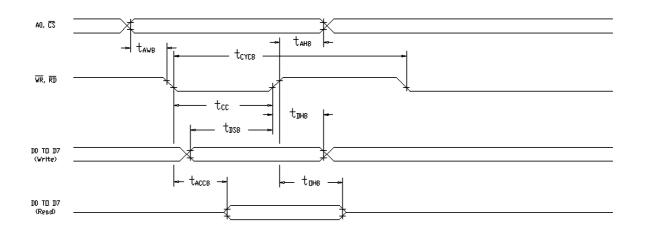
Iten	n	Symbol	Min.	Тур.	Max.	Unit
Supply V (Log	•	V <sub>DD</sub> -V <sub>SS</sub>	4.75	5.0	5.25	V
Supply V (LCD D	•	Vlcd	1	21.5	-	V
Input Signal	High	$V_{IH}$ $(V_{DD}=5.0)$	$0.8V_{DD}$	-	V <sub>DD</sub> +0.3	V
Voltage	Low	$V_{IL}$ $(V_{DD}=5.0)$	0	-	$0.2V_{\mathrm{DD}}$	V
Supply c (Log		$I_{DD}$ $(V_{DD}-V_{SS}=5.0V)$	-	-	0.5	mA
Supply current (LCD Drive)		${ m I}_{ m EE}$	-	-	0.7	mA
Supply c		$ m I_{EL}$	-	1.1	-	mA

# 6.2 Interface Signals

Pin No.	Symbol	Level	Description
1	V5	-	Bias Voltage For Non-Select (Common Driver)
2	V2	-	Bias Voltage For Non-Select(Common Driver)
3	VEE	21.5V	Power Supply For LCD
4	VDD	5.0V	Power Supply For LOGIC
5	FRM	H/L	Frame Start Signal
6	VGND	0V	GROUND
7	LOAD	H/L	Latch Pulse of Display Data
8	VSS	0V	GROUND
9	DF	H/L	Switch Signal to Convert LCD Driver
10	D/OFF	H/L	H: Display On L: Display Off
11	CP	H/L	Clock Pules for Segment Shift Register
12	V4	-	Bias Voltage For Non-Select(Segment Driver)
13	V3	-	Bias Voltage For Non-Select(Segment Driver)
14	D3	H/L	Input Data Signal
15	D2	H/L	Input Data Signal
16	D1	H/L	Input Data Signal
17	D0	H/L	Input Data Signal
18	VGND	0V	GROUND
19	VSS	0V	GROUND
20	EL1	-	Power Supply For EL Backlight
21	VSS	0V	GROUND
22	EL2	-	Power Supply For EL Backlight

## 6.3 Interface Timing Chart

## 8080 family interface timing



Ta=-20 to 75 deg. C

Signal	Cymbol	Parameter	V <sub>DD</sub> =4.5	to 5.5V	V <sub>DD</sub> =2.7	to 5.5V	Unit	Condition
Signal Symbol	Parameter	min	max	min	max	Ollit	Condition	
A0, <del>CS</del>	$t_{AH8}$	Address hold time	10	-	10	1	ns	
Au,CS	$t_{ m AW8}$	Address setup time	0	ı	0	ı	ns	
WD DD	$t_{\rm CYC8}$	System cycle time	See note.	-	See note.	-	ns	
WR,RD	$t_{CC}$	Strobe pulsewidth	120	ı	150	ı	ns	CL=100pF
	$t_{ m DS8}$	Data setup time	120	ı	120	ı	ns	
D0 to D7	$t_{ m DH8}$	Data hold time	5	ı	5	ı	ns	
D0 10 D7	t <sub>ACC8</sub>	RD access time	-	50	-	80	ns	
	$t_{ m OH8}$	Output disable time	10	50	10	55	ns	

Note

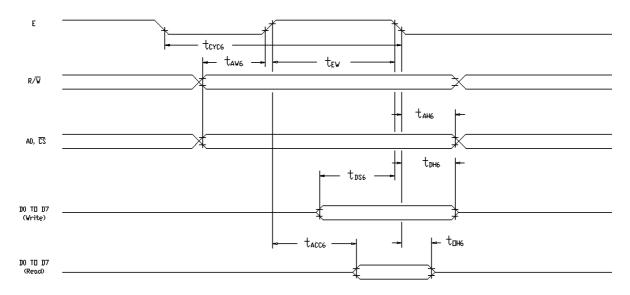
For memory control and system control commands:

$$t_{CYC8} = 2 t_C + t_{CC} + t_{CEA} + 75 > t_{ACV} + 245$$

for all other commands:

$$t_{CYC8} = 4 t_C + t_{CC} + 30$$

## 6800 family interface timing



#### Note

teros indicates the interval during which CS is LOW and E is HIGH

Ta=-20 to 75 deg. C

G: 1	01 -1	D	V <sub>DD</sub> =4.5	to 5.5V	V <sub>DD</sub> =2.7	to 5.5V	TT	Condition	
Signal Symbol		Parameter	min	Max	min	max	Unit	Condition	
A0,CS	$t_{\rm CYC6}$	System cycle time	See note.	-	See note.	-	ns		
Au,CS	$t_{ m AW6}$	Address setup time	0	-	10	-	ns		
R/W	$t_{AH6}$	Address hold time	0	-	0	-	ns		
	$t_{ m DS6}$	Data setup time	100	ı	120	ı	ns	CL=100pF	
D0 to D7	$t_{ m DH6}$	Data hold time	0	-	0	-	ns		
Do to D7	$t_{ m OH6}$	Output disable time	10	50	10	75	ns		
t <sub>ACC6</sub>		Access time	-	85	-	130	ns		
Е	$t_{\rm EW}$	Enable pulsewidth	120	-	150	-	ns		

#### Note

For memory control and system control commands:

$$t_{CYC8} = 2 t_C + t_{CC} + t_{CEA} + 75 > t_{ACV} + 245$$

for all other commands:

$$t_{\rm CYC8} = 4 t_{\rm C} + t_{\rm EW} + 30$$

## 6.4 Instruction Code (Controller: SED1335F)

Class	Command						Code						Command description	Number
		RD	WR	A0	D7	D6	D5	D4	D3	D2	D1	D0		of
														Bytes
System	SYSTEM SET	1	0	1	0	1	0	0	0	0	0	0	Initialize device and display	8
cotrol	SLEEP IN	1	0	1	0	1	0	1	0	0	1	1	Enter standby mode	0
	DISP ON/OFF	1	0	1	0	1	0	1	1	0	0	D	Enable and disable display and	1
													display flashing	
Display	SCROLL	1	0	1	0	1	0	0	0	1	0	0	Set display start address and	10
control													display regions	
	CSRFORM	1	0	1	0	1	0	1	1	1	0	1	Set cursor type	2
	CGRAM ADR	1	0	1	0	1	0	1	1	1	0	0	Set start address of character	2
													generator RAM	
	CSRDIR	1	0	1	0	1	0	0	1	1	CD	CD	Set direction of cursor	0
													movement	
	HDOT SCR	1	0	1	0	1	0	1	1	0	1	0	Set horizontal scroll position	1
	OVLAY	1	0	1	0	1	0	1	1	0	1	1	Set display overlay format	1
Drawing	CSRW	1	0	1	0	1	0	0	0	1	1	0	Set cursor address	2
control	CSRR	0	1	1	0	1	0	0	0	1	1	1	Read cursor address	2
Memory	MWRITE	1	0	1	0	1	0	0	0	0	1	0	Write to display memory	-
control	MREAD	0	1	1	0	1	0	0	0	0	1	1	Read from display memory	-

#### Notes

- 1. In general, the internal registers of the SED1335F/1336F are modified as each command parameter is input. However, the microprocessor does not have to set all the parameters of a command and may send a new command before all parameters have been input. The internal registers for the parameters that have been input will have been changed but the remaining parameter registers are unchanged.
  - 2-byte parameters (where two bytes are treated as 1 data item) are handled as follows:
  - a.CSRW,CSRR:Each byte is processed individually. The microprocessor may read or write just the low byte of the cursor address.
  - b.SYSTEM SET,SCROLL,CGRAM ADR:Both parameter bytes are processed together.If the command is changed after half of the parameters has been input, the single byte is ignored.
- 2. APL and APH are 2-byte parameters, but are treated as two 1-byte parameters.

# 7. Optical Characteristics

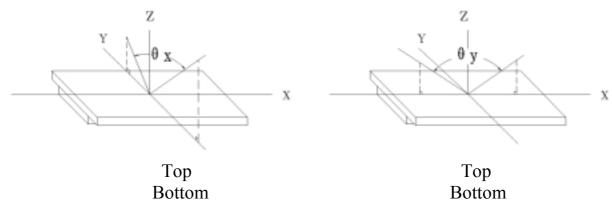
7.1 Optical Characteristics

Ta=25°C

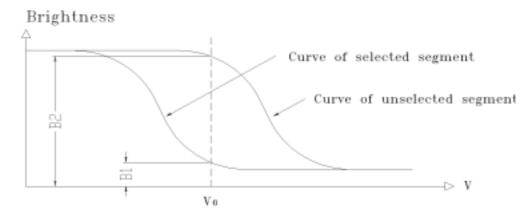
7.1 Spirear					Iu			
Item		Symbol	Cone	dition	Min.	Тур.	Max.	Unit
Viewing Angle		$\theta_{x}$	Cr≥2	θ <sub>y</sub> =0°	-30		20	Dog
		θу	Cr <u>~</u> 2	θ <sub>x</sub> =0°	-30	)	30	Deg
Contrast 1	Ratio	Cr	$\theta_{x}=0^{\circ}$ $\theta_{y}=0^{\circ}$		3.0	1	-	
Response	Turn on	Ton	θ <sub>x</sub> =0°		-	-	350	Ms
Time	Turn off	Toff	$\theta_{y}$ =	=0°	-	-	350	IVIS

## 7.2 Definition of Optical Characteristics

## 7.2.1 Definition of Viewing Angle



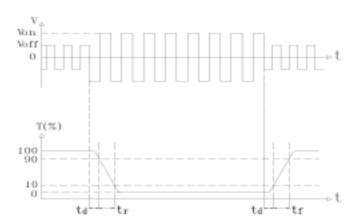
#### 7.2.2 Definition of Contrast Ratio



Contrast Ratio = 
$$B2/B1 = \frac{\text{unselected state brightness}}{\text{selected state brightness}}$$

Measuring Conditions:

1) Ambient Temperature: 25°C; 2) Frame frequency: 64Hz 7.2.3 Definition of Response time



Turn on time:  $t_{on} = t_d + t_r$  Turn off time:  $t_{off} = t_d + t_f$ 

Measuring Condition:

1) Operating Voltage: 21.5V; 2) Frame frequency: 64Hz

# 8. Reliability

8.1 Content of Reliability Test

Ta=25°C

	Tollient of Rendomity		Tu 25 0		
No.	Test Item	Content of Test	Test condition		
1	High Temperature	Endurance test applying the high	80°C 240H		
	Storage	storage temperature for a long time	Restore 4H at 25°C		
2	Low Temperature	Endurance test applying the low	-30°C 240H		
	Storage	storage temperature for a long time	Restore 4H at 25°C		
		Endurance test applying the			
3	High Temperature	electric stress (voltage & current)	<b>70</b> ℃		
	Operation	and the thermal stress to the	240H		
		element for a long time	21011		
4	Low Temperature	Endurance test applying the	-20°C		
4	Operation	electric stress under low temperature for a long time	240H		
			60°C 95%RH		
5	High Temperature	Endurance test applying the high temperature and high humidity	240H		
	/Humidity Storage	storage for a long time	Restore 4H at 25°C		
		Endurance test applying the low	Restore III at 25 C		
		and high temperature cycle	-30°C/80°C		
6	Temperature	-30°C ← 25°C ← 80°C ← 25°C			
	Cycle	30min 5min 30min 5min	10 cycles		
		1 cycle	Restore 4H at 25°C		
			10Hz~500Hz,		
7	Vibration Test	Endurance test applying the	$100 \text{m/s}^2$ ,		
	(package state)	vibration during transportation	120min		
	Shock Test	Endurance test applying the shock	Half- sine wave,		
8	(package state)	during transportation	$300 \text{m/s}^2$ ,		
	(Pachage state)	5 1	18ms		
	Atmospheric	Endurance test applying the	25kPa 16H		
9	Pressure Test	atmospheric pressure during transportation by air	Restore 2H		
		uansportation by all			

# 8.2 Failure Judgment Criterion

Criterion			To	est l	Iten	n N	0.			Failure Judgement Criterion
Item	1	2	3	4	5	6	7	8	9	randre Judgement Criterion
Basic Specification	1	1	1	1	1	1	1			Out of the basic Specification
Electrical Specification	<b>V</b>	<b>V</b>	1	1	1					Out of the electrical specification
Mechanical Specification							<b>V</b>	<b>√</b>		Out of the mechanical specification
Optical Characteristic	1	1	1	1	1	<b>V</b>			<b>√</b>	Out of the optical specification
Note	Fo	For test item refer to 8.1								
Remark		Basic specification = Optical specification + Mechanical specification								

## 9. QUALITY LEVEL

Examination	At T <sub>a</sub> =25°C	Inspection					
or Test	(unless otherwise stated)	Min.	Max.	Unit	IL	AQL	
External Visual Inspection	Under normal illumination and eyesight condition, the distance between eyes and LCD is 25cm.	See Ap	opendix A	Ą	II	Major 1.0 Minor 2.5	
Display Defects	Under normal illumination and eyesight condition, display on inspection.	See Ap	opendix l	3	II	Major 1.0 Minor 2.5	

Note: Major defects: Open segment or common, Short, Serious damages, Leakage

Miner defects: Others

Sampling standard conforms to GB2828

#### 10. Precautions for Use of LCD Modules

- 10.1 Handling Precautions
- 10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
  - Isopropyl alcohol
  - Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- 10.1.6 Do not attempt to disassemble the LCD Module.
- 10.1.7 If the logic circuit power is off, do not apply the input signals.
- 10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  - a. Be sure to ground the body when handling the LCD Modules.
  - b. Tools required for assembly, such as soldering irons, must be properly ground.
  - c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
  - d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

- 10.2 Storage precautions
- 10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 10.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature:  $0^{\circ}\text{C} \sim 40^{\circ}\text{C}$ 

Relatively humidity: ≤80%

- 10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.
- 10.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

# Appendix A

# Inspection items and criteria for appearance defects

Items	Contents	Criteria	Criteria		
Leakage		Not permitted	<u> </u>		
Rainbow		According to	the lir	mit specimen	
	Wrong polarizer attachment	Not permitted			
D. I.	Bubble between	Not counted		Max. 3 defects al	lowed
Polarizer	polarizer and glass	ф<0.3mm		0.3mm≤¢≤0.5r	nm
	Scratches of polarizer	According to the limit specimen			
Black spot		Not counted	Max. 3 spots allowed		
(in viewing area)		X<0.2mm	0.2mm≤X≤0.5mm		Max. 3
	α	X=(a+b)/2	spots (lines)		
Black line (in viewing		Not counted	Max	. 3 lines allowed	allowed
area)	b	a<0.02mm	0.021	mm≤a≤0.05mm b≤2.0mm	
Progressive cracks		Not permitted	1		

Appendix A

Inspection item and criteria for appearance defects (continued)

Items	Contents	Criteria					
Glass Cracks	Cracks on pads	a	b		С	Max. 2	
		≤3mm	≪W	V/5	€T/2	Cracks	
	b-9/4	≤2mm	≪W	V/5	T/2 <c<t< td=""><td>allowed</td></c<t<>	allowed	
	Cracks on contact side	a			b		
		≤3mm		≤T/2			
		≤2m	m	7	T/2 <b<t< td=""><td></td><td>Moy 5</td></b<t<>		Moy 5
		C shall be not reach the seal area			Max. 2 cracks	Max. 5 cracks allowed	
	Cracks on non-contact side	a		b		allowed	
	O SW -	≤3mm			≤T/2		
		≤2mm		T/2 <b<t< td=""><td></td></b<t<>			
		C≤0.5mm					
		d≤SW/3					
	Corner cracks	e<2.0mm <sup>2</sup>			Max. 3 cracks allowed		
	f-v	f<2.0mm <sup>2</sup>					

**Appendix B**Inspection items and criteria for display defects

Items		Contents	Criteria			
Open segment or open common			Not permitted			
Short			Not permitted			
Wrong viewing angle			Not permitted			
Contrast radio uneven			According to the limit specimen			
Crosstalk			According to the limit specimen			
		100	Not counted	Max.3 dots allowed		
		<b>₽</b>	X<0.1mm	0.1mm≤X≤0.2mm		
Pin holes		X=(a+b)/2	Max.3 dots			
and cracks in segment	_=i i=_ ∏	Not counted	Max.2 dots allowed			
(DOT)		A<0.1mm	0.1mm≤A≤0.2mm D<0.25mm			
Black spot			Not counted	Max.3 spots allowed		
(in viewing area)		X<0.1mm	0.1mm≤X≤0.2mm			
		X=(a+b)/2	Max.3 spots (lines)			
Black line	b b	Not counted	Max.3 lines allowed	allowed		
(in viewing area)		a<0.02mm	0.02mm≤a≤0.05mm b≤0.5mm			

Appendix B

Inspection items and criteria for display defects (continued)

Items	Content	Criteria				
		Not counted	Max. 2 defects allowed			
		x<0.1mm	0.1mm≤x≤0.2mm			
		x=(a+b)/2				
				Max.3		
	D-711-a	Not counted	Max. 1 defects allowed	defects		
Transfor- mation of segment		a<0.1mm	0.1mm≤a≤0.2mm D>0			
		Max.2 defects allowed 0.8W≤a≤1.2W				
		a=measured value of width W=nominal value of width				