



PNP SILICON PLANAR EPITAXIAL TRANSISTORS



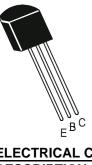
MPS2907 MPS2907A

TO-92 Plastic Package

General Purpose Transistors ABSOLUTE MAXIMUM RATINGS(Ta=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	MPS2907	MPS2907A	UNITS
Collector Emitter Voltage	V _{CEO}	40	60	V
Collector Base Voltage	V _{CBO}	60) 75	V
Emitter Base Voltage	V _{EBO}	5		V
Collector Current Continuous	I _C	60	0	mA
Power Dissipation @ Ta=25°C	P _D	62	5	mW
Derate Above 25°C		5		mW/ºC
Power Dissipation @ Tc=25°C	PD	1.:	W	
Derate Above 25°C		12	2	mW/ºC
Operating And Storage Junction	T _j , T _{stg}	-55 to	+150	°C
Temperature Range				
THERMAL RESISTANCE				
Junction to ambient	R _{th(j-a)}	20	0	°C/W
Junction to case	R _{th(j-c)}	83	.3	°C/W

PNP SILICON PLANAR EPITAXIAL TRANSISTORS



TO-92 Plastic Package

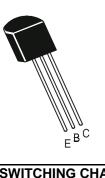
ELECTRICAL CHARACTERISTICS	(Ta=25ºC Unless Specified Otherwise)
DESCRIPTION	

DESCRIPTION	SYMBOL TEST CONDITION		MPS2907		MPS2907A		UNITS	
			MIN	MAX	MIN	MAX		
Collector Emitter Voltage	BV_{CEO} *	I _C =10mA,I _B =0	40		60		V	
Collector Base Voltage	BV_{CBO}	$I_C=10\mu A, I_E=0$	60		60		V	
Emitter-Base Voltage	BV_{EBO}	$I_{E}=10\mu A, I_{C}=0$	5		5		V	
Collector-Cut off Current	I _{CBO}							
		V_{CB} =50V, I_{E} = 0		20		10	nA	
		V_{CB} =50V, I_{E} = 0,		20		10	μA	
		$T_{A} = 150^{0}C$						
Collector-Cut off Current	I _{CEX}	V_{CE} =30V, V_{EB} (off)=0.5V		50		50	nA	
Collector-Cut off Current	I _{CEO}	V _{CE} =10V		10		10	nA	
Emitter Cut off Current	I _{EBO}	$V_{EB}=3V$, $I_{C}=0$		10		10	nA	
Base Cut off Current	I _{BEX}	V_{CE} =30V, V_{EB} (off)=0.5V		50		50	nA	
Collector-Emitter Saturation Voltage	$V_{CE(sat)}^{*}$	I _C =150mA,I _B =15mA		0.4		0.4	V	
		I _C =500mA,I _B =50mA		1.6		1.6	V	
Base-Emitter Saturation Voltage	V _{BE(sat)} *	I _C =150mA,I _B =15mA		1.3		1.3	V	
		I _C =500mA,I _B =50mA		2.6		2.6	V	
DC Current Gain							μΑ	
	h _{FE}	V _{CE} =10V,I _C =0.1mA	35		75			
		V_{CE} =10V,I _C =1mA	50		100			
		V_{CE} =10V,I _C =10mA	75		100			
		V_{CE} =10V*,I _C =150mA	100	300	100	300		
		V_{CE} =10V*,I _C =500mA	30		50			

DYNAMIC CHARACTERISTICS	SYMBOL TEST CONDITION		MPS2907		MPS2907A		UNITS	
			MIN	MAX	MIN	MAX		
Transition Frequency	$f_{T}^{*(1)}$	I _C =50mA, V _{CE} =20V f=100MHz	200		200		MHz	
Output Capacitance	C_{ob}	I _E =0, V _{CB} =10V f=1MHz		8		8	₽F	
Input Capacitance	Ci _b	lc=0, V _{EB} =2V f=1MHz		30		30	₽F	

PNP SILICON PLANAR EPITAXIAL TRANSISTORS

MPS2907 MPS2907A



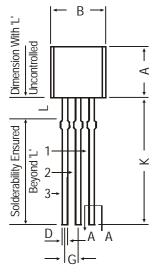
TO-92 Plastic Package

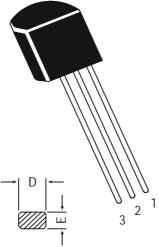
SWITCHING CHARCTERISTICS	SYMBOL	SYMBOL TEST CONDITION		MPS2907		MPS2907A	
		I _C =150mA,I _{B1} =15mA,	MIN	MAX	MIN	MAX	
		$V_{CC} = 30V$					
Delay Time	t _d			10		10	ns
Rise Time	t _r			40		40	ns
Turn On Time	t _{on}			45		45	ns
		$I_{C} = 150 \text{mA}, I_{B1} = I_{B2} \ 15 \text{m}$	A,				
		V _{CC} =6V					
Storage Time	t _s			80		80	ns
Fall Time	t _f			30		30	ns
Turn Off Time	t _{off}			100		100	ns

 $^{(1)}f_{T}$ is defined as the frequency at which $lh_{fe}l$ extrapolates to unity.

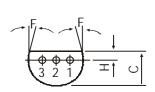
TO-92 Plastic Package

TO-92 Plastic Package





SEC AA



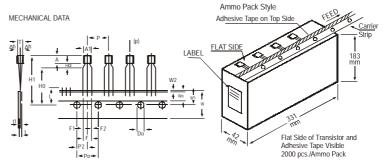
PIN CONFIGURATION

- COLLECTOR 1.
- BASE 2.
- 3. EMITTER

DIM	MIN.	MAX.
А	4.32	5.33
В	4.45	5.20
С	3.18	4.19
D	0.41	0.55
Е	0.35	0.50
F	5 D	EG
G	1.14	1.40
Н	1.14	1.53
К	12.70	_
L	1.982	2.082

All diminsions in mm.

I



TO-92 Transistors on Tape and Ammo Pack

All dimensions in mm unless specified otherwise

ITEM			SPECIF	ICATIO	N	DELLA DIVO
TTEIM	SYMBOL	MIN.	MIN. NOM.		TOL .	REMARKS
BODY WIDTH	A1	4.0		4.8		
BODY HEIGHT	A	4.8		5.2 4.2		
BODY THICKNESS PITCH OF COMPONENT	T P	3.9	12.7	4.2	±1	
FFFD HOLF PITCH	Po		12.7		+0.3	CUMULATIVE PITCH
	10				10.0	ERROR 1.0 mm/20
FEED HOLE CENTRE TO						PITCH
COMPONENT CENTRE	P2		6.35		±0.4	TO BE MEASURED AT
						BOTTOM OF CLINCH
DISTANCE BETWEEN OUTER	F		5.08		+0.6	
COMPONENT ALIGNMENT	∆h		0.00	1	-0.2	AT TOP OF BODY
TAPE WIDTH	Ŵ		18		±0.5	
HOLD-DOWN TAPE WIDTH	Wo		6		±0.2	
HOLE POSITION	W1		9		+0.7	
			0.5			
HOLD-DOWN TAPE POSITION	W2 Ho		0.5 16		±0.2 +0.5	
COMPONENT HEIGHT	HU H1		10	23.25	±0.5	
LENGTH OF SNIPPED LEADS	L			11.0		
FEED HOLE DIAMETER	Do		4		±0.2	
TOTAL TAPE THICKNESS	t		2.54	1.2		t1 0.3 - 0.6
LEAD - TO - LEAD DISTANCEF1,	F2		2.54		+0.4	
CLINCH HEIGHT	H2			3	0.1	
PULL - OUT FORCE	(P)	6N				

NOTES
1. MAXIMUM ALIGNMENT DEVIATION BETWEEN LEADS NOT TO BE GREATER THAN 0.2 mm.
2. MAXIMUM NON-CUMULATIVE VARIATION BETWEEN TAPE FEED HOLES SHALL NOT EXCEED 1 mm IN 20 PITCHES.
3. HOLDDOWN TAPE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NO

EXPOSURE OF ADHESIVE. NO MORE THAN 3 CONSECUTIVE MISSING COMPONENTS ARE PERMITTED. A TAPE TRAILER, HAVING AT LEAST THREE FEED HOLES ARE REQUIRED AFTER THE LAST COMPONENT. SPLICES SHALL NOT INTERFERE WITH THE SPROCKET FEED HOLES.

4.

5. 6.

Packing Detail

PACKAGE	STANDARD PACK		PACK INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-92 Bulk	1K/polybag	200 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	23 kgs
TO-92 T&A	2K/ammo box	645 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	12.5 kgs

Notes

MPS2907 MPS2907A

TO-92 Plastic Package

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



CDIL is a registered Trademark of Continental Device India Limited C-120 Naraina Industrial Area, New Delhi 110 028, India. Telephone + 91-11-579 6150 Fax + 91-11-579 9569, 579 5290 e-mail sales@cdil.com www.cdil.com

Data Sheet