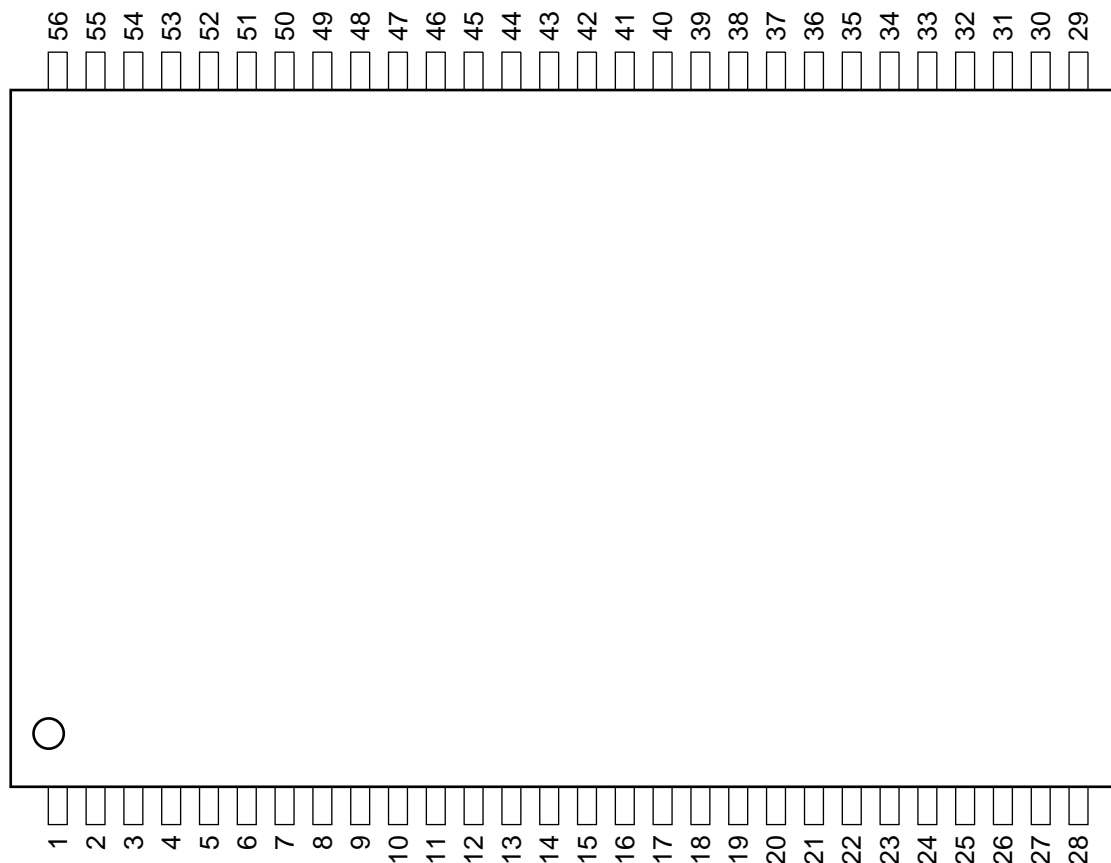


32LINE MEMORY FOR TBC

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	O	BD20	15	I	CLR	29	I	WD23	43	I	MA27
2	O	BD21	16	I	RMAI	30	I	WD22	44	I	MA26
3	O	BD22	17	—	GND	31	I	WD21	45	I	MA25
4	O	BD23	18	O	RD24	32	I	WD20	46	I	MA24
5	O	RD23	19	O	RD25	33	I/O	DOBY1	47	I	MA23
6	O	RD22	20	O	RD26	34	I/O	DOBY2	48	I	MA22
7	O	RD21	21	O	RD27	35	—	GND	49	I	MA21
8	O	RD20	22	O	BD27	36	I	MOD1	50	I	MA20
9	I	WCK	23	O	BD26	37	I	MOD2	51	—	Vcc
10	I	NWCK	24	O	BD25	38	I	MA212	52	I	WE
11	I	DO	25	O	BD24	39	I	MA211	53	I	WD24
12	I	RSUB	26	I/O	DOB	40	I	MA210	54	I	WD25
13	I	RCK	27	I/O	DORY	41	I	MA29	55	I	WD26
14	I	NRCK	28	—	Vcc	42	I	MA28	56	I	WD27

INPUTS

CLR	: CHROMA READ ZERO
DO	: DROPOUT WRITE CLOCK
MA20 - MA29, MA210 - MA212	: MEMORY ADDRESS
MOD1	: SYSTEM SELECT (H : NTSC, L : PAL)
MOD2	: FORMAT SELECT (H : U-MATIC, L : BETACAM)
NRCK	: 1/8 RCK
NWCK	: 1/8 WCK
RCK	: READ CLOCK
RMAI	: CHROMA READ CLOCK
RSUB	: DROPOUT READ CLOCK
WCK	: WRITE CLOCK
WD20 - WD27	: SERIAL DATA

OUTPUTS

BD20 - BD27	: B-Y DATA
RD20 - RD27	: R-Y DATA

INPUTS/OUTPUTS

DOB	: DROPOUT DETECT
DOBY1, DOBY2	: B-Y DROPOUT DETECT
DORY	: R-Y DROPOUT DETECT

INPUTS				OUTPUTS										
MOD 1	MOD 2	CLR	DO	RD20	RD21	RD22	RD23	RD24	RD25	RD26	RD27	DOB	DORY	DOBY
				BD20	BD21	BD22	BD23	BD24	BD25	BD26	BD27			
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
0	0	1	0	1	1	1	1	1	1	1	1	0	0	0
0	0	1	1	1	1	1	1	1	1	1	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
1	1	1	0	1	1	1	1	1	1	1	1	1	1	1
1	1	0	1	1	1	1	1	1	1	1	1	1	1	1
1	1	0	0	1	1	1	1	1	1	1	1	1	1	1

0 : LOW LEVEL

1 : HIGH LEVEL