

HT2050 Five LAMP/LED Flash Driver

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Features

- C-MOS Metal-Gate Process
- Operating voltage: 1.2V~4.5V
- Low stand-by current: 1µA at 3V
- A five lamp flash driver
- · Random or sequence flash

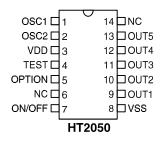
- On/Off toggle control
- 1/10 duty cycle output
- A built-in oscillator
- Minimum external components

General Description

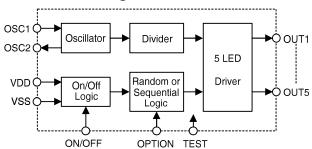
The HT2050 is a low cost, low-power CMOS fabricated LSI chip designed for lamp and LED flash drivers. It contains five flash outputs. Each flash output is with a 10mA capability that can implement random or sequential flash-

ing controlled by a single option pin. The chip requires only one external resistor for normal applications. It is very suitable for use in products that require flashing lights, such as gift cards, Christmas decoration, etc.

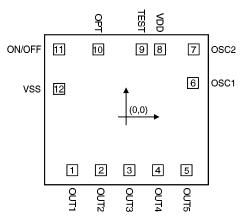
Pin Assignment



Block Diagram



Pad Coordinates



Unit: mil

| Pad No. X | | Y | Pad No. | X | Y | |
|-----------|--------|-------|---------|--------|-------|--|
| 1 | -21.09 | -20.9 | 7 | 26.55 | 26.5 | |
| 2 | -9.89 | -20.9 | 8 | 13.35 | 26.5 | |
| 3 | 1.31 | -20.9 | 9 | 6.15 | 26.5 | |
| 4 | 12.51 | -20.9 | 10 | -10.89 | 26.5 | |
| 5 | 23.71 | -20.9 | 11 | -26.25 | 26.5 | |
| 6 | 26.23 | 13.3 | 12 | -26.25 | 10.98 | |

Chip size: $64 \times 59 \text{ (mil)}^2$

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^{*} The IC substrate should be connected to VDD in the PCB layout artwork.



Pad Description

| Pad No. | Pad Name | I/O | Description | | |
|----------|----------|-----|---------------------------------------|--|--|
| 1 | OUT1 | О | Lamp/LED flash drive output | | |
| 2 | OUT2 | О | Lamp/LED flash drive output | | |
| 3 | OUT3 | О | Lamp/LED flash drive output | | |
| 4 | OUT4 | О | Lamp/LED flash drive output | | |
| 5 | OUT5 | О | Lamp/LED flash drive output | | |
| 6 | OSC1 | I | Oscillator input | | |
| 7 | OSC2 | О | Oscillator output | | |
| 8 | VDD | I | Positive power supply | | |
| 9 | TEST | I/O | For IC test only | | |
| 10 | OPTION | I | Random or sequence function selection | | |
| 11 | ON/OFF | I | Toggle ON/OFF control | | |
| 12 | VSS | I | Negative power supply, GND | | |

Note: OPTION=VDD \rightarrow Sequential Mode OPTION=Open \rightarrow Random Mode

Absolute Maximum Ratings

Supply Voltage -0.3V to 5V Storage Temperature $-50^{\circ}C$ to $125^{\circ}C$ Input/Output Voltage ... V_{SS} -0.3V to V_{DD} +0.3V Operating Temperature $0^{\circ}C$ to $70^{\circ}C$

Electrical Characteristics

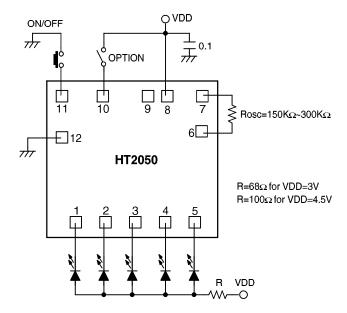
| Symbol | Parameter | Test Condition | | Min. | Tem | Max. | Unit |
|-------------------|----------------------|----------------|-----------------------|--------|------|------|------|
| | Parameter | V_{DD} | Condition | MIIII. | Тур. | Max. | Ome |
| V_{DD} | Operating Voltage | | _ | 1.2 | 3 | 4.5 | V |
| I _{STB} | Stand-by Current | 3V | | _ | 1 | 2 | μΑ |
| I_{DD} | Operating Current | 3V | No load | _ | 200 | 500 | μΑ |
| I _{OL} | Output Sink Comment | 1.5V | $V_{OL}=0.5V$ | 5 | 8 | _ | mA |
| | Output Sink Current | 3V | V _{OL} =0.5V | 10 | 15 | _ | mA |
| Fosc | Oscillator Frequency | _ | R=150K~300KΩ | _ | 64K | _ | Hz |

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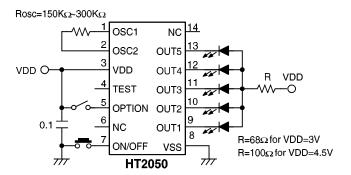


Application Circuit

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* The IC substrate should be connected to VDD in PCB layout artwork.



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