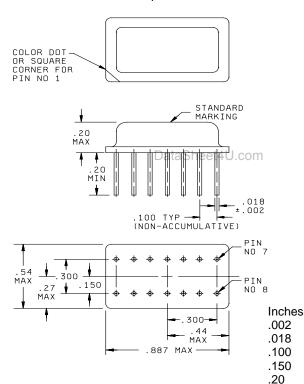
INCH-POUND MIL-PRF-55310/18D 8 July 2002 SUPERSEDING MIL-PRF-55310/18C 25 March 1998

PERFORMANCE SPECIFICATION SHEET

OSCILLATOR, CRYSTAL CONTROLLED, TYPE 1 (CRYSTAL OSCILLATOR (XO)), 0.01 Hz THROUGH 15.0 MHz, HERMETIC SEAL, SQUARE WAVE, CMOS

This specification is approved for use by all Departments and Agencies of the Department of Defense.

The requirements for acquiring the product described herein shall consist of this specification and MIL-PRF-55310.



Pin number	Function
1	NC
2	NC
3	NC
4	NC
5	NC
6	NC
7	B - (GND/CASE)
8	OUTPUT
9	NC
10	NC
11	NC
12	NC
13	NC
14	B+
	nches mm 27 6.9

.300

.44

.54

.887

7.62

11.2

13.7

22.53

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NOTES:

- 1. Dimensions are in inches.
- 2. Metric equivalents are given for general information only.
- 3. Unless otherwise specified, tolerances are ±.005 (0.13 mm) for three place decimals and ±.02 (0.5 mm) for two place decimals.
- 4. All pins with NC function may be connected internally and are not to be used as external tie points or connections.

FIGURE 1. Dimensions and configuration.

AMSC N/A 1 of 5 <u>DISTRIBUTION STATEMENT A</u>. Approved for public release; distribution is unlimited. FSC 5955

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0.46

2.54

3.81

5.1

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	REQUIREMENTS:							
	Interface and physical dimensions: See figure 1.							
	Mounting: See figure 1.							
	Terminals: See figure 1.							
	Seal: Hermetic in accordance with MIL-PRF-55310, maximum leakage rate 5 x 10^{-8} atm cc/s.							
	Weight: 0.5 ounce, maximum.							
	* Oscillator: Class 2 (see 1.2.3 of MIL-PRF-55310).							
	Calibration: Manufacturer calibrated.							
	* Screening: In accordance with MIL-PRF-55310, product level B or S, as applicable.							
	Temperature:							
	Operating: See table I.							
	Storage: -62°C to +125°C.							
et4U.com	Oscillator load: 10 kilohms \pm 5 percent shunted by 15 pF \pm 5 percent capacitor for CMOS compatible square.							
	Output waveform: Symmetrical square wave.							
	Output voltage: At designated CMOS load. DataSheet4U.com							
	Logic 1: See table I.							
	Logic 0: See table I.							
	Rise and fall times: See table I.							
	Duty cycle: 40 to 60 percent duty cycle.							
	Supply voltage: See table I.							
	Input current: At designated supply voltage (see table I).							
	Output frequency: Frequency as designated at time of acquisition (see table I).							
	Initial accuracy at reference temperature (up to 30 days after shipment): See table I.							
	Initial frequency-temperature accuracy (one-half temperature cycle): Verification applicable. $1/$							
	Frequency-temperature tolerance (one-half temperature cycle, referenced to frequency measured at +23°C \pm 1°C, immediately prior to starting of the test): See table I. Measurements taken at ten equally spaced increments over the specified operating temperature range. <u>1</u> /							
	<u>1</u> / For the purpose of transitioning this device to MIL-PRF-55310, 'Frequency stability versus temperature' has been renamed 'Frequency-temperature tolerance'. The verification requirements of 'initial frequency-temperature accuracy (one-half temperature cycle)' shall apply except that frequency measurements shall be referenced to the frequency measured at +23°C ±1°C (fref) instead of to the nominal frequency (fnom).							

							characteristi				
Dash number	Output frequency range	Supply voltage ±10%	Maximum Input current at maximum	Output voltage		Rise and fall times (max)	Initial accuracy ppm at +23°C	Frequency aging ppm/year after	Frequency-temperature tolerance (ppm)		
			supply	Logic: 1	Logic: 0		±1°C	30 days	-55°C	-55°C	-20°C
			voltage	(min)	(max)				to	to	to
			<u>1</u> /						+125°C	+105°C	+70°0
									A	В	С
01	0.01 Hz to	<u>V dc</u> +15	<u>mA</u> 25	<u>V dc</u> 12.8	<u>V dc</u> 1.5	<u>ns</u> 30	±15	±5	±50	±40	±25
02	15.0 MHz	+15	25	12.8	1.5	30	±25	±10	±100	±80	±50
11	0.01 Hz	+12	20	10.2	1.2	35	±15	±5	±50	±40	±25
12	to 12.0 MHz	+12	20	10.2	1.2 at	35	±25	±10	±100	±80	±50
21	0.01 Hz	+10	15	8.5	1.0 💁	40	±15	±5	±50	±40	±25
22	to 10.0 MHz	+10	15	8.5	1.0 tz	40	±25	±10	±100	±80	±50
31	0.01 Hz	+8	10	6.8	0.8 🧮	50	±15	±5	±50	±40	±25
32	to 8.0 MHz	+8	10	6.8	0.8 0.8	50	±25	±10	±100	±80	±50
41	0.01 Hz	+5	3	4.2	0.5	70	±15	±5	±50	±40	±25
42	to 5.0 MHz	+5	3	4.2	0.5	70	±25	±10	±100	±80	±50

TABLE I. Dash numbers and operating characteristics.

1/ Maximum input current for no load condition. Actual configuration of CMOS loads must be added to determine power supply requirements.

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Frequency-voltage tolerance: ± 2 ppm maximum for a ± 10 percent change in supply voltage. Measurements taken at reference temperature and operating temperature range end points.

Frequency aging: Measurements shall be taken at +70°C \pm 0.2°C at intervals of not more than every 72 hours for 30 days minimum (see table I).

<u>±5 ppm per year, maximum</u>	<u>±10 ppm per year, maximum</u>
± 0.7 ppm per 30 days. ± 1.5 ppm per 90 days	\pm 1.5 ppm per 30 days \pm 3 ppm per 90 days

Terminal strength: Method 211 of MIL-STD-202, test condition C.

Applied force: 2 pounds each terminal for 10 seconds.

Bends: Five at 45 degrees each.

Frequency-environmental tolerance: Not applicable.

Vibration, sinusoidal: In accordance with MIL-PRF-55310 and method 204 of MIL-STD-202.

Nonoperating: Test condition D.

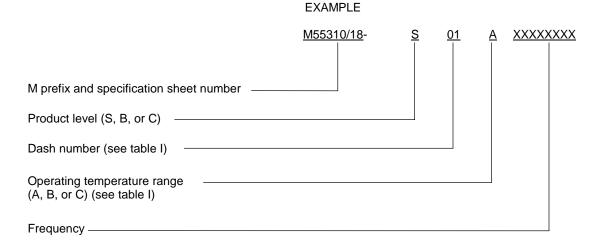
Operating: Not required.

et4U.com Ambient pressure:

Nonoperating: In accordance with MIL-PRF-55310. DataSheet4U.com

Operating: Method 105 of MIL-STD-202, test condition C.

Part or Identifying Number (PIN): Consists of "M" prefix followed by specification sheet number, a dash and coded alphas, and numeric number. See example:



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