

High power NPN transistor

Features

- High power dissipation
- Low collector-emitter saturation voltage

Description

The device is a planar NPN transistor mounted in TO-3 metal case. It is intended for linear amplifiers and inductive switching applications.

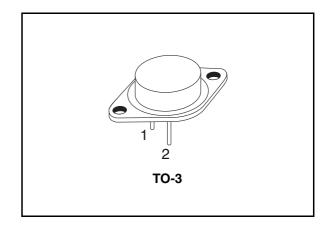


Figure 1. Internal schematic diagram

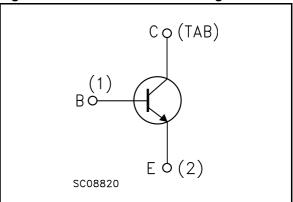


Table 1. Device summary

Order code	Marking	Package	Packaging
2N3773	2N3773	TO-3	Tray

Electrical ratings 2N3773

1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter		Unit
V _{CEO}	Collector-emitter voltage (I _B = 0)	140	V
V _{CEV}	Collector-emitter voltage (V _{BE} = -1.5 V)	160	V
V _{CBO}	Collector-base voltage (I _E = 0)	160	V
V _{EBO}	Emitter-base voltage ($I_C = 0$)	7	V
I _C	Collector current	16	Α
I _{CM}	Collector peak current (t _P < 5 ms)	30	Α
I _B	Base current	4	Α
I _{BM}	Base peak current (t _P < 1 ms)	15	Α
P _{tot}	Total dissipation at T _c ≤ 25 °C		W
T _{stg}	Storage temperature		°C
T _j	Max. operating junction temperature		°C

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R _{thj-case}	Thermal resistance junction-case Max	1.17	°C/W

2 Electrical characteristics

 $(T_{case} = 25 \, ^{\circ}C \text{ unless otherwise specified})$

Table 4. Electrical characteristics

Symbol	Parameter	Test cor	nditions	Min.	Тур.	Max.	Unit
I _{CEV}	Collector cut-off current (V _{BE} = -1.5 V)	V _{CE} = 140 V V _{CE} = 140 V	T _C = 150 °C			2 10	mA mA
I _{CEO}	Collector cut-off current (I _B = 0)	V _{CE} = 120 V				10	mA
I _{CBO}	Collector cut-off current $V_{CB} = 140 \text{ V}$					2	mA
I _{EBO}	Emitter cut-off current (I _C = 0)	V _{EB} = 7 V				5	mA
V _{CEO(sus)} (1)	Collector-emitter sustaining voltage (I _B = 0)	I _C = 0.2 A		140			>
V _{CEV(sus)} (1)	Collector-emitter sustaining voltage (V _{BE} = -1.5 V)	I _C = 0.1 A		160			V
V _{CER(sus)} (1)	Collector-emitter sustaining voltage $(R_{BE} = 100 \Omega)$	I _C = 0.2 A		150			٧
V _{CE(sat)} (1)	Collector-emitter saturation voltage	I _C = 8 A I _C = 16 A				1.4 4	V V
V _{BE} ⁽¹⁾	Base-emitter voltage	I _C = 8 A	V _{CE} = 4 V			2.2	V
h _{FE} ⁽¹⁾	DC current gain	I _C = 8 A I _C = 16 A	V _{CE} = 4 V V _{CE} = 4 V	15 5		60	
I _{s/b}	Second Breakdown Collector Current	V _{CE} = 30 V t = 1 s (non re	petitive)	5			А

^{1.} Pulsed: Pulse duration = 300 μ s, duty cycle \leq 2 %

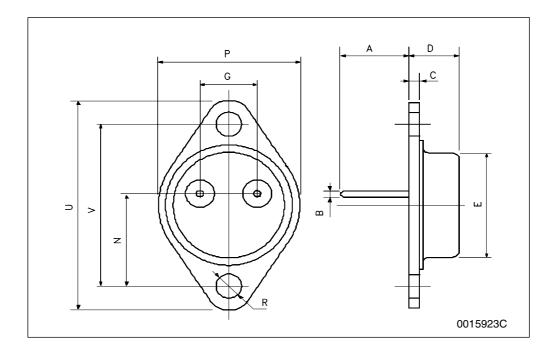
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3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

TO-3 mechanical data

DIM.	mm.				
	min.	typ	max.		
Α	11.00		13.10		
В	0.97		1.15		
С	1.50		1.65		
D	8.32		8.92		
E	19.00		20.00		
G	10.70		11.10		
N	16.50		17.20		
Р	25.00		26.00		
R	4.00		4.09		
U	38.50		39.30		
V	30.00		30.30		



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Revision history 2N3773

4 Revision history

Table 5. Document revision history

Date	Revision	Changes	
03-Apr-2006	1	Initial release.	
10-Oct-2008	2	Content reworked to improve readability, no technical changes.	

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