

SI-3000KWM Series 2-Output, Surface-Mount, Low Dropout Voltage Linear Regulator ICs

■Features

- Compact surface-mount package (TO252-5)
- Output current: 1.0A × 2
- Low dropout voltage: $V_{DIF} \leq 0.6V$ (at $I_o = 1A$)
- Built-in overcurrent and thermal protection circuits

■Applications

- Secondary stabilized power supply (local power supply)

■Absolute Maximum Ratings

Parameter	Symbol	Ratings		(T _a =25°C)
		SI-3002KWM		
DC Input Voltage	V _{IN} ^{*1}	18		V
Output Control Terminal Voltage	V _C	6		V
Output Current ^{*1}	I _{O1}	1.0		A
	I _{O2}	1.0		
Power Dissipation (with two outputs ON)	P _D ^{*2}	1		W
Junction Temperature	T _j	-30 to +125		°C
Operating Ambient Temperature	T _{op}	-30 to +85		°C
Storage Temperature	T _{stg}	-40 to +125		°C
Thermal Resistance (Junction to Ambient Air)	θ _{j-a}	95		°C/W
Thermal Resistance (Junction to Lead)	θ _{j-c}	6		°C/W

*1: V_{IN} (max), I_{O1} (max) and I_{O2} (max) are restricted by the relation $P_D = (V_{IN} - V_{O1}) \times I_{O1} + (V_{IN} - V_{O2}) \times I_{O2}$.

*2: When mounted on glass-epoxy board of $30 \times 30\text{mm}^2$ (copper laminate area 4.3%)

Thermal protection may operate when the junction temperature exceeds 135°C.

■Electrical Characteristics

Parameter	Symbol	Ratings			Unit	
		SI-3002KWM				
		min.	typ.	max.		
Output Voltage	V _{O1}	3.234	3.300	3.366	V	
	Conditions	$V_{IN}=5V, I_o=10\text{mA}$				
Output Voltage	V _{O2}	2.450	2.500	2.550	V	
	Conditions	$V_{IN}=5V, I_o=10\text{mA}$				
Line Regulation	ΔV _{LINE1}			20	mV	
	Conditions	$V_{IN}=4.5 \text{ to } 10V, I_o=10\text{mA}$				
Line Regulation	ΔV _{LINE2}			20	mV	
	Conditions	$V_{IN}=4.5 \text{ to } 10V, I_o=10\text{mA}$				
Load Regulation	ΔV _{LOAD1}			30	mV	
	Conditions	$V_{IN}=5V, I_o=0 \text{ to } 1A$				
Load Regulation	ΔV _{LOAD2}			30	mV	
	Conditions	$V_{IN}=5V, I_o=0 \text{ to } 1A$				
Dropout Voltage	V _{DIF1}			0.6	V	
	Conditions	$I_o=1A$				
Temperature Coefficient of Output Voltage	ΔV _{O1} /ΔT _a		±0.3		mV/°C	
	Conditions	$T_j=0 \text{ to } 100^\circ\text{C}$				
Temperature Coefficient of Output Voltage	ΔV _{O2} /ΔT _a		±0.3		mV/°C	
	Conditions	$T_j=0 \text{ to } 100^\circ\text{C}$				
Ripple Rejection	R _{REJ1}		60		dB	
	Conditions	$V_{IN}=5V, f=100 \text{ to } 120\text{Hz}$				
Ripple Rejection	R _{REJ2}		60		dB	
	Conditions	$V_{IN}=5V, f=100 \text{ to } 120\text{Hz}$				
Overcurrent Protection Starting Current ^{*1}	I _{S1} 1	1.2			A	
	Conditions	$V_{IN}=5V$				
Overcurrent Protection Starting Current ^{*1}	I _{S1} 2	1.2			A	
	Conditions	$V_{IN}=5V$				
Quiescent Circuit Current	I _Q		1	1.5	mA	
	Conditions	$V_{IN}=5V, I_o=0A, V_c=2V$				
Circuit Current at Output OFF	I _{Q(OFF)}			0.5	mA	
	Conditions	$V_{IN}=5V, V_c=0V$				
V _C Terminal	Control Voltage (Output ON) ^{*2}	V _C , I _H	2		V	
	Control Voltage (Output OFF)	V _C , I _L				
V _C Terminal	Control Current (Output ON)	I _C , I _H		5	μA	
	Control Current (Output OFF)	I _C , I _L	-100			
Output OFF Voltage			$V_c=0.4V$		μA	
	V _{O(OFF)}			0.5		

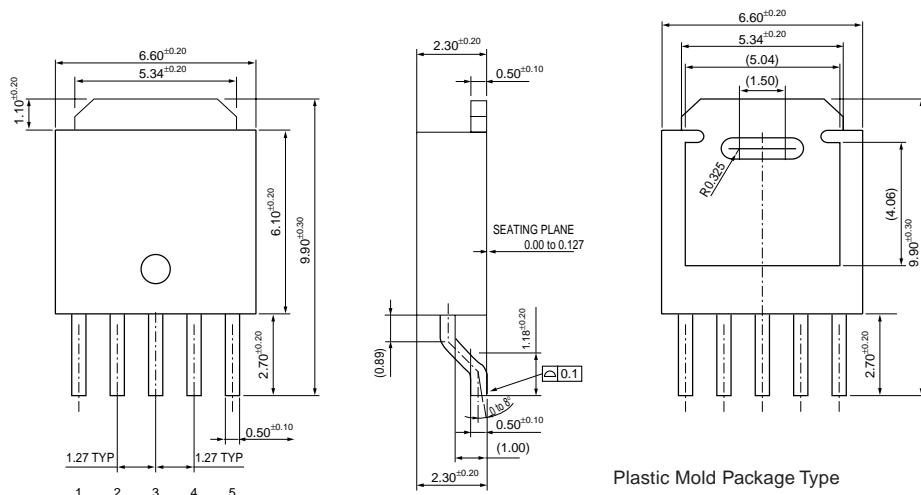
*1: I_{S1} 1 and I_{S1} 2 are specified at the 5% drop points of output voltages V_{O1} and V_{O2} on the condition that V_{IN} = the condition of protection starting current, $I_o = 10\text{ mA}$.

*2: Output is ON when the output control terminal V_C is open. Each input level is equivalent to LS-TTL. Therefore, the devices can be driven directly by LS-TTLs.

Channels 1 and 2 are turned on or off at the same time.

■External Dimensions (TO252-5)

(Unit : mm)



Pin Assignment

- ① Vc
- ② Vo1
- ③ GND
- ④ Vin
- ⑤ Vo2

■Block Diagram

