SBT3906

PNP Silicon Transistor

Descriptions

- General small signal application
- Switching application

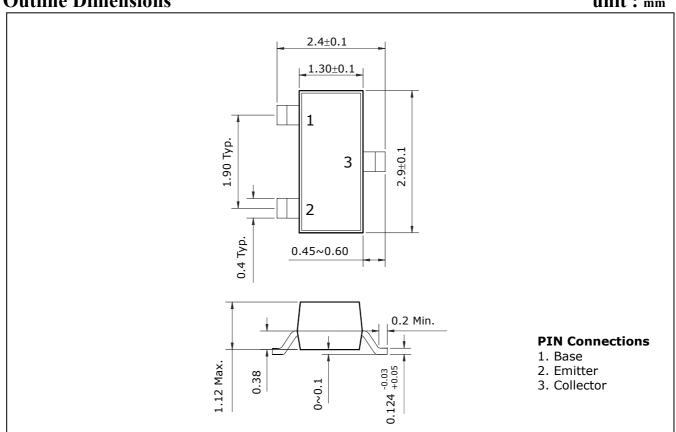
Features

- Low collector saturation voltage
- Collector output capacitance
- Complementary pair with SBT3904

Ordering Information

Type NO.	Marking	Package Code		
SBT3906	2A	SOT-23		

Outline Dimensions unit: mm



Absolute maximum ratings

Ta=25°C

Characteristic	Symbol	Ratings	Unit
Collector-Base voltage	V_{CBO}	-40	V
Collector-Emitter voltage	V_{CEO}	-40	V
Emitter-base voltage	V_{EBO}	-5	V
Collector current	I_{C}	-200	mA
Collector dissipation	P_C^*	350	mW
Junction temperature	T_{j}	150	°C
Storage temperature range	T_{stg}	-55~150	°C

^{* :} Package mounted on 99.5% alumina $10\times8\times0.6$ mm

Electrical Characteristics

Ta=25°C

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Collector-Base breakdown voltage	BV _{CBO}	I_{C} =-10 μ A, I_{E} =0	-40	-	-	V
Collector-Emitter breakdown voltage	BV _{CEO}	$I_C=-1$ mA, $I_B=0$	-40	-	-	V
Emitter-Base breakdown voltage	BV _{EBO}	$I_E = -10 \mu A, I_C = 0$	-5	-	-	V
Collector cut-off current	I_{CEX}	V _{CE} =-30V, V _{EB} =-3V	-	-	-50	nA
DC current gain	h _{FE}	V _{CE} =-1V, I _C =-10mA	100	-	300	-
Collector-Emitter saturation voltage	$V_{CE(sat)}$	I_C =-50mA, I_B =-5mA	-	-	-0.4	V
Transition frequency	f _T	V_{CE} =-20V, I_{C} =-10mA, f =100MHz	250	-	-	MHz
Collector output capacitance	C _{ob}	V_{CB} =-5V, I_E =0, f=1MHz	-	1	4.5	pF
Delay time	t _d	$V_{CC}=-3V_{dc}$, $V_{BE(off)}=-0.5V_{dc}$,	-	-	35	ns
Rise time	t _r	$I_C=-10\text{mA}_{dc}$, $I_{B1}=-1\text{mA}_{dc}$	-	-	35	ns
Storage time	t _s	$V_{CC}=-3V_{dc}$, $I_{C}=-10$ mA _{dc} ,	-	-	225	ns
Fall Time	t _f	$I_{B1}=I_{B2}=-1\text{mA}_{dc}$	-	-	75	ns

Electrical Characteristic Curves

Fig. 1 P_C-T_a

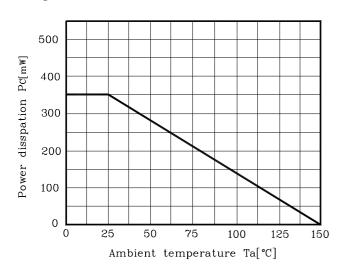


Fig. 2 h_{FE} - I_C

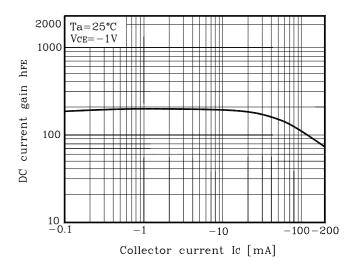
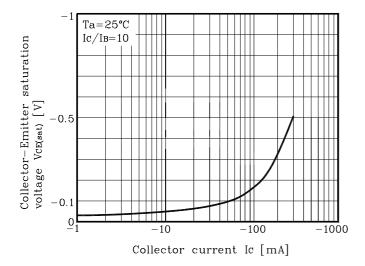


Fig. 3 $V_{CE(sat)}$ - I_C



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